ELECTRONICS AND COMMUNICATIONS ENGINEERING

SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (AUTONOMOUS)

(AFFILIATED TO ANDHRA UNIVERSITY, VISAKHAPATNAM) (RECOGNISED BY ALL INDIA COUNCIL FOR TECH. EDN., NEW DELHI)

Accredited by NAAC with 'A' Grade

Recognised as Scientific and Industrial Research Organisation

CHINNA AMIRAM (P.O):: BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN: 534 204

Dr. G. P. Saradhi Varma

B.Tech.(CSE), M.Tech.(CSE), Ph.D.(CSE).

PRINCIPAL

Estd. 1980

Phones: Off : EPABX :08816-223332 Ext.201
Direct :08816-222748
Fax:08816-224516
College Fax:08816-229377
Mobile No.98483 81818
Email: principal@srkrec.ac.in
principalsrkrec@gmail.com
gpsvarma@gmail.com
Web Site : www.srkrec.ac.in

SRKREC/BOS-2/ECE/2016-17

Date: 29.05.2017

To
Prof. G.Sasibhushana Rao
Department of Electronics and Communication Engineering
Andhra University
Visakhapatnam - 530 003.

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 08.06.2017 at 11-00 A.M in A.C. Auditorium, I-Block.

The Boards of Studies meetings will be conducted in respective departments as per the schedule mentioned below:

S.No.	BOARD	DATE	TIME
1	Civil Engineering	08-06-2017	2.00 P.M.
2	Computer Science & Engineering	08-06-2017	2.00 P.M.
3	Electronics & Communication Engineering	08-06-2017	2.00 P.M.
4	Electrical & Electronics Engineering	08-06-2017	2.00 P.M.
5	Information Technology	08-06-2017	2.00 P.M.
6	Mechanical Engineering	08-06-2017	2.00 P.M.
7	Combined Board of Studies in Engineering Chemistry/Engineering Mathematics/Engineering Physics / Humanities and Social Sciences	08-06-2017	2.00 P.M.

The T.A / D.A. will be paid to all the external members as per the university guidelines. We request you to make it convenient to attend the meetings.

Yours faithfully,

G.P.S

PRINCIPAL

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SRKREC/BOS-2/ECE/2016-17

Date: 29.05.2017

To
Dr. N.V.S.N.Sarma
Department of ECE
NIT-Warangal.

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SRKREC/BOS-2/ECE/2016-17

Date: 29.05.2017

To
Dr. B.V.Ramana Reddy
Professor & Dean
Guru Gobind Indra Prastha University
Dwarka, New Delhi

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SRKREC/BOS-2/ECE/2016-17

Date: 29.05.2017

To
Dr. A.M.Prasad
Professor – Department of ECE
JNTUK, Kakinda

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SRKREC/BOS-2/ECE/2016-17

Date: 29.05.2017

To
Sri Rama Raju
Director, Envision
Jubilee Hills, Hyderabad-500033

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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gpsvarma@gmail.com
Web Site : www.srkrec.ac.in

SRKREC/BOS-2/ECE/2016-17

Date: 29.05.2017

To
Dr. M.Chakravarty
Scientist G
DLRL, Hyderabad

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE(A)
CHINNA AMIRAM :: BHIMAVARAM-534204
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Dt: 07-06-2017

CIRCULAR

This is to inform you that the Department of ECE has convened a meeting on 08-06-2017 at 2 PM in the ECE Department Library. In this connection, all the Board of Studies members are requested to attend the same.

Agenda:

1. Finalization of scheme for third and final years of UG and syllabus for 2nd year which will come into force from 2017-2018 (second year students) batch.

Head of the Department

Head of ECE Department
S.R.K.R. Engg. College
BHIMAVARAM-534 204

C.C to:
1. The Members of Board of studies
2. Office file

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

10

BoS Meeting for ECE
on 08-06-2017

08-06-2017.

The Board of Studies meeting for UG Courses is conducted on 08-06-2017 at 2PM in ECE department.

Venue: at Dept Library   Time: 2PM
Date: 08-06-2017.

Agenda: Finalization of scheme for 2nd and final years of UG, and syllabus for 2nd year which will come into force for 2017-2018 (Second year) batch students.

Members present   Signature

1. Dr M. Chakraborty, SCG, DLRL, Hyd — M. Chakraborty
2. Dr B.V.Ramana Reddy, Prof in ECE, IP University, New Delhi — [signature] 08/06/17
3. Dr A. Mallikarjuna Prasad, Prof in ECE, JNTUK — A.M. Prasad 08/06/17
4. Sri N.V.Rama Raju, Envision — [signature]
5. Dr P.V. Rama Raju — [signature]
6. Dr K.V.S.N. Raju — [signature]
7. Prof N.V. Rao — [signature]
8. Prof DVR Mohan — [signature]
9. Prof P. Subba Rao — [signature]
10. Dr N. Udaya Kumar — [signature]
11. Dr B.V.S.N. Raju — [signature]
12. Prof G.V.S. Padma Rao — GVPRao
13. Dr M. Vijaya Rama Raju — [signature]
14. B.V.S.N. Raju — [signature]
15. G.V.P.S. ManiKantah (UG student) — G. ManiKantah
16. I.V.S. Subrahmanyam (UG student) — I.V.S. Subrahmanyam

M. Jagapathi Raju
PRINCIPAL
S.R.K.R. Engg. College
BHIMAVARAM-534 204.

08-06-2017.

Resolutions:

The Scheme for 3/4, 4/4 of UG Course is finalised. The scheme and Syllabus for 3/4 of UG Course is also finalised. It will come into force for second year students from the academic year 2017-2018. for Andhra University affiliated batches.

Recommendations: 08-06-2017

1) It has been decided to incorporate the latest trends in the last chapter in each course by considering feedback.
2) It has been decided to follow the guidelines of NSQF (National Skill Qualification Frame work)
3) It has been decided to confine the no of prescribed text books to two and the no of reference books to two or three to the maximum extent possible and also indicate the specific chapter or topic from the specified book.
4) It has been decided to the pleas of examinations to give strict instructions to paper setters to set the papers giving equal weightage to all the units in the syllabus
5) It has been decided to offer open electives in future
6) It has been decided to include simulation experiments in all labs.

M. Jagapathi Raju

PRINCIPAL
S.R.K.R. Engg. College
BHIMAVARAM-534 204.

10

BOS Meeting for ECE
on 08-06-2017

08-06-2017.

The Board of Studies meeting for UG Courses is conducted on 08-06-2017 at 2 PM in ECE department.

Venue : At Dept- Library Time : 2 PM
Date : 08-06-2017.

Agenda : Finalization of scheme for ~~II~~ III and IV years of UG. as per syllabus for 2nd years which will come into force for 2017-2018 (Second year) batch. Students.

Members present-

(1)	Dr M. Chakravarthy, SG, DLRL, Hyd	Signature M. Chakravarthy
(2)	Dr BV Ramana Reddy, Prof in ECE, IP University, New Delhi	08/06/17
(3)	Dr A. Kalli Khagma Prasad, Prof in ECE, JNTUK	A. Prasad
(4)	Sri N.V Rama Raju, Assistant	
(5)	Dr P V Rama Raju	
(6)	Dr KVSN Raju	
(7)	Prof N. V. Rao	
(8)	Prof DVR Murthy	
(9)	Prof P. Subba Rao	P. Subba Rao
(10)	Dr N. Udaya Kumar	
(11)	Dr BVSSN Raju	
(12)	Prof G V S Satya Rao	Satya Rao
(13)	Sri M. Vijaya Kumar Raju.	
(14)	BVRSN Rajesh	
(15)	G.V.P.S. Manikantah (UG Student)	G. Manikantah
(16)	V.V.S. Subrahmanyam (UG student)	V.S. Subrahmanyam

P. Nagaraju

PRINCIPAL
S.R.K.R. Engg. College
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08-06-2017.

Resolutions:

The Scheme for 3/4, 4/4 of UG Course is finalised. The scheme and Syllabus for 3/4 of UG Course is also finalised. This will come into force for second year students from the academic year 2017-2018. for Andhra university affiliated batches.

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4) It has been decided to the deans of examinations to give strict instructions to paper setters to set the papers giving equal weightage to all the units in the Syllabus
5) It has been decided to offer open electives in future
6) It has been decided to include Simulation experiments in all labs.

K. Jagapathi Raju

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As per the Resolutions dated 08-06-2017

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ESTD: 1980

SCHEME OF INSTRUCTION & EXAMINATION
(Regulation R16)

II/IV B.TECH
(With effect from **2016-2017** Admitted Batch onwards)
Under Choice Based Credit System

ELECTRONICS AND COMMUNICATION ENGINEERING

I-SEMESTER

Code No.	Course	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Total Contact Hrs/Week	Sessional Marks	Exam Marks	Total Marks
B16 ENG 2101	Mathematics - IV	4	3	1	-	4	30	70	100
B16 EE 2104	Circuit Analysis & Synthesis	4	3	1	-	4	30	70	100
B16 EE 2105	Electrical Technology	4	3	1	-	4	30	70	100
B16 EC 2101	Analog Electronic Circuits	4	3	1	-	4	30	70	100
B16 CS 2104	Elementary Data Structures	4	3	1	-	4	30	70	100
B16 EC 2102	Probability Theory & Random Processes	4	3	1	-	4	30	70	100
B16 EE 2107	Network And Machines Lab	2	-	-	3	3	50	50	100
B16 EC 2105	Electronic Devices & Circuits Lab	2	-	-	3	3	50	50	100
B16 ENG 2104	English Proficiency	2	1	1	--	2	50	50	100
B16 ENG 2106	Industry Oriented Training	1	--	--	2	2	50	--	50
	Total	**31**	**19**	**7**	**8**	**34**	**380**	**570**	**950**

II/IV B.Tech-I Sem
ECE

Code: B16 ENG 2101

MATHEMATICS – IV
(Common to CIV,ECE,EEE & ME)

Theory	**: 3 Periods**	**Sessionals**	**30**
Tutorial	**: 1 Period**	**Ext. Marks**	**70**
Exam	**: 3 Hrs.**	**Credits**	**4**

M. Jagapathi Raju

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Course Objectives:

Students learn

1. The concepts of Gradient, Divergence, Curl, Directional derivative, solenoidal and Irrotational fields
2. Green"s, Stokes" and Divergence theorems
3. Classification of 2nd order Partial Differential Equations as well as solution of 1-Dimensional Wave equation and 1-Dimensional Heat equation
4. the concept of Analytic function, CR equations
5. Cauchy"s Integral Theorem and Integral Formula
6. Taylor and Laurent series, Residues and Residue theorem

Course Outcomes:

Students will be able to

1. Apply the concepts of Gradient, Divergence, Curl, Directional derivative, solenoidal and Irrotational fields
2. Determine scalar potential, circulation and work done
3. Evaluate integrals using Green"s, Stokes" and Divergence theorems
4. Obtain the solution of 1-D wave equation and 1-D heat equation
5. Determine the zeroes and poles of functions and residues at poles
6. Evaluate certain real definite integrals that arise in applications by the use of Residue theorem

SYLLABUS

Vector Calculus-1

Definitions of Scalar and Vector point functions, Differentiation of vectors, Vector differential operator del, Del applied to scalar point function – gradient, Del applied to vector point function- divergence and curl, physical interpretation of gradient, divergence and curl(without proof), Del applied twice to a point function, Del applied to product of two functions, Irrotational and Solenoidal Fields, scalar potential

Vector Calculus-2

Integration of vectors, line integral, circulation, work done, surface integral, Flux, Green"s, Stokes" and Gauss Divergence Theorems (Without proofs). Introduction to orthogonal curvilinear coordinates, cylindrical polar coordinates and spherical polar coordinates.

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Applications Of Partial Differential Equations

Classification of second order partial differential equations, Method of separation of variables, One –dimensional wave equation- vibrations of a stretched string (no derivation)-, one-dimensional heat equation – Heat flow along a long horizontal bar (no derivation) (problems on heat equation involving homogeneous end conditions only), two dimensional Laplace equation in Cartesian coordinates.

Complex Variables-1

Review- Cartesian form and polar form of a complex variable, Real and imaginary parts of z^n , e^z, sin z, sinh z and log z .

Limit and continuity of a function of the complex variable, derivative, analytic function, properties of Analytic functions, Cauchy- Riemann equations, Harmonic functions and Orthogonal system, application of analytic function to flow problems, geometric representation of w=f(z), conformal mapping – Bilinear transformation only.

Complex Variables-2

Integration of complex functions, Cauchy"s theorem, Cauchy"s integral formula (statements only) . Taylor and Laurent series expansions of functions (statement of theorems only), zeros and singularities, Residue, calculation of residues, Cauchy"s Residue theorem (without proof), Evaluation of real and definite integrals- integration around a unit circle

Text Book:

1. "Higher Engineering Mathematics", by Dr.B.S.Grewal, 43rd Edition, Khanna Publishers.

Reference Books:

1. Advanced Engineering Mathematics, by Erwin Kreyszig, Wiley.
2. A text book of Engineering Mathematics, by N.P.Bali and Dr. Manish Goyal, Lakshmi Publications.
3. Advanced Engineering Mathematics, by H.K.Dass, S.Chand Company.
4. Higher Engineering Mathematics, by B.V.Ramana, Tata Mc Graw Hill Company.
5. Higher Engineering Mathematics, by Dr. M.K.Venkatraman, The National Publishing Company.

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Code: B16 EE 2104

CIRCUIT ANALYSIS & SYNTHESIS

Theory	**: 3 Periods**	**Sessionals**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 4**

Course Objectives

1. To learn the concept of network theory and definitions of circuit elements for medelingpractical electric circuits
2. To learn various theorems and techniques in electric circuit analysis and to know their significance and applications
3. To learn phasor concept and apply it to analysis of circuits in sinusoidal steady state.
4. To learn the concept of two-port network analysis and find models using different parameter sets.
5. To learn the concept of Laplace transform and its application to circuit analysis.

Course Outcomes

1. Students will learn circuit conventions and analyze DC circuits using various techniques like mesh analysis, nodal analysis and theorems.
2. Students will learn the significance of energy storing elements (Inductance & Capacitance) in circuits and analysetransient and steady state responses.
3. Students will learn the concepts of single and three-phase balanced circuits and analyze sinusoidal steady-state using phasor concept.
4. Student will learn the concept of network functions and analyze poles, zeros and time domain behavior from pole-zero plots.
5. Student will learn the concept of positive real functions and test whether the given network function is Hurwitz and positive real or not.

SYLLABUS

Analysis of DC Circuits:

Active elements, Passive elements, Reference directions for current and voltage, Kirchoffs Laws, Voltage and Current Division Nodal Analysis, Mesh analysis, Linearity and superposition, Thevinin"s theorem and Norton"s theorem, Reciprocity theorem, Z,Y,H,S-parameters.

DC transients:
Inductor, Capacitor, source free RL, RC and RLC response, Evaluation of Initial conditions, Application of unit-step function to RL, RC and RLC circuits, concepts of Natural, Forced and Complete response.

Sinusoidal Steady State Analysis:
The sinusoidal forcing function, Phasor Concept, Average and Effective value of Voltage and Current, Instantaneous and Average Power, Complex Power, Steady State Analysis using mesh and node analysis, Application of network theorems to AC circuits, resonance, Concept

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Network Functions:
Network functions for single port and two port, Calculation of Network functions for Ladder and General Networks, Poles and Zeroes, Restriction of Poles and Zeroes for Driving point and Transfer functions, Time Domain Behavior from Pole Zero plot, Transfer Functions in terms of Y and Z functions, Scaling Network Functions.

Positive Real Functions:
Positive real function and other properties, Herwitz polynomials, Computation of residues, even and Odd functions, Test for Positive Real Functions.

TextBooks:

1. Engineering Circuit Analysis, William H.Hayt Jr. and Jack E. Kemmerley, 5th Edition, McGraw Hill International Edition.
2. Modern Network Synthesis, M. E. Van Valkenburg, Wiley Eastern.

Reference Books:

1. Introduction to modern Network Synthesis : Van Valkenburg; John Wiley
2. Network Analysis: Van Valkenburg; PHI
3. Basic circuit theory:DasoerKuh; McGraw Hill.
4. A Course in Electrical Circuit Analysis by Soni& Gupta; DhanpatRai Publication.
5. Circuit Analysis : G.K. Mithal; Khanna Publication.
6. Networks and Systems :D.RoyChoudhury; New Age International.
7. Network Analysis, M. E. Van Valkenburg, 3rd Edition, PHI.
8. Network Analysis &Synthesis :UmeshSinha; SatyaPrakash Pub.
9. Network Analysis &Synthesis :F.F.Kuo; John Wiley & Sons Inc.

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Code: B16 EE 2105

ELECTRICAL TECHNOLOGY

Theory	**: 3 Periods**	**Sessionals**	**30**
Tutorial	**: 1 Period**	**Ext. Marks**	**70**
Exam	**: 3 Hrs.**	**Credits**	**4**

Course Objectives

1. To learn the basic principles of magnetic field theory.
2. To know the Construction and working of D.C Machines and A.C Machines.
3. To learn the characteristics of D.C Machines and A.C Machines.
4. To learn different tests of DC machines and A.C Machines to know its performance.

Course Outcomes

1. Classify the parts of DC Machines, Transformers, Three Phase Induction motors & Three Phase Synchronous machines.(k2)
2. Interpret the operation and working principle of DC Machines, Transformers, Three Phase Induction motors , Three Phase Synchronous machines.(k2)
3. Develop performance characteristics of various machines.(k3)
4. Construct experiments on various machines.(k3)
5. Analyze the application of electrical machines in various fields of engineering.(k4)

SYLLABUS

Magnetic Circuits:

Definitions of magnetic circuit, Reluctance, MMF, Magnetic flux, Hysteresis loss. Faraday"s laws of Electromagnetic induction, Induced E.M.F., Dynamically induced E.M.F., Statically induced E.M.F., Self-inductance, Mutual inductance, Lenz"s law

DC Machines:

Principle of operation DC Generator - EMF equation - types - DC motor types - torque equation – speed control methods- applications - three point starter-Testing-Load test on D.C Shunt Motor, D.C Series Motor,Swinburne"s test.

Transformers:

Principle of operation of single phase transformers - EMF equation - equivalent circuit –losses - efficiency and regulation-Testing- Open circuit and Short circuit tests.

Induction Motors:

Construction - Principle of operation of induction motor - slip - torque characteristics - Power flow diagram.

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Synchronous Machines :

Construction-Principle of operation of alternators – EMF equation of alternator- regulation by synchronous impedance method, Principle of operation of synchronous motors, methods of starting, applications.

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Text Books:

1. Electrical Machinery by Dr.P.S Bimbhra,Khanna publications.
2. Electrical machines by J.B Gupta, Published by S K Kataria and Sons.
3. A Textbook of Electrical Technology By R.K.Rajput,Laxmi Publications.
4. Principles of Electrical Engineering, V.K mehta, Rohit Mehta, S. Chand Publications.
5. A Textbook Of Electrical Technology B. L. THERAJA (Volume II)

Reference Books:

1. Basic Electrical Engineering, V.K mehta, Rohit Mehta, S. Chand Publications.
2. Basic Electrical Engineering, S.N. Singh, PHI.

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Code: B16 EC 2101

ANALOG ELECTRONIC CIRCUITS

Theory	**: 3 Periods**	**Sessionals**	**30**
Tutorial	**: 1 Period**	**Ext. Marks**	**70**
Exam	**: 3 Hrs.**	**Credits**	**4**

Course Objectives

The aim of this course is to

1. Understand the concepts of different type of amplifiers and analyze them.
2. Learn the classification of feedback amplifiers and analyze them.
3. Compare the voltage and power amplifiers and analyze them.
4. Understand the principle of oscillator and analyze different types of sinusoidal oscillators.
5. Learn the classification of tuned amplifiers and analyze them.
6. Understand the concept and analyze applications of op-amp.

Course Outcomes

After the completion of the course, students will be able to:

1. Know the equivalent circuit of multistage amplifier and its analysis. [K3]
2. Identify the different feedback topologies and analyze them. [K1]
3. Explain the principle of oscillator and design different types of sinusoidal oscillators.[K3]
4. Define the difference between voltage and power amplifiers and design different
5. classes. [K1, K3]
6. Know that Tuned amplifiers amplify a narrow band of frequencies and will also be able to analyze them.[K2, K3]
7. Identify that Op-amp not amplifies but also perform different operations and analyze some applications.[K1,K2]

SYLLABUS

Multistage Amplifiers

Transistor at high frequencies, CE short circuit current gain and concept of GainBandwidth Product. BJT and FET RC Coupled Amplifiers at low and high frequencies. Frequency Response and calculation of Band Width of Multistage Amplifiers.

Feed Back Amplifiers

Concept of Feed Back Amplifiers - Effect of Negative Feedback on the amplifier characteristics. Four feedback topologies, Method of analysis of Voltage Series, Current Series, Voltage Shunt and Current Shunt feedback Amplifiers.

Sinusoidal Oscillators

Condition for oscillations –LC Oscillators – Hartley, Colpitts, Clapp and TunedCollector Oscillators – Frequency and amplitude Stability of Oscillators – CrystalOscillators – RC Oscillators -- RC Phase Shift and Weinbridge Oscillators.

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Power Amplifiers

Classification of Power Amplifiers – Class A, Class B and Class AB power Amplifiers. Series Fed, Single Ended Transformer Coupled and Push Pull Class A and Class B Power Amplifiers. Cross-over Distortion in Pure Class B Power Amplifier, Class AB Power Amplifier – Complementary Push Pull Amplifier with trickle Bias, Derating Factor – Heat Sinks.

Tuned Voltage Amplifiers

Single Tuned and Stagger Tuned Amplifiers – Analysis – Double Tuned Amplifier – Bandwidth Calculation.

Operational Amplifiers

Concept of Direct Coupled Amplifiers. Ideal Characteristics of an operational Amplifier – Differential Amplifier - Calculation of common mode Rejection ratio – Differential Amplifier supplied with a constant current – Normalized TransferCharacteristics of a differential Amplifier – Applications of OP-Amp as an Inverting and Non-Inverting Amplifier,Integrator, Differentiator Summing and Subtracting Amplifier and Logarithmic Amplifier. Parameters of an Op-Amp, Measurement of OP-Amp Parameters.

Text Books:

1. Millman's Integrated Electronics by Jacob Millman, Christos Halkias, Chetan Parikh.
2. Electronic Devices and Circuits by Salivahanan

References:

1. Electronic Devices and Circuits by G.S.N.Raju.
2. Electronic devices and circuits-Mottershead
3. Op-amps and Linear Integrated Circuits – Gayakwad

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Code: B16 CS 2104

ELEMENTARY DATA STRUCTURES

Theory	**: 3 Periods**	**Sessionals**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 4**

Course Objectives:

The aim of this course is to

1. Student will learn about data structures and the algorithms for manipulating them, and how to analyse the time and memory requirements of them.
2. Student will master some complex searching and sorting algorithms and their data structures, advanced types of trees,and graph representations and graph algorithms.
3. Student will learn when and how to use techniques for developing algorithms.
4. Student will also become skilled in algorithmic analysis and algorithm development using the latest techniques.

Course Outcomes:

After the completion of the course, students will be able to:

1. Be able to write programs and class libraries given a specification;
2. Implement various data structures.
3. Implement and analyse various sorting algorithms.
4. Understand abstract data types and how they are implemented in C.

SYLLABUS

Revision of C language:overview

Arrays and Functions:

Organization and use of one Dimensional , Two dimensional and Multi dimensional Arrays, Handling of character strings, string operations, Concept of function, Parameter passing , Recursion.

Structures, Pointers and Files:

Definition of structure and Union, Programming examples, Pointers , Pointer Expressions, Programming examples, Dynamic Memory Allocation, Preprocessor Directives

Linear Data structures:

Stack -Representation, Operations, Queue- representation, Operations, Circular Queue, Linked List- Representation, Operations, Double Linked List and Circular List.

Non-linear Data Structures:

Trees, Binary Tree Representation, Tree Traversals, Conversion of a General Tree to Binary Tree.

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Graphs

Representation of Graphs, Linked Representation of Graphs, Graph Traversals and Spanning Trees.

Searching & Sorting:

Basic search Techniques- Linear and Binary searching, Tree searching, Sorting-Insertion, Selection, Bubble, Quick and Merge Sorting.

Text Books:

1. Programming With C, by Schaum"s Outlines, Byron Gottifried.
2. Fundamentals of Data Structures in C, by Sahni- Horowitz, Second Edition.

Reference Books:

1. An Introduction To Data Structures With Applications, Trembly and Sorenson.
2. The C-Programming Language, Kerningham and others.

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Code: B16 EC 2102

PROBABILITY THEORY & RANDOM PROCESSES

Theory	**: 3 Periods**	**Sessionals**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 4**

Course Objectives:

1. To introduce the fundamental concepts and theorems of probability theory.
2. To introduce fundamental concepts of random variables and their statistical descriptions.
3. To discuss about various types of random processes, their properties,spectral representation and applications.
4. To understand the difference between time averages and statistical averages, Stationarity and Ergodicity.
5. To apply elements of stochastic processes for problems in real life.
6. To understand the concept of Noise as applicable to linear Systems.

Course Outcomes:

On completion of the course the student will be able to

1. Understand the axiomatic formulation of modern probability theory.
2. Characterize Probability Models and functions of Random variables based on single and multiple random variables.
3. Evaluate and apply moments and characteristic functions and understand the concept of Inequalities and probabilistic limits.
4. Understand the concept of Random process and determine covariance and spectral density of stationary random processes.
5. Demonstrate the specific applications to Poisson and Gaussian process and representation of low pass and band pass noise models.
6. Analyze the response of random inputs to linear time invariant systems.

SYLLABUS

Probability Theory

Definitions of Probability, Axioms of Probability, Probability Spaces, Properties of Probabilities, Joint and Conditional Probabilities, Independent Events

Random Variables

Probability Distribution Functions, Probability Density Functions, Joint Distribution of Two Variables, Conditional Probability Distribution and Density, Independent Random Variables.

Statistical Averages

Functions of Random Variables and Random Vectors, Statistical Averages, Characteristic Function of Random Variables, Inequalities of Chebyshev and Schwartz, Convergence Concepts, Central Limit Theorem.

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Random Processes
Stationarity, Ergodicity, Covariance Function and their Properties, Spectral Representation, Weiner-Kinchine Theorem.

Linear Systems and Random Noise Processes
Linear operations, Gaussian processes, Poisson Processes, Low-pass and Band-pass Noise Representation.

Text Books:

1. Probability Theory and Random Processes, S. P. Eugene Xavier, S. Chand and Co. New Delhi, 1998 (2nd Edition).
2. Probability Theory and Random Signal Principles, Peebles, Tata McGrew Hill Publishers.

Reference Books:

1. Signal Analysis, Papoulis, McGraw Hill N. Y., 1977.
2. Introduction to Random Signals and Noise, Davenport W. B. Jrs. and W. I.Root, McGraw Hill N.Y., 1954.
3. B.P. Lathi, "Signals, Systems & Communications", B.S. Publications, 2003.
4. Probability, Random Variables & Random Signal Principles, STARK et al, Pearson, 2002

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Code: B16 EE 2107

NETWORKS AND MACHINES LAB

Lab	**: 3 Periods**	**Sessionals**	**50**
Exam	**: 3 Hrs.**	**Ext. Marks**	**50**
		Credits	**2**

Course Objectives:

1. To learn to make simple electric circuits by using different sources, loads and components and verify basic laws.
2. To experimentally verify various theorems of circuit analysis.
3. To learn to find circuit models for two-terminal devices and two-port networks.
4. Conducting experiments on characteristics of generators & motors
5. Load tests on series,shunt,compound motors and compound generators-swinburne's, Hopkinson's test.
6. OC & SC tests on single phase transformers, Sumpner's test.

Course Outcomes

1. Students will gain the skill to make and experiment with practical electric circuits.
2. Students will be able to measure voltage, current, power in practical electric circuits.
3. Students will know the significance of various theorems and their applications.
4. Students will be able to model devices for circuit analysis.
5. Students will be able to assess the behavior of different electrical machines.
6. Students will be able to predetermine the efficiency and regulation of different machines.

LIST OF EXPERIMENTS

1. Maximum Power Transfer Theorem
2. Superposition Theorem
3. Thevinin"s Theorem
4. Series Resonance
5. Ohm"s Law And Characteristics Of Filament Lamp
6. Parameters Of Iron Cored Inductor
7. Swinburne"s Test
8. Load Test on Dc Shunt Motor
9. Load Test on Dc Series Motor
10. Load Test on 3 Phase Slip ring Induction Motor
11. OC and SC Test on Single Phase Transformer
12. Voltage Regulation of An Alternator by Synchronous Impedance Method
13. Speed Control of Dc Shunt Motor

Reference: Lab Manual

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Code: B16 EC 2105

ELECTRONIC DEVICES & CIRCUITS LAB
(Common to ECE & EEE)

Lab	**: 3 Periods**	**Sessionals**	**50**
Exam	**: 3 Hrs.**	**Ext. Marks**	**50**
		Credits	**2**

Course Objectives:

1. To familiarize the student with test and measuring equipment like CROs, Multimeters, Ammeters, Voltmeters etc. and also to prepare the student to use signal generators, bread boards and to make the student identify the terminals of basic electronic devices like diodes, transistors and JFETs.
2. To familiarize the student with features of Multisim and to prepare the student to construct and simulate various electronic circuits using Multisim.
3. To make the student study experimentally the characteristics of basic electronic devices like ordinary pn diodes, LEDs, Zener diodes, BJTS, JFETs and rectifiers with and without filters.
4. To make the student to conduct experiments to analyze various parameters of BJT amplifiers and FET amplifiers.

Course Outcomes:

After the successful completion of the lab course, the students will be able

1. To understand the role of basic electronic devices like ordinary Pn diodes, Zener diodes, LEDs, BJTS and JFETs in achieving various functionalities like rectification, voltage regulation, amplification, switching action etc. in various electronic circuits.
2. To construct and simulate different electronic circuits using Multisim.
3. To have the hardware skills and software skills required in the design of electronic systems for various applications.

LIST OF EXPERIMENTS

1. V-I characteristics of semiconductor diode, LED and Zener diode.
2. Half wave and full wave rectifier with and without filters.
3. Input and output characteristics of transistor in CE configuration.
4. Transistor biasing circuits and transistor as a switch.
5. CE amplifier.
6. JFET common source amplifier.

LIST OF SIMULATION EXPERIMENTS

7. V-I characteristics of semiconductor diode, LED and Zener diode.
8. Regulation characteristics of Zener diode.
9. Input and output characteristics of transistor in CB configuration
10. JFET Characteristics.
11. CC amplifier
12. JFET common source amplifier

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Code: B16 ENG 2104

ENGLISH PROFICIENCY
(Common to All Branches)

Theory	**: 1 Period**	**Sessionals**	**50**
Tutorial	**: 1 Period**	**Ext. Marks**	**50**
Exam	**: 3 Hrs.**	**Credits**	**2**

AIM:

Enriching the communicative competency of the students by adopting the activity-based as well as the class-oriented instruction with a view to facilitate and enable them to enhance their language proficiency skills.

Course Objectives:

Students be able to
1. Understand the importance of professional communication.
2. Learn language skills and vocabulary in order to improve their language competency.
3. Know and perform well in real life contexts.
4. Identify and examine their self-attributes which require improvementand motivation.
5. Build their confidence and overcome their inhibitions.
6. Improve their strategies in reading skills.

Course Outcomes:

1. Students enhance their vocabulary and use it in the relevant contexts .
2. They improve speaking skills.
3. They learn and practice the skills of composition writing.
4. They enhance their reading and understanding of different texts.
5. They enrich their communication both in formal and informalcontexts.
6. They strengthen their confidence in presentation skills.

SYLLABUS

Speaking Skills

PPT
Describing event/place/thing
Picture Description
Extempore
Debate
Telephonic Skills
Analyzing Proverbs

Vocabulary
Affixes
Pairs of Words

Reading Skills
Reading Comprehension
Reading/Summarizing News Paper Artic

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Writing Skills
Designing Posters
Essay writing
Resume Writing

Reference Books:

1. Interchange (4th edition) Student"s books 1&2 by Jack C. Richards, CUP.
2. Fundamentals of Technical Communication by Meenakshiraman, Sangeta Sharma of OUP
3. English and Communication Skills for Students of Science and Engineering, by S.P.
4. Dhanavel, Orient Blackswan Ltd. 2009
5. Enriching Speaking and Writing Skills, Orient Blackswan Publishers
6. The Oxford Guide to Writing and Speaking by John Seely OUP

(***Note: Sessional Marks will be evaluated based on Continuous Comprehensive Evaluation of the students" Performance - 40M, Attendance – 10M and External Marks will be evaluated based on Presentation Skills – 30M, Project 20M)

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Code: B16 ENG 2106

INDUSTRY ORIENTED TRAINING
(Common to ECE & EEE)

Lab : 2 Periods	**Sessionals : 50**
Exam : 3 Hrs.	**Credits : 1**

Course Objectives:

1. Be familiar with basic Data structures.
2. Master the implementation of linear data structures.
3. Master the implementation of non linear data structures.
4. Be familiar with Object Oriented Concepts.

Course Outcomes:

1. Application using implementation of Data structures.
2. Application using implementation of Linear and non linear Data structures in view of industry.
3. Applications using Object Oriented Concepts in view of industry.

Syllabus: Industry Oriented Applications on following topics.

BASIC CONCEPTS

System Life Cycle, Algorithm Specification, Recursive Algorithms, Data Abstraction, Performance Analysis, Space Complexity, Time Complexity, Asymptotic Notation, Comparing Time Complexities

IMPLEMENTATION (Using C)

Arrays
Stacks
Queues
Linked List
Double linked lists
Trees
Graphs

Applications of linear and nonlinear data structures and solving simple to complex problems in perspective of industry requirements.

Basic Concepts of OOP
Procedural Paradigms, Object Oriented Paradigm, OOP Principles and Terminology, OOP benefits, Procedure and Object Oriented programming languages, advantages and disadvantages, creating class, defining objects in C++ and JAVA.

Applications using OOP in solving simple to complex problems in perspective of industry requirements.

(Note: Total Marks will be evaluated based on Continuous Evaluation - 25 Marks, Coding

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Contest- 25 Marks)

SCHEME OF INSTRUCTION & EXAMINATION
(Regulation R16)

II/IV B.TECH
(With effect from **2016-2017** Admitted Batch onwards)
Under Choice Based Credit System

ELECTRONICS AND COMMUNICATION ENGINEERING

II-SEMESTER

Code No.	Course	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Total Contact Hrs/Week	Sessional Marks	Exam Marks	Total Marks
B16 EC 2201	Switching Theory and Logic Design	4	3	1	--	4	30	70	100
B16 EC 2202	Electromagnetic Field theory & Transmission Lines	4	3	1	--	4	30	70	100
B16 EC 2203	Pulse and Digital Circuits	4	3	1	--	4	30	70	100
B16 EC 2204	Analog Communications	4	3	1	--	4	30	70	100
B16 EC 2205	Signals & Systems	4	3	1	--	4	30	70	100
B16 ENG 2201	Environmental Studies	2	3	1	--	4	30	70	100
B16 EC 2207	Analog Communications Lab	2	--	--	3	3	50	50	100
B16 EC 2208	Analog Electronic Circuits Lab with Simulation	2	--	--	3	3	50	50	100
B16 EC 2209	Industry oriented Technology Lab	1	--	--	2	2	50	--	50
B16 ENG 2204	Industry Oriented Training	1	--	--	2	2	50	--	50
	Total	**28**	**18**	**6**	**10**	**34**	**380**	**520**	**900**

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Code: B16 EC 2201

SWITCHING THEORY AND LOGIC DESIGN

Theory	**: 3 Periods**	**Sessionals**	**30**
Tutorial	**: 1 Period**	**Ext. Marks**	**70**
Exam	**: 3 Hrs.**	**Credits**	**4**

Course Objectives

1. To provide insight of number systems and minimization of Boolean functions
2. To introduce the design of various combinational and sequential circuits
3. To introduce the design of Finite State Machines and Asynchronous Machines

Course Outcomes

By the end of the course the learners (students) will be able to

1. Able to understand various basic number system conversion and simplification of Boolean expressions
2. Design and analyze combinational and sequential circuits using logic gates, latches and flip-flops.
3. Analyze and design Finite State Machines
4. Analyze and design Asynchronous Machines

SYLLABUS

Number Systems And Codes

Number Systems, Base Conversion Methods, Complements of Numbers, Codes, Error detecting and Error Correcting Codes.

Logic Gates And Minimization Of Boolean Functions

Symbols and Truth Tables of Gates – AND, OR, NOT, NAND, NOR and XOR. Proof of Boolean theorems and functions– Karnaugh Map (up to 6 variables) and QuineMcClusky methods.

Combinational Logic Circuits And Design

Logic Design of Combinational circuits – Binary addition, Subtraction, Multiplexers, Demutiplexers, Decoders, Encoders, Code Conversion, Priority Encoders, Seven – segment Displays, Comparators and PLDs.

Sequential Logic Circuits And Design

The Flip-flops – SR, RS, JK, MSJK, T and D-Flip-flops. Design of Clocked Flip-flops, Flip-Flop conversion from one type to another.Design of Shift Registers and Counters.

Traditional Approaches to Sequential Analysis and Design

Analysis and Design of Finite State Machines, State Reduction.

Asynchronous Finite State Machines

Analysis and Design of Asynchronous Machines, Cycles, Races and Hazards.

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Text Books:

1. Switching and Finite Automata Theory, 2nd Edition, ZviKohavi, Tata McGraw-Hill, 1978.
2. Fundamentals of Digital Circuits-A.Anand Kumar-2nd Edition-PHI Learning Private Limited.

Reference Books:

1. Introduction to Switching Theory and Logical Design, 3rd Edition, Frederick J. Hill and Gerald R. Peterson, John Wiley and Sons, 1981.
2. An Engineering Approach to Digital Design, William I. Fletcher, PHI.

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Code: B16 EC 2202

ELECTRO MAGNETIC FIELD THEORY & TRANSMISSION LINES

Theory	**: 3 Periods**	**Sessionals**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 4**

Course Objectives:

1. To introduce the concepts of static electric field , steady magnetic field and time varying electromagnetic fields in real time applications.
2. To introduce Maxwell"s equations and their applications in practical situations.
3. To introduce the fundamental theory of electromagnetic wave propagation in bounded and unbounded media.
4. To study the propagation of energy in practical transmission lines and wave guides.

Course Outcomes:

After the completion of the course, Students should have the

1. Ability to apply the knowledge of mathematics, Science and engineering to the analysisand design of systems involving electric and magnetic fields as well as electromagnetic Waves.
2. Ability to identify, formulate and solve engineering problems in the area of electric and Magnetic fieldsand waves.
3. Ability to use Maxwell"s equations to solve electromagnetic field problems.
4. Ability to apply the knowledge of electromagnetic fields in practical transmission linesand waveguides.

SYLLABUS

Electrostatics:

Introduction, Coulomb"s law and electric field intensity, electric field due to different types of charge distributions, electric flux density, gauss"s law and applications. Energy and potential, electric field in terms of potential gradient, electric dipole, stored energy in static electric field and energy density, convection and conduction currents, continuity equation, conductors in electric field, relaxation time, dielectrics in electric field, Laplace"s and Poisson"s equations, uniqueness theorem, different capacitance configurations, Boundary conditions on $\bar{E}$&$\bar{D}$ at the interface between two media, Related Problems.

Magneto statics:

Introduction, Biot-savart"s law, Ampere"s circuital law, applications of Ampere"s circuital law, magnetic flux density, Gauss"s law for magnetic fields, scalar and vector magnetic potentials, forces due to magnetic fields, magnetization in materials, inductance, boundary conditions on $\bar{H}$&$\bar{B}$ at the interface between two media, energy stored in steady magnetic field, Related problems.

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Time varying fields and Maxwell's equations:
Introduction, Faraday"s law of electromagnetic induction, Transformer emf and motional emf, Maxwell"s equations in integral and differential forms, word statements, Maxwell"s equations using phasor notation, Boundary conditions an $\bar{E}, \bar{D}, \bar{H} \& \bar{B}$ at the interface between two media, Related problems.

Electromagnetic Waves:
Introduction, Wave equations for free space and for a conductive medium, uniform plane waves, properties of uniform plane waves, Relation between E and H in uniform plane wave, wave propagation in lossless and lossy media, Propagation in good conductors and good dielectrics, depth of penetration, polarization, Reflection of plane waves by a perfect conductor for normal and Oblique incidences, Reflection of plane waves by a perfect dielectric for normal and Oblique incidences, Brewster angle and critical angle, Poynting"s theorem, Related Problems.

Transmission lines:
Introduction, types of transmission lines, equivalent circuit of transmission line, Primary and secondary constants of the line, Transmission line equations, characteristic impedance and expression for characteristic impedance, Reflection coefficient, standing wave ratio, lossless line, distortion less line, input impedance of transmission line, shorted and open circuited lines, impedance transformation with $\frac{\lambda}{8}, \frac{\lambda}{4} and \frac{\lambda}{2}$ lines, Construction of smith chart, applications of smith chart, Single stub matching, Related problems.

Rectangular Waveguides:
Introduction, TM modes in rectangular waveguides, TE modes in rectangular waveguides, Impossibility of TEM mode in waveguides, Characteristics of TE and TM modes, cutoff frequency, cutoff wavelength, phase and group velocities, characteristic wave impedance, dominant mode, related problems.

Text Books:

1. Principles of Electromagnetics - N.O.Sadiku, Oxford University Press,4th edition.
2. Electromagnetic field theory and Transmission Lines – G.SasibhusanaRao,Wiley India Pvt.Ltd.

Reference Books:

1. EM Waves and Radiating Systems – E.C.Jordan, Printice Hall India.
2. Electromagnetics with applications – Kraus and Fleisch, McGraw Hill.
3. Electromagnetic field theory and Transmission lines – G.S.N.Raju, Pearson Education Pvt.Ltd.
4. Engineering Electromagnetics – W.A.Hayt and JABuck, Tata McGraw Hill.

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BHIMAVARAM-534 204.

Code: B16 EC 2203

PULSE AND DIGITAL CIRCUITS

Theory	**: 3 Periods**	**Sessionals**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 4**

Course Objectives:

Students should learn

1. Applications of integrator, differentiator, clippers and clamper circuits.
2. Design of various multivibrators for various applications.
3. Design various Time base generators.
4. Synchronization techniques for sweep circuits.
5. Different logic families, realize logic gates using diodes and transistors.

Course Outcomes:

Students will be able to

1. Understand the applications of integrator, differentiator, clippers and clamper circuits.
2. Design different multivibrators for various applications.
3. Design different time base generators.
4. Analyze synchronization techniques for sweep circuits.
5. Understand different logic families & realize logic gates using diodes and transistors.

SYLLABUS

Linear Wave Shaping:

High pass and Low pass RC circuits, Response of High pass and Low pass RC circuits to sinusoidal, step, pulse, square, exponential and Ramp inputs, High pass RC circuit as a differentiator, Low pass RC circuit as an integrator. Attenuators and its application as CRO probe, RL and RLC Circuits and their response for step input, Ringing Circuit.

Nonlinear Wave Shaping:

Diode clippers, Transistor Clippers, Clipping at two independent levels, Comparator, Applications of voltage Comparators, Diode Comparator, Clamping Operation, Clamping Circuits using Diode with Different Inputs, Clamping Circuit Theorem, Practical Clamping circuits, Effect of diode Characteristics on Clamping Voltage.

BistableMultivibrators:

Transistor as a switch, Switching times of a transistor, Design and Analysis of Fixed-bias and self-bias transistor binary, Commutating capacitors, Triggering schemes of Binary, Transistor Schmitt trigger and its applications.

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Monostable And AstableMultivibrators:

Design and analysis of Collector coupled Monostable Multivibrator, Expression for the gate width and its waveforms. Design and analysis of Collector coupled Astable Multivibrator, expression for the Time period and its waveforms, The Astable Multivibrator as a voltage to frequency convertor.

Time Base Generators:

General features of a time-base signal, Methods of Generating time base waveform, Exponential voltage sweep circuit, Basic principles of Miller and Bootstrap time base generators, transistor Miller sweep generator, transistor Bootstrap sweep generator, Current Sweep circuit, Linearity correction through adjustment of driving Waveform.

Synchronization And Frequency Division:

Principles of Synchronization, Frequency division in sweep circuit, Synchronization of AstableMultivibrators, Synchronization of a sweep circuit with symmetrical signals, Sine wave frequency division with a sweep circuit.

Logic Families:

Realization of gates using diodes and Transistors, RTL, DTL.

TEXT BOOKS:

1. Pulse Digital and Switching Waveforms, J. Millman ,H. Taub, andM.S.PrakashRao McGraw-Hill, Second Edition .
2. Pulse and Digital Circuits, A. Anand Kumar, PHI, Second Edition, 2005.

Reference Books:

1. Pulse and Digital Circuits, K.VenkatRao, Pearson Education India, 2nd Edition, 2010.
2. Pulse Digital and Switching Waveforms, J. Millman and H. Taub, McGraw-Hill, 2nd Edition 1991.
3. Pulse Switching and Digital Circuits – David A.Bell, PHI,5thEdn., Oxford University Press.

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Code: B16 EC 2204

ANALOG COMMUNICATIONS

Theory	**: 3 Periods**	**Sessionals**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 4**

Course Objectives:

1. To understand the fundamental concepts of communication systems.
2. To familiarize with the concepts of linear or amplitude modulation (AM, DSB-SC, SSB and VSB) and demodulation techniques.
3. To familiarize with the concepts of angular or non-linear modulation (FM and PM) and demodulation techniques.
4. To understand and compare different analog modulation schemes.
5. To familiarize with techniques for generating and demodulating narrow-band and wide-band frequency and phase modulated signals.
6. To provide a good understanding of the behavior of analog communications in the presence of noise.
7. To develop a clear insight into the relations between the input and output signals in various stages of a transmitter and a receiver of AM & FM systems.
8. To classify and discuss different types of transmitters and receivers as applicable to analog communication systems.

Course Outcomes:

Upon successful completion of this course the student will be able to

1. Understand the need for modulation and learn about the basic elements of communication system.
2. Understand the concepts of Analog Modulation and Demodulation techniques.
3. Evaluate various parameters of analog modulated waveform in Time and Frequency domain.
4. Analyze and compare the performance of various analog modulation techniques in the presence of noise.
5. Analyze different characteristics of transmitters.
6. Analyze different characteristics of receivers.

SYLLABUS

Linear Modulation Systems:

Need for Modulation, Frequency Translation, Method of Frequency Translation, Amplitude Modulation, Modulation Index, Spectrum of AM Signal, Modulators and Demodulators (Diode detector), DSB-SC Signal and its Spectrum, Balanced Modulator, Synchronous Detectors, SSB Signal, SSB Generation Methods, Power Calculations in AM Systems, Application of AM Systems.

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Angle Modulation Systems:
Angle Modulation, Phase and Frequency Modulation and their Relationship, Phase and Frequency Deviation, Spectrum of an FM Signal, Bandwidth of Sinusoidally Modulated FM Signal, Effect of the Modulation Index on Bandwidth, Spectrum of Constant Bandwidth FM, Phasor Diagram for FM Signals.FM Generation: Parameter variation method, Indirect method of Frequency Modulation (Armstrong Method), Frequency Multiplication, PLL FM Demodulator, Pre – emphasis and De – emphasis, Comparison of FM and AM.

Noise In AM and FM Systems:
Sources of Noise, Resistor Noise, Shot Noise, Noise in AM Systems, Noise in Angle Modulation Systems, Comparison between AM and FM with respect to Noise, Threshold in Frequency Modulation

Radio Transmitters:
Classification of Radio Transmitters, AM and FM Transmitters, Radio Telegraph and Telephone Transmitters, SSB Transmitters

Radio Receivers:
Radio receiver Types, AM Receivers – RF Section, Frequency Changing and Tracking, Intermediate Frequency and IF Amplifiers, Automatic Gain Control (AGC); FM Receivers – Amplitude Limiting, FM Demodulators, Ratio Detectors, ISB Receiver, Comparison with AM Receivers. Communication Receivers: Extensions of the Super-heterodyne Principles, Additional Circuits.

Text Books:

1. Principles of Communication Systems, H. Taub and D. L. Schilling, McGraw Hill, 1971.
2. Electronic Communications System : Fundamentals Through Advanced by Wayne Tomasi.

Reference Books:

1. Modern Digital and Analog Communication Systems, B. P. Lathi (2nd Edition).
2. Electronic Communications Modulation and Transmission, Robert J. Schoenbeck, PHI N. Delhi, 1999.
3. Communication systems, R.P.Singh and S.D.Sapre 2nd edition TMH 2008.
4. Communication Systems, Simon Haykins (2nd Edition).
5. Electronic Communication Systems, G. Kennedy, McGraw Hill, 1977 (2nd Edition)

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Code: B16 EC 2205

SIGNALS AND SYSTEMS

Theory	**: 3 Periods**	**Sessionals**	**30**
Tutorial	**: 1 Period**	**Ext. Marks**	**70**
Exam	**: 3 Hrs.**	**Credits**	**4**

Course Objectives:

1. To introduce the fundamental concepts and techniques associated with the understanding of signals and systems.
2. To familiarize with techniques suitable for analyzing both continuous-time and discrete time LTI systems using transforms.
3. To familiarize with development of the mathematical skills to solve problems involving convolution, filtering, and sampling.

Course Outcomes:

After completion of the course the student will be able to

1. Understand the basic concepts of signals and systems.
2. Analyze the spectral characteristics of Continuous Time and Discrete Time periodic and aperiodic signals using Fourier analysis.
3. Analyze system properties based on impulse response and Fourier analysis
4. Classify systems based on their properties and determine the response of LTI systems using convolution and also understand the concept of correlation between signals.
5. Apply Z- transforms for analyzing discrete-time signals and systems
6. Understand the process of sampling and the effects of under sampling.

SYLLABUS

Introduction to signals and linear time Invariant systems

Continuous –Time and Discrete –Time signals, Signal Energy and Power, Periodic Signals, Even and odd Signals, Continuous- Time complex Exponential and Sinusoidal Signals, Discrete –Time complex Exponential and Sinusoidal Signals, Periodicity of Continuous – Time and Discrete –Time Complex Exponentials, Continuous-Time Unit impulse and Unit step Signals, Discrete- Time Unit Impulse and Unit Step Sequences, Continuous –Time and Discrete –Time Systems, Interconnections of Systems, Basic System Properties of Continuous –Time and Discrete –Time Systems, Introduction of continuous–Time LTI Systems and Discrete –Time LTI Systems, Casual LTI Systems Described by Differential and Difference Equations, Singularity Functions.

Fourier Series Representation of Periodic Signals

Introduction, Fourier Series Representation of continuous time Periodic Signals, convergence of the Fourier Series, Properties of continuous time Fourier Series, Fourier Series representation of discrete time periodic signals, Properties of discrete time Fourier Series.

Continuous and Discrete time Fourier Transform

Introduction, Representation of Aperiodic signals, the continuous time Fourier Transform, The Fourier Transform for periodic signals, Properties of the continuous time Fourier

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Convolution of signals
Introduction of convolution integral and convolution sum, Graphical interpretation of Convolution,System analysis by Convolution,Convolution as a superposition of impulse resp onse,Convolution of a function with a unit impulse,Convolution relationships, Signal comparison.

Correlation of signals
Introduction of Correlation of signals, properties of correlation functions, Introduction of Energy Density Spectrum(ESD) and Power Density Spectrum(PSD), Relation between Autocorrelation function and ESD/PSD, Relation between Convolution and Correlation. Correlation functions for nonfinite energy signals.(8 Periods)

Sampling Theorem and Z-transform
Introduction to signal reconstruction from its samples using interpolation, the effect of under sampling: aliasing, Discrete time processing of continuous time signals, sampling of Discrete time signals. The Z-Transform, The Inverse Z-Transform, Properties of Z-Transform, The initial and final value theorems, some common Z-transform pairs, Analysis and characterization of LTI systems using the Z-Transforms, System function algebra and block diagram representation.(10 Periods)

Text Books:

1. Signals and Systems- Alan V. Oppenheim, Alan S. Willsky and Ian T. Young, PHI, 2ndEdn.
2. Signals Systems and Communication-B. P. Lathi, BS Publication.

Reference Books:

1. Signals and Systems – P.RamakrishnaRao, TMH.
2. Signals and Systems- A.AnandaKumar,PHI.
3. Signals and Systems – P.RameshBabu,SciTech Publication.
4. Signals and Systems-K. Raja Rajeswari and B. V. Rao, Prentice Hall of India

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Code: B16 ENG 2201

ENVIRONMENTAL STUDIES

(Common to ECE,EEE & ME)

Theory	**: 3 Periods**	**Sessionals**	**30**
Tutorial	**: 1 Period**	**Ext. Marks**	**70**
Exam	**: 3 Hrs.**	**Credits**	**2**

Course Objectives:

Students learn

1. To develop an awareness and sensitivity to the total environment and its related problems.
2. To participate actively participation in environmental protection and improvement.
3. To develop skills for active identification and development of solutions to environmental problems
4. To evaluate environment programmes in terms of social, economic, ecological and aesthetic factors.
5. To Create a "CONCERN AND RESPECT FOR THE ENVIRONMENT"

Course Outcomes:

Students will be able to

1. Get awareness among the students about the nature and natural ecosystems.
2. Learn sustainable utilization of natural resources like water, land, minerals, air.
3. Learn resource pollution and over exploitation of land, water, air and catastrophic (events) impacts of climate change, global warming, ozone layer depletion, marine, radioactive pollution etc to inculcate the students about environmental awareness and safe transfer of our mother earth and its natural resources to the next generation.
4. Safe guard against industrial accidents particularly nuclear accidents.
5. Learn Constitutional provisions for the protection of natural resources.

SYLLABUS

Global Environmental Crisis:

Environmental Studies - Definition, Scope and importance, Need for public awareness. Global Environmental Crisis

Ecosystems:

Basic concepts, Forest Ecosystems, Grassland Ecosystems and Desert Ecosystems, Aquatic Ecosystems

Biodiversity:

Introduction to Biodiversity, Value of Bio-diversity, Bio-geographical classification of India, India as a Mega-diversity habitat, Threats to biodiversity, Conservation of Biodiversity: In-situ and Ex-situ conservation of bio-diversity.

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Environmental and Natural Resources Management:

Land Resources: Land degradation, soil erosion and desertification, Effects of modern agriculture **Forest Resources:** Use and over exploitation-Mining and Dams-their effects on forest and tribal people, **Water resources**: Use and over utilization of surface and ground water, Floods, droughts, conflict over water, water logging and salinity, dams – benefits and problems

Energy Resources: Renewable and non-renewable energy sources, use of alternate energy sources-impact of energy use on environment.

Environmental Pollution:

Causes, Effects and Control measures of - Air pollution, Water pollution, Soil pollution, Marine Pollution, Thermal pollution, Noise pollution, Nuclear Hazards; Climate change and global warming, acid rain and Ozone layer depletion.

Environmental Problems in India:

Drinking water, Sanitation and Public health, population growth and environment; Water Scarcity and Ground Water Depletion; Rain water harvesting, Cloud seeding and Watershed management.

Text Books:

1. Environmental Studies (From Crisis to Cure) by R. Rajagopalan, Oxford university Press, 2008
2. Environmental Studies by Anubha Kaushik & C.P. Kauskik, New Age International (P) Ltd, New Delhi, 2006

Reference Books:

1. Environmental Sciences by G.Tyler Miller, JR,10th ed, Thomson publishers, 2004

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Code: B16 EC 2207

ANALOG COMMUNICATION LAB

Lab	**: 3 Periods**	**Sessionals**	**50**
Exam	**: 3 Hrs.**	**Ext. Marks**	**50**
		Credits	**2**

Course Objectives:

1. The purpose of this course is to provide the student with a practical perspective of various Analog communication modules.
2. This course also helps the student to implement various analog modulation and demodulation schemes using discrete components.
3. To be familiar with the design of various parameters of LPF , BPF and HPF
4. To design IF and RF amplifiers and plot their frequency response.
5. To be familiar with different types of experiments like pre-emphasis, de-emphasis and DSB-SC waveform generators.

Course Out Comes:

1. Design and implement modulation and demodulation circuits for amplitude modulation technique.
2. Design and implement modulation and demodulation circuits for frequency modulation technique.
3. Design second order passive and active filters for various frequency bands.
4. Construct the circuit and study the characteristics of different transmitter and receiver circuits such as Harmonic generator, RF Amplifier, IF Amplifier, pre-emphasis and -emphasis.

SYLLABUS

1. Generation of AM Signal and measurement of Modulation Index.
2. Diode Detector for AM Signals.
3. Generation of FM Signal.
4. FM Detector.
5. Receiver Measurements.
6. Balanced Modulator.
7. Passive Filters (LPF, HPF, BPF).
8. Active Filters.
9. Attenuator.
10. Equalizer and Twin-T-Network.
11. Frequency Multiplier/Limiter.
12. SSB Generation and Detection.
13. Pre-emphasis and De-emphasis.
14. PLL.
15. IF Amplifier.

Reference : Lab Manuals.

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Code: B16 EC 2208

ANALOG ELECTRONIC CIRCUITS LAB WITH SIMULATION

(Common to ECE & EEE)

Lab	**: 3 Periods**	**Sessionals**	**: 50**
Exam	**: 3 Hrs.**	**Ext. Marks**	**: 50**
		Credits	**: 2**

Course Objectives:

1. This laboratory course enables students to get practical experience in design, assembly and evaluation of analog electronic circuits. They will use Multisim to test their electronic designs.

Course Outcomes:

Students will be able to:

1. Acquire a basic knowledge on simple applications of operational amplifier.
2. Observe the amplitude and frequency responses of negative feedback amplifier and two stage RC coupled amplifier.
3. Design and test sinusoidal oscillators.
4. Design and test a power amplifier.
5. Design,construct,and take measurement of the analog electronic circuits tocompare experimental results in the laboratory with theoretical analysis.
6. Use Multisim to test their electronic design.

LIST OF EXPERIMENTS

1. Design of LC Oscillators (Hartley Oscillator,Colpitts Oscillator)
2. Design of RC Oscillators (Wien Bridge Oscillator,RC phase Shift Oscillator)
3. Design of Basic Applications of Operational Amplifier.
4. Frequency response of Two Stage RC Coupled Amplifier.
5. Frequency response ofCurrent Series Feedback Amplifier(with and without feedback)
6. Measurement of resonant frequency, bandwidth and quality factor of single Tuned Voltage Amplifier.
7. Calculation of Collector Circuit efficiency of Class B Push Pull Power Amplifier.

LIST OF EXPERIMENTS
(Simulation)

8. Design of LC Oscillators (Hartley Oscillator,Colpitts Oscillator)
9. Design of RC Oscillators (Wien Bridge Oscillator,RC phase Shift Oscillator)
10. Design of Basic Applications of Operational Amplifier.
11. Frequency response of Two Stage RC Coupled Amplifier.
12. Frequency response ofCurrent Series Feedback Amplifier(with and without feedback)
13. Measurement of resonant frequency, bandwidth and quality factor of single Tuned Voltage Amplifier.
14. Calculation of Collector Circuit efficiency of Class B Push Pull Power Amplifier.

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Code: B16 EC 2209

INDUSTRY ORIENTED TECHNOLOGY LAB

Lab	**: 2 Periods**	**Sessionals**	**50**
Exam	**: 3 Hrs.**	**Credits**	**1**

Course Objectives:

This laboratory course enables students to get practical experience in design, interfacing of different sensors and motors with the microcontrollers like Aurdino and Single Board Computer like Raspberry Pi.

Experiments:

1. Familiarization with Aurdino microcontroller and raspberry pi, different sensors, drivers and motors.
2. Control of conducting and non-conducting periods of LED using Aurdino microcontroller.
3. Interfacing humidity and temperature sensor (DHT11) with Aurdino microcontroller.
4. Interfacing ultrasonic sensor, PIR sensor with Aurdino microcontroller.
5. Interfacing DC motor with Aurdino microcontroller using L298 motor driver.
6. Interfacing and control of servo motor with Aurdino microcontroller.
7. Introduction to Raspberry Pi and interfacing different sensors and motors to it.
8. Interfacing camera with Raspberry Pi and performing different operations using open CV (Computer Vision).
9. Controlling DC motor based on DHT11 sensor output using Raspberry Pi.
10. Controlling of DC motor based on ultrasonic sensor output using Raspberry Pi.
11. Image capturing based on PIR sensor output using Raspberry Pi.
12. Introduction to GSM/GPS module and interfacing them with Aurdino and Raspberry Pi.

(Note: Total Marks will be evaluated based on Continuous Evaluation - 25 Marks, Record/Report-10 Marks, Exam-10 Marks and Attedndance-5 Marks)

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Code: B16 ENG 2204

INDUSTRY ORIENTED TRAINING

(Common to ECE & EEE)

Lab	**: 2 Periods**	**Sessionals**	**50**
Exam	**: 3 Hrs.**	**Credits**	**1**

Course Objectives:

1. Be familiar with core JAVA.
2. Master the implementation of Applet programming.
3. Master the implementation of Networking concepts in core JAVA.
4. Be familiar with CORBA , J2EE, RMI concepts..

Course Outcomes:

1. Application using implementation of core JAVA concepts.
2. Application using implementation of AWT, Applets
3. Applications using Networking concepts in view of industry.

Syllabus: Industry Oriented Applications on following topics.

BASIC CONCEPTS

Fundamentals: HTML, OOP Concepts, Comparing JAVA with C & C++,JAVA Programming language Syntax, Variables, Data types, statements and expressions.

Control Statements: If else, for, while, and do while loops, Switch statements.

Arrays & Structures: One Dimensional & Two Dimensional Arrays, Named Structures.

Functions: Parameter Passing, Static Modifier.

IMPLEMENTATION (Using JAVA)

Classes and Interfaces
Threads and multithreaded programming
packages.

Applications of AWT, Applets and Networking concepts and solving simple to complex problems in perspective of industry requirements.

(Note: Total Marks will be evaluated based on Continuous Evaluation - 25 Marks, Coding Contest- 25 Marks)

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(Affiliated to Andhra University, Visakhapatnam), (Recognized by AICTE, New Delhi)
Accredited by NAAC with „A" Grade
Recognized as Scientific and Industrial Research Organization
CHINNA AMIRAM (P.O), BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN: 534 204
ESTD: 1980

SCHEME OF INSTRUCTION & EXAMINATION
(Regulation R16)
III/IV B.TECH
(With effect from **2016-2017** Admitted Batch onwards)
Under Choice Based Credit System
ELECTRONICS AND COMMUNICATION ENGINEERING
I-SEMESTER

Code No.	Course	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Total Contact Hrs/Week	Sessional Marks	Exam Marks	Total Marks
B16 EC 3101	Linear ICs and Applications	4	3	1	-	4	30	70	100
B16 ENG 3101	Principles of Economics and Management	4	3	1	-	4	30	70	100
B16 EC 3102	Computer Architecture and Organization	4	3	1	-	4	30	70	100
B16 EC 3103	Antennas and Propagation	4	3	1	-	4	30	70	100
B16 EC 3104	Electronic Measurements and Instrumentation	4	3	1	-	4	30	70	100
B16 EC 3106	Linear Integrated Circuits & Pulse Circuits Lab with Simulation	4	3	1	-	4	30	70	100
B16 EC 3107	Digital Integrated Circuits &Hardware Descriptive Language	2	-	-	3	3	50	50	100
B16 ENG 3102	Verbal & Quantative Aptitude-I	2	5	--	--	5	100	--	100
#M-I	MOOCS-I	2	4	--	--	4	100	--	100
#M-II	MOOCS – II	2	4	--	--	4	100	--	100
	Total	**34**	**31**	**6**	**6**	**43**	**580**	**520**	**1100**

#M-I	B16 ENG 3103	Basic Coding
	B16 EC 3109A	Microwave Theory and Techniques
	B16 EC 3109B	Analysis and Design Principles of Microwave Antennas
	B16 EC 3109C	Principles of Digital Communications
#M-II	B16 EC 3110A	Introduction to Wireless and Cellular Communications
	B16 EC 3110B	Fabrication Techniques for MEMS-based Sensors: Clinical Perspective
	B16 EC 3110C	Introduction to information theory, Coding and cryptography
	B16 EC 3110D	Digital Image Processing

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ELECTRONICS AND COMMUNICATION ENGINEERING
II-SEMESTER

Code No.	Course	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Total Contact Hrs/Week	Sessional Marks	Exam Marks	Total Marks
B16 EC 3201	Microwave Engineering	4	3	1	--	4	30	70	100
B16 EC 3202	Microprocessors and its Applications	4	3	1	--	4	30	70	100
B16 EC 3203	Digital Communication	4	3	1	--	4	30	70	100
#ELE-I	ELECTIVE-I	4	3	1	--	4	30	70	100
B16 EC 3206	Digital Signal Processing	4	3	1	--	4	30	70	100
#ELE-II	ELECTIVE-II	4	3	1	--	4	30	70	100
B16 EC 3211	DSP Laboratory	2	--	--	3	3	50	50	100
B16 EC 3212	Microprocessors and Microcontrollers Lab	2	--	--	3	3	50	50	100
B16 ENG 3202	Verbal & Quantative Aptitude-II	2	5	--	--	5	100	--	100
B16 EC 3213	Mini Project	2	--	--	3	3	50	--	50
#M-III	MOOCS-III	2	4	--	--	4	100	--	100
	Total	**34**	**27**	**6**	**9**	**42**	**530**	**520**	**1050**

#ELE-I	B16 EC 3204	Radar & Navigation
	B16 EC 3205	Information Theory And Coding
	B16 CS 3210	Object oriented programming
	B16 CS 3211	Web technologies
	B16 CS 3212	Software Engineering
#ELE-II	B16 EC 3207	Embedded Systems & Microcontrollers
	B16 EC 3208	Micro Electronics
	B16 EC 3209	Telecommunication Switching Systems
	B16 EC 3210	Digital Signal Processors And Architectures
#M-III	B16 ENG 3204	Advanced Coding
	B16 EC 3214A	Principles of Signals and Systems
	B16 EC 3214B	Analog Circuits
	B16 EC 3214C	Microprocessors and Microcontrollers

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CHINNA AMIRAM (P.O), BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN: 534 204
ESTD: 1980

SCHEME OF INSTRUCTION & EXAMINATION
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IV/IV B.TECH
(With effect from **2016-2017** Admitted Batch onwards)
Under Choice Based Credit System
ELECTRONICS AND COMMUNICATION ENGINEERING

I-SEMESTER

Code No.	Course	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Total Contact Hrs/Week	Sessional Marks	Exam Marks	Total Marks
B16 EC 4101	Digital Image Processing	4	3	1	-	4	30	70	100
B16 EC 4102	VLSI Design	4	3	1	-	4	30	70	100
B16 EC 4103	Fiber Optic Communications	4	3	1	-	4	30	70	100
B16 EC 4104	Microwave Engineering & Optical communications Lab	2	-	-	3	3	50	50	100
B16 EC 4105	Digital Communication Lab	2	-	-	3	3	50	50	100
B16 EC 4106	Project Phase-I	2	-	-	3	3	50	--	50
	Total	**18**	**9**	**3**	**9**	**21**	**240**	**310**	**550**

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(Regulation R16)

IV/IV B.TECH
(With effect from **2016-2017** Admitted Batch onwards)
Under Choice Based Credit System
ELECTRONICS AND COMMUNICATION ENGINEERING
II-SEMESTER

Code No.	Course	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Total Contact Hrs/ Week	Sessional Marks	Exam Marks	Total Marks
B16 EC 4201	Cellular and Mobile Communications	4	3	1	-	4	30	70	100
B16 EC 4202	Computer Networks	4	3	1	-	4	30	70	100
# ELE-III	ELECTIVE-III	4	3	1	-	4	30	70	100
B16 EC 4208	Project Phase-II	12	-	-	9	9	50	100	150
	Total	**24**	**9**	**3**	**9**	**21**	**140**	**310**	**450**

# ELE-III	B16 EC 4203	Internet of things.
	B16 EC 4204	Digital System design Through HDL
	B16 EC 4205	Bio Medical Signal Processing.
	B16 EC 4206	Satellite Communication
	B16 EC 4207	Digital TV

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (AUTONOMOUS)

(AFFILIATED TO ANDHRA UNIVERSITY, VISAKHAPATNAM) (RECOGNISED BY ALL INDIA COUNCIL FOR TECH. EDN., NEW DELHI)
Accredited by NAAC with 'A' Grade, Recognised as Scientific and Industrial Research Organisation
CHINNA AMIRAM (P.O):: BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN. 534 204
Phones: Off : EPABX :08816-223332 Ext.201
Direct :08816-222748
Mobile No.98483 81818
Email: gpsvarma@gmail.com
principal@srkrec.ac.in
principalsrkrec@gmail.com
Web Site : www.srkrec.ac.in

Dr. G. P. Saradhi Varma
B.Tech.(CSE), M.Tech.(CSE), Ph.D.(CSE).
PRINCIPAL

Estd. 1980

PROCEEDINGS OF THE PRINCIPAL

Ref. No: SRKREC/Committee/BoS/ECE//2 Date: 05-07-2017

Sub: Appointment of BoS members for Electronics and Communication Engineering (ECE) department-Reg.

The following members are nominated as Board of Studies members for the Department of Electronics and Communication Engineering (ECE).This order will come into force with immediate effect until further orders.

S No	Name	Position in committee	Associated with
01	Dr. P.V.Rama Raju	Chair Person	Professor & Head, SRKREC
02	Dr. K.PadmaPriya	JNTUK Nominee	Prof. Dept. of ECE, UCEK, JNTUK
03	Prof. G.Sasibhushana Rao	AU Nominee	Prof. Dept. of Electronics and Communication Engineering , AU
04	Dr. N.V.S.N.Sarma	Experts from other Universities	Prof. NITW
05	Dr. B.V.Ramana Reddy		Prof. & Dean, Guru GobindIndraprastha University
06	Dr. A.M.Prasad		Prof. JNTUK
07	Sri Rama Raju	Industry Expert	Director, Envision Enterprise Solutions
08	Dr. M.Chakravarty	Expertfrom Research Organization	Scientist G, DLRL Hyderabad
09	Dr. K.V.S.N. Raju	Faculty of each specialization	Vice-Principal, SRKREC
10	Prof. N.Venkateswara Rao		Professor ,SRKREC
11	Prof.D.V.R.Mohan		Professor, SRKREC
12	Prof.P.Subba Rao		Professor,SRKREC
13	Prof.N.Udaya Kumar		Professor, SRKREC
14	Dr. B.V.S.S.N.Raju		Professor,SRKREC
15	Prof.G.V.S.Padma Rao		Professor, SRKREC
16	Sri M.Vijaya Rama Raju		Associate Prof., SRKREC
17	Sri S.S.Mohan Reddy		Associate Prof., SRKREC
18	Sri Y.Rama Lakshmana		Associate Prof., SRKREC
19	Student Representatives		

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S.R.K.R. Engineering College
(Autonomous)
Chinna Amiram, Bhimavaram-534 204

c.c.to:
1. Principal's table
2. HOD-ECE,
3. All the above Members 4 Office files

S.R.K.R. ENGINEERING COLLEGE (AUTONOMOUS) Engg. College CHINNA AMIRAM BHIMAVARAM Pin: 534 204.

AGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (AUTONOMOUS)

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B.Tech.(CSE), M.Tech.(CSE), Ph.D.(CSE).

PRINCIPAL

Estd. 1980

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principalsrkrec@gmail.com
gpsvarma@gmail.com
WebSite : www.srkrec.ac.in

SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Dr. K. Padma Priya
Professor, Dept. of ECE
University College of Engineering Kakinada
JNTUK, Kakinada.

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding.

***

We are happy to inform you that you have been nominated as Member of Board of Studies to our EEE Department by JNTUK, Kakinada. We request you to kindly give your expert advise in designing of the curriculum to maintain quality in Academics.

In this connection, we wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

The Boards of Studies meetings will be conducted in respective departments as per the schedule mentioned below:

S.No.	BOARD	DATE	TIME
1	Civil Engineering	16-07-2017	2.00 P.M
2	Computer Science & Engineering	16-07-2017	2.00 P.M
3	Electronics & Communication Engineering	16-07-2017	2.00 P.M
4	Electrical & Electronics Engineering	16-07-2017	2.00 P.M
5	Information Technology	16-07-2017	2.00 P.M
6	Mechanical Engineering	16-07-2017	2.00 P.M
7	Combined Board of Studies in Engineering Chemistry/Engineering Mathematics/Engineering Physics / Humanities and Social Sciences	16-07-2017	2.00 P.M

The T.A / D.A. will be paid to all the external members as per the university guidelines.

We request you to make it convenient to attend the meetings.

Encl: Copy of JNTUK Nomination Letter

Yours faithfully,

G.P.S

PRINCIPAL

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

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gpsvarma@gmail.com
WebSite : www.srkrec.ac.in

SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Prof. G.Sasibhushana Rao
Department of Electronics and Communication Engineering
Andhra University
Visakhapatnam - 530 003.

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

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We request you to make it convenient to attend the meetings.

Yours faithfully,

G.P.S

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SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Dr. N.V.S.N.Sarma
Department of ECE
NIT-Warangal

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

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The T.A / D.A. will be paid to all the external members as per the university guidelines.

We request you to make it convenient to attend the meetings.

Yours faithfully,

G.P.S

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

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SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (AUTONOMOUS)

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gpsvarma@gmail.com
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SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Dr. B.V.Ramana Reddy
Professor & Dean
Guru Gobind Indra Prastha University
Dwarka, New Delhi

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

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We request you to make it convenient to attend the meetings.

Yours faithfully,

G.P.S

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BHIMAVARAM-534 204.

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SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Dr. A.M.Prasad
Professor – Department of ECE
JNTUK, Kakinda.

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

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Yours faithfully,

G.P.S

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SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Sri Rama Raju
Director, Envision
Jubilee Hills, Hyderabad-500033

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

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Yours faithfully,

G.P.S

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BHIMAVARAM-534 204.

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SRKREC/BOS-1/ECE/2017-18

Date: 08.07.2017

To
Dr. M.Chakravarty
Scientist G
DLRL, Hyderabad

Dear Madam,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies will be held on 16.07.2017 at 11-00 A.M in A.C. Auditorium.

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The T.A / D.A. will be paid to all the external members as per the university guidelines.

We request you to make it convenient to attend the meetings.

Yours faithfully,

G.P.S

PRINCIPAL

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE(A)
CHINNA AMIRAM :: BHIMAVARAM-534204
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Dt: 14-07-2017

CIRCULAR

This is to inform you that the Department of ECE has convened a meeting on 16-07-2017 at 2.30 PM in the ECE Department Library. In this connection, all the Board of Studies members are requested to attend the same.

Agenda:

1. Finalization of scheme for second,third and fourth years of UG and PG courses.Which will come into force from 2017-2018 first year admitted batch.

Head of the Department

Head of ECE Department
S.R.K.R. Engg. College
BHIMAVARAM-534 204

C.C to:
1. The Members of Board of studies
2. Office file

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

6

BOS Meeting for ECE on 16-07-2017

16-07-2017.

The Board of Studies meeting for UG and PG Courses is conducted on 16-07-2017 at 2.30 PM in ECE department

Venue: ECE Dept Library Time: 2.30 P.M.

Date: 16-07-2017

Agenda: Finalization of scheme for second, third and final years of UG and PG courses which will come into force from 2017-2018 first year admitted batch.

	Members present	Signature
1	Prof NVSN Sarma, NIT, W	NVSN Sarma
2	Dr M. CHAKRAVARTHY, SCG, DLRL, DRDO	M Chakravarthy
3	Dr. K. Padma Raju, Professor, ECE dept, JNTUK	
4	A.M. Prasad	
5	Dr N. USHA KIRAN	
6	Dr. P.V. Ramana Rao	
7	N. Venkateswarlu	
8	Prof K.V.S.N. Raju	
9	Prof. P. Sudha Rao	
10	BVSSN Raju	
11	G.V.S. Padma Rao	
12	M. VIDYA SAGAR RAJU.	
13	S.S. Mohan Reddy	
14	M. DVR Mohan	
15	J.B. Skandha Vardhan (B.E)	Skandha JB
16	K. Surya Mohan Reddy (B.E)	K. Surya

M. Jagapathi Raju

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(17)	B. Chandra Baby	(M.Tech)	B. Chandra Baby
18	Inapanurthi Rambabu	(M.Tech)	I. Rambabu
19.	Ch. Murali Krishna	(M.Tech)	Ch. Krishna

Resolutions:

1. Syllabus for the first year courses B.Tech & M.Tech related to ECE department as per the courses finalized in the Joint Board is approved and passed in BOS for JNTUK affiliated batches 2017-2018.
2. Scheme for B.Tech 2nd, 3rd & 4th years and Scheme for M.Tech 1st & 2nd years is finalized and passed in BOS for JNTUK affiliated batch 2017-2018
3. Syllabus for Elements of Electronics Engineering (for CSE & IT branches) is discussed and finalized.

M. Jagapathi Raju

PRINCIPAL
S.R.K.R. Engg. College
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2ND BOS RESOLUTION

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ESTD: 1980

SCHEME OF INSTRUCTION & EXAMINATION
(Regulation R17)
I/IV B.TECH
(With effect from **2017-2018** Admitted Batch onwards)
Under Choice Based Credit System
GROUP-B (CSE, ECE & IT)
I-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 BS 1101	English – I*	3	3	1	--	4	30	70	100
B17 BS 1102	Mathematics – I*	3	3	1	--	4	30	70	100
B17 BS 1103	Mathematics-II	3	3	1	--	4	30	70	100
B17 BS 1104	Engineering Physics	3	3	1	--	4	30	70	100
B17 CS 1101	Computer Programming Using C	3	3	1	--	4	30	70	100
B17 CE 1101	Environmental Studies *	2	2	1	--	3	30	70	100
B17 BS 1106	Engineering Physics Lab	2	--	--	3	3	50	50	100
B17 BS 1108	English Communication Skills Lab – I *	2	--	--	3	3	50	50	100
# DL1	Department Lab	2	--	--	3	3	50	50	100
B17 BS 1110	Engineering Physics Virtual Labs-Assignments	--	--	--	2	2	--	--	--
B17 BS 1112	NCC	--	--	--	2	2	--	--	--
	Total	**23**	**17**	**6**	**13**	**36**	**330**	**570**	**900**

*** Common to both Group - A and Group - B**

#DL 1	CSE & IT	B17 CS 1102	C Programming Lab & Hardware Fundamentals
	ECE	B17 CS 1103	C Programming Lab

S.R.K.R. Engg. College
BHIMAVARAM-534 204.

Code: B17 CS 1102

C PROGRAMMING LAB& HARDWARE FUNDAMENTALS

(Common to CSE & IT)

Lab : 3 Periods **Int.Marks : 50**
Exam : 3 Hrs. **Ext. Marks : 50**
Credits : 2

Course Objectives:

1. Understand the basic concept of C Programming, and its different modules that includes conditional and looping expressions, Arrays, Strings, Functions, Pointers, Structures and File programming.
2. Acquire knowledge about the basic concept of writing a program.
3. Role of constants, variables, identifiers, operators, type conversion and other building blocks of C Language.
4. Use of conditional expressions and looping statements to solve problems associated with conditions and repetitions.
5. Role of Functions involving the idea of modularity.

Course Outcomes:

1. Apply and practice logical ability to solve the problems.
2. Understand C programming development environment, compiling, debugging, and linking and executing a program using the development environment.
3. Analyzing the complexity of problems, Modularize the problems into small modules and then convert them into programs.
4. Understand and apply the in-built functions and customized functions for solving the problems.
5. Understand and apply the pointers, memory allocation techniques and use of files for dealing with variety of problems.
6. Document and present the algorithms, flowcharts, and programs in form of user manuals.
7. Identification of various computer components, Installation of software

List of Programs

Exercise - 1 Basics

a) What is an OS Command, Familiarization of Editors - vi, Emacs
b) Using commands like mkdir, ls, cp, mv, cat, pwd, and man
c) C Program to Perform Adding, Subtraction, Multiplication and Division of two numbers From Command line

Exercise - 2 Basic Math

a) Write a C Program to Simulate 3 Laws at Motion
b) Write a C Program to convert Celsius to Fahrenheit and vice versa

Exercise - 3 Control Flow - I

a) Write a C Program to Find Whether the Given Year is a Leap Year or not.
b) Write a C Program to Add Digits & Multiplication of a number

Exercise – 4 Control Flow - II

a) Write a C Program to Find Whether the Given Number is
   i) Prime Number
   ii) Armstrong Number
b) Write a C program to print Floyd Triangle
c) Write a C Program to print Pascal Triangle.

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Exercise – 5 Functions

a) Write a C Program demonstrating of parameter passing in Functions and returning values.

b) Write a C Program illustrating Fibonacci, Factorial with Recursion without Recursion.

Exercise – 6 Control Flow – III

a) Write a C Program to make a simple Calculator to Add, Subtract, Multiply or Divide Using switch…case

b) Write a C Program to convert decimal to binary and hex (using switch call function the function)

Exercise – 7 Functions - Continued

a) Write a C Program to compute the values ofsin x and cos x and e^x values using Series expansion. (use factorial function)

Exercise – 8 Arrays

Demonstration of arrays

a) Search-Linear.

b) Sorting-Bubble, Selection.

c) Operations on Matrix.

Exercises - 9 Structures

a) Write a C Program to Store Information of a Movie Using Structure

b) Write a C Program to Store Information Using Structures with Dynamically Memory Allocation

c) Write a C Program to Add Two Complex Numbers by Passing Structure to a Function

Exercise - 10 Arrays and Pointers

a) a)Write a C Program to Access Elements of an Array Using Pointer

b) Write a C Program to find the sum of numbers with arrays and pointers.

Exercise – 11 Dynamic Memory Allocations

a) Write a C program to find sum of n elements entered by user. To perform this program, allocate memory dynamically using malloc () function.

b) Write a C program to find sum of n elements entered by user. To perform this program, allocate memory dynamically using calloc () function.
Understand the difference between the above two programs

Exercise – 12 Strings

a) Implementation of string manipulation operations **with** library function.

i) copy
ii) concatenate
iii) length
iv) compare

b) Implementation of string manipulation operations **without** library function.

i) copy
ii) concatenate
iii) length
iv) compare

Exercise -13 Files

a) Write a C programming code to open a file and to print it contents onscreen.

b) Write a C program to copy files

Exercise - 14 Files Continued

a) Write a C program merges two files and stores their contents in another file.

b) Write a C program to delete a file.

Exercise - 15

a) System Assembling, Disassembling and identification of Parts / Peripherals.

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b) Operating System Installation-Install Operating Systems like Windows, Linux along with necessary Device Drivers.

Exercise - 16

a) MS-Office / Open Office

i) Word - Formatting, Page Borders, Reviewing, Equations, symbols

ii) Spread Sheet-Organize data, usage of formula, graphs, charts.

iii) PowerPoint - features of power point, guidelines for preparing an effective presentation.

b) Network Configuration & Software Installation-Configuring TCP/IP, Proxy, and firewall settings. Installing application software, system software & tools.

Note:

a) All the Programs must be executed in the Linux Environment. (Mandatory)

b) The Lab record must be a print of the LATEX (.tex) Format.

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Recognised as Scientific and Industrial Research Organisation
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SCHEME OF INSTRUCTION & EXAMINATION
(Regulation R17)
II/IV B.TECH
(With effect from **2017-2018**Admitted Batch onwards)

ELECTRONICS & COMMUNICATION ENGINEERING

I-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 BS 2101	Mathematics - IV	3	3	1	--	4	30	70	100
B17 EC 2101	Electronic Devices and Circuits	3	3	1	--	4	30	70	100
B17 EC 2102	Switching Theory and Logic Design	3	3	1	--	4	30	70	100
B17 EC 2103	Signals and Systems	3	3	1	--	4	30	70	100
B17 EE 2104	Network Analysis	3	3	1	--	4	30	70	100
B17 EC 2104	Probability Theory and random Processes	3	3	1	--	4	30	70	100
B17 EC 2107	Electronic Devices and Circuits Lab	2	--	--	3	3	50	50	100
B17 EE 2106	Networks and Electrical Technology Lab	2	--	--	3	3	50	50	100
B17 BS 2106	Programming Skills-I	1	--	--	2	2	50	---	50
B17 BS 2107	English Proficiency-I	--	1	1	--	2	--	--	--
	Total	**23**	**19**	**7**	**8**	**34**	**330**	**520**	**850**

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ELECTRONICS & COMMUNICATION ENGINEERING

II-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 EC 2201	Electronic Circuit Analysis	3	3	1	--	4	30	70	100
B17 EE 2203	Control Systems	3	3	1	--	4	30	70	100
B17 EC 2202	Electromagnetic Field Theory and Transmission Lines	3	3	1	--	4	30	70	100
B17 EC 2203	Analog Communications	3	3	1	--	4	30	70	100
B17 EC 2204	Computer Architecture and Organization	3	3	1	--	4	30	70	100
B17 BS 2201	Management Science	3	3	1	--	4	30	70	100
B17 EC 2207	Electronic Circuit Analysis Lab With Simulation	2	--	--	3	3	50	50	100
B17 EC 2208	Analog Communication Lab	2	--	--	3	3	50	50	100
B17 BS 2205	Programming Skills–II	1	--	--	2	2	50	--	50
B17 BS 2204	Professional Ethics & Human Values	--	2	--	--	2	--	--	--
B17 BS 2206	English Proficiency-II	--	1	1	--	2	--	--	--
	Total	**23**	**21**	**7**	**8**	**36**	**330**	**520**	**850**

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SCHEME OF INSTRUCTION & EXAMINATION

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III/IV B.TECH

(With effect from 2017-2018 Admitted Batch onwards)

Under Choice Based Credit System

ELECTRONICS AND COMMUNICATION ENGINEERING

I-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 EC 3101	Pulse and Digital Circuits	3	3	1	--	4	30	70	100
B17 EC 3102	Linear ICs and Applications	3	3	1	--	4	30	70	100
B17 EC 3103	Electronic Measurements And Instrumentation	3	3	1	--	4	30	70	100
B17 EC 3104	Digital Communication	3	3	1	--	4	30	70	100
B17 EC 3105	Antennas and Propagation	3	3	1	--	4	30	70	100
B17 EC 3106	Computer Network Engineering	3	3	1	--	4	30	70	100
B17 EC 3107	Linear Integrated Circuits and Pulse Circuits Lab with Simulation	2	--	--	3	3	50	50	100
B17 EC 3108	Digital ICs Laboratory with simulation	2	--	--	3	3	50	50	100
B17 BS 3101	Problem Solving & Linguistic Competence	1	--	3	--	3	30	70	100
B17 BS 3102	Basic Coding	1	--	--	3	3	50	50	100
Total		**24**	**18**	**9**	**9**	**36**	**360**	**640**	**1000**

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II-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 EC 3201	Microprocessors and its Applications	3	3	1	--	4	30	70	100
B17 EC 3202	Microwave Engineering	3	3	1	--	4	30	70	100
B17 EC 3203	VLSI Design	3	3	1	--	4	30	70	100
B17 EC 3204	Digital Signal Processing	3	3	1	--	4	30	70	100
B17 EC 3205	Radar Engineering	3	3	1	--	4	30	70	100
#OE	OPEN ELECTIVE	3	3	1	--	4	30	70	100
B17 EC 3208	Microprocessors and Microcontrollers Lab	2	--	--	3	3	50	50	100
B17 EC 3209	VLSI Lab	2	--	--	3	3	50	50	100
B17 BS 3201	Employability Skills	1	--	3	--	3	30	70	100
B17 BS 3203	Advanced Coding	1	--	--	3	3	50	50	100
B17 BS 3206	IPR & PATENTS	--	--	2	--	2	--	--	--
Total		**24**	**18**	**11**	**9**	**38**	**360**	**640**	**1000**

OPEN ELECTIVE	B17EC3206	Microcontrollers
	B17CS3214	Oops through Java
	B17CS3215	Data Mining
	B17ME3210	Industrial Robotics
	B17EE3209	Power Electronics
	B17EC3207	Bio Medical Engineering
	B17CS3216	Artificial Neural Networks

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Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 EC 4101	Wireless Communications & Networks	3	3	1	--	4	30	70	100
B17 EC 4102	Digital Image Processing	3	3	1	--	4	30	70	100
B17 EC 4103	Fiber Optic Communications	3	3	1	--	4	30	70	100
B17 EC 4104	Embedded Systems & Internet Of Things	3	3	1	--	4	30	70	100
#ELE-I	ELECTIVE-I	3	3	1	--	4	30	70	100
B17 EC 4108	Digital Signal Processing Lab	2	--	--	3	3	50	50	100
B17 EC 4109	IOT Lab	2	--	--	3	3	50	50	100
	Total	**19**	**15**	**5**	**6**	**26**	**250**	**450**	**700**

#ELE - I	B17 EC 4105	Information Theory and Coding
	B17 EC 4106	Satellite Communications & GPS
	B17 EC 4107	Analog IC Design

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II-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs.	Tutorial Hrs.	Lab Hrs.	Contact Hrs./ Week	Internal Marks	External Marks	Total Marks
B17 EC 4201	Cellular and Mobile Communications	3	3	1	--	4	30	70	100
#ELE-II	ELECTIVE - II	3	3	1	--	4	30	70	100
B17 EC 4205	Advanced Communication Lab	2	--	--	3	3	50	50	100
B17 EC 4206	Seminar	2	--	--	--	--	50	--	50
B17 EC 4207	Project	10	--	--	6	6	60	140	200
	Total	**20**	**6**	**5**	**9**	**20**	**220**	**330**	**550**

#ELE - II	B17 EC 4202	AI & Machine Learning
	B17 EC 4203	Network Security and Cryptography
	B17 EC 4204	Digital Signal Processors and Architectures

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SCHEME OF INSTRUCTION & EXAMINATION
(Regulation R17)

M.TECH (COMMUNICATION SYSTEMS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

(With effect from **2017-2018** Admitted Batch onwards)
Under Choice Based Credit System

I-SEMESTER

Code No.	Name of the Subject	Credits	Lec-ture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
M17 CS 1101	Detection & Estimation Theory	3	3	1	--	4	30	70	100
M17 CS 1102	Digital Data Communications	3	3	1	--	4	30	70	100
M17 CS 1103	Coding Theory & Applications	3	3	1	--	4	30	70	100
M17 CS 1104	Advanced Digital Signal Processing	3	3	1	--	4	30	70	100
#ELE-1	Elective-I	3	3	1	--	4	30	70	100
#ELE-2	Elective-II	3	3	1	--	4	30	70	100
M17 CS 1111	Optical & Data Communications Laboratory	2	--	--	3	3	50	50	100
Total		**20**	**18**	**6**	**3**	**27**	**230**	**470**	**700**

	Course Code	Course
#ELE-1	M17 CS1105	Radar Signal Processing
	M17 CS 1106	Optical Communication Technology
	M17 CS 1107	Advanced Computer Networks
#ELE-2	M17 CS 1108	Wireless LANs and PANs
	M17 CS1109	Mobile Computing Technologies
	M17 CS 1110	Network Security & Cryptography

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M.TECH (COMMUNICATION SYSTEMS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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II-SEMESTER

Code No.	Name of the Subject	Credits	Lec-ture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
M17 CS 1201	RF Circuit Design	3	3	1	--	4	30	70	100
M17 CS 1202	Wireless Communications and Networks	3	3	1	--	4	30	70	100
M17 CS 1203	Image and Video Processing	3	3	1	--	4	30	70	100
M17 CS 1204	Software Defined Radio	3	3	1	--	4	30	70	100
#ELE-3	Elective-III	3	3	1	--	4	30	70	100
#ELE-4	Elective-IV	3	3	1	--	4	30	70	100
M17 CS 1211	Advanced Communications Laboratory	2	--	--	3	3	50	50	100
Total		**20**	**18**	**6**	**3**	**27**	**230**	**470**	**700**

	Course Code	Course
#ELE-3	M17CS1205	Soft Computing Techniques
	M17CS1206	Smart Antennas
	M17CS1207	Secure Communications
#ELE-4	M17CS1208	Optical Networks
	M17CS1209	Digital Signal Processors and Architectures
	M17CS1210	Internet Of Things

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M.TECH (COMMUNICATION SYSTEMS)
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III-SEMESTER

Course Code	Course	Scheme of Examination	C	Int	Ext	Total
M17 CS 2101	Comprehensive Viva-Voce	Viva-Voce	2	50	-	50
M17 CS 2102	Seminar-I	Oral Presentation	2	50	-	50
M17 CS 2103	Project Work Part-I	Review	16	50	-	50
		Total	**20**	**150**	**-**	**150**

1. The Viva-Voce for the Comprehensive Viva-Voce and Seminar-I shall be held with the Project Guide, PG coordinator, and Head of the Department. The marks shall be awarded in the ratio of 20, 10 and 20 Marks by the members respectively
2. Candidates can do their Project Work Part-I&II work within the department or in any industry/research organization for two semesters (i.e. 3rd and 4th semesters). In case of thesis done in an industry/research organization, one advisor (Guide) should be from the department and one advisor (Co-Guide) should be from the industry/research organization.
3. The Project Work Part-I should be submitted at the end of 3rd Semester and it will be evaluated through Review by a committee consisting of Head of the Department, PG coordinator and Project guide. The marks shall be awarded in the ratio of 20, 10 and 20 Marks by the members respectively.

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IV-SEMESTER

Course Code	Course	Scheme of Examination	C	Int	Ext	Total
M17 CS 2201	Seminar-II	Oral presentation	2	50	-	50
M17 CS 2202	Project Work Part-II	Viva-voce	18		100	100
		Total	**20**	**50**	**100**	**150**

1. The viva-voce for Seminar-II shall be held with the Project Guide, PG coordinator, and Head of the Department. The marks shall be awarded in the ratio of 20, 10 and 20 Marks by the members respectively.
2. A publication of a paper on the thesis work in a National/International Journal at the end of 4th semester is mandatory for the submission of thesis work.
3. The Project Work Part-II should be submitted at the end of 4th semester and it will be evaluated through Viva–Voce examination by a committee consisting of External Examiner, Head of the Department, Project guide and PG coordinator. The marks shall be awarded in the ratio of 40, 20, 20 and 20 Marks by the members respectively.

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Code: B17 EC 1201

ELEMENTS OF ELECTRONICS ENGINEERING
(Common to CSE & IT)

Lecture	**: 3 Periods**	**Int.Marks**	**: 30**
Tutorial	**: 1 Period.**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 3**

Course Objectives:

1. To give the exposure to the students on semiconductor physics of the intrinsic and extrinsic semiconductors and basics of P-N junction diode.
2. To give the exposure to the students on the basics of special purpose diodes like Zener diode, photo diode, LED, and tunnel diode and rectifier circuits using diodes.
3. To give the exposure to the students on basics of BJT, and transistor circuit configurations.
4. To give exposure to the students on transistor biasing and thermal stabilization.
5. To give exposure tothe students on the basics of JFET, MOSFET and FET biasing.

Course Outcomes:

After completion of the course the students will be able to

1. Understand the basic concepts of transport of charge carriers in semiconductors,drift and diffusion currents, physical structure,operation, V-I characteristics of semiconductor diode.
2. Understand the basic concepts ofspecial types of diodes like Zener Diode, LED, Photo Diode and tunnel diode, rectifier circuits with and without filters.
3. Understand the physical structure, operation, input and output characteristics of BJT in CE,CB,CC circuit configurations.
4. Understand the basic concepts oftransistor biasing and thermal stabilization.
5. Understand the physical structure, operation, characteristics and circuit models of JFET's and MOSFET's.

SYLLABUS

UNIT I: Semiconductors and P-N junction diode:
Intrinsic and extrinsic semiconductors, charge densities in semiconductors, Drift and Diffusion currents, Hall Effect, Mass action law.Basic operation and V-I Characteristics of semiconductor diode, Diode current equation, Avalanche breakdown and Zener breakdown phenomenon.

UNIT II: Special Diodes and Diode Rectifiers:
Zener Diode, LED, Photo Diode and tunnel diode, Half wave and Full wave Rectifiers- with and without filters, Bridge Rectifier,Expressions - Ripple factor,Efficiency, Capacitor filters

.

UNIT III: Bipolar Junction Transistor:
Introduction, construction, basic operation of npn and pnp transistors, Transistor circuit configurations- CE,CB, CC- Input and output Characteristics in various configurations.h-parameter model for transistor amplifier.*(Introductory Treatment only).*

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UNIT IV:Transistor Biasing and Thermal Stabilization:
Transistor Biasing, Thermal runaway, stabilization, Different methods of Biasing-Fixed Bias, collector feedback bias, self-bias, Bias compensation.

UNIT V: Field Effect Transistors: Junction field Effect Transistors (JFET)- JFET characteristics, JFET Parameters, Small Signal model of FET, Depletion and Enhancement type MOSFET's.

Text Books:

1. Electronic Device and Circuits by SanjeevGuptha, Dhanpatrai&Co.Pvt.Ltd.
2. Electronic Device and Circuits by K.Satya Prasad, VGS.

Reference Books:

1. Integrated Electronics- Millman&Halkias, TMH.

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Dr.G.P.Saradhi Varma
B.Tech (CSE), M.Tech (CSE), Ph.D (CSE)

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Estd.1980

SRKREC/BOS-2/ECE/2017-18

Date: 30.04.2018

To
Dr. A.M.Prasad
Professor – Department of ECE
JNTUK, Kakinada.

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

Dear Sir,

We wish to inform you that the meeting of the Joint Board of Studies meeting will be held on 08.05.2018 at 11-00 A.M in A.C. Auditorium.

We request you to kindly attend the meeting and give your expert advice in designing the curriculum to maintain quality in academics.

The Boards of Studies meetings will be conducted in respective departments as per the schedule mentioned below:

S.No.	BOARD	DATE	TIME
1	Civil Engineering	08-05-2018	2.00 P.M
2	Computer Science & Engineering	08-05-2018	2.00 P.M
3	Electronics & Communication Engineering	08-05-2018	2.00 P.M
4	Electrical & Electronics Engineering	08-05-2018	2.00 P.M
5	Information Technology	08-05-2018	2.00 P.M
6	Mechanical Engineering	08-05-2018	2.00 P.M
7	Combined Board of Studies in Engineering Chemistry/Engineering Mathematics/Engineering Physics / Humanities and Social Sciences	08-05-2018	2.00 P.M

The T.A / D.A. will be paid to all the external members as per the university guidelines. We request you to make it convenient to attend the meetings.

Yours faithfully,

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(AFFILIATED TO JNTU KAKINADA, KAKINADA)(RECOGNISED BY ALL INDIA COUNCIL FOR TECHNICAL EDUCATION, NEW DELHI)

Accredited by NAAC with 'A' Grade

Recognised as Scientific and Industrial Research Organisation

CHINNA AMIRAM (P.O) :: BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN: 534 204

Phones : Off : EPABX : 08816-223332 Ext.201
Direct : 08816-222748
Fax : 08816-224516
College Fax : 08816-229377
Mobile No : 98483 81818
Email : gpsvarma@gmail.com
principalsrkrec@gmail.com
principal@srkrec.ac.in
Website : www.srkrec.ac.in

Dr.G.P.Saradhi Varma
B.Tech (CSE), M.Tech (CSE), Ph.D (CSE)

PRINCIPAL

Estd.1980

SRKREC/BOS-2/ECE/2017-18

Date: 30.04.2018

To
Dr. B.V.Ramana Reddy
Professor & Dean
Guru Gobind Indra Prastha University
Dwarka, New Delhi

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

***

We wish to inform you that the meeting of the Joint Board of Studies meeting will be held on 08.05.2018 at 11-00 A.M in A.C. Auditorium.

We request you to kindly attend the meeting and give your expert advice in designing the curriculum to maintain quality in academics.

The Boards of Studies meetings will be conducted in respective departments as per the schedule mentioned below:

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7	Combined Board of Studies in Engineering Chemistry/Engineering Mathematics/Engineering Physics / Humanities and Social Sciences	08-05-2018	2.00 P.M

The T.A / D.A. will be paid to all the external members as per the university guidelines. We request you to make it convenient to attend the meetings.

Yours faithfully,

PRINCIPAL

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S.R.K.R. Engg. College
BHIMAVARAM-534 204

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Estd.1980

SRKREC/BOS-2/ECE/2017-18

Date: 30.04.2018

To
Dr. K. Padma Priya
Professor, Dept. of ECE
University College of Engineering Kakinada
JNTUK, Kakinada.

Dear Madam

Sub. S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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S.R.K.R. ENGINEERING COLLEGE Pin : 534 204 BHIMAVARAM

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Website : www.srkrec.ac.in

SRKREC/BOS-2/ECE/2017-18 Date: 30.04.2018

To
Dr. M.Chakravarty
Scientist G
DLRL, Hyderabad

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SRKREC/BOS-2/ECE/2017-18

Date: 30.04.2018

To
Dr. N.V.S.N.Sarma
Department of ECE
NIT-Warangal

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SRKREC/BOS-2/ECE/2017-18

Date: 30.04.2018

To
Prof. G.Sasibhushana Rao
Department of Electronics and Communication Engineering
Andhra University
Visakhapatnam - 530 003.

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SRKREC/BOS-2/ECE/2017-18

Date: 30.04.2018

To
Sri Rama Raju
Director, Envision
Jubilee Hills, Hyderabad-500033

Dear Sir,

Sub: S.R.K.R. Engineering College-Board of Studies Meeting-Invitation-Regarding

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SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE(A)
CHINNA AMIRAM :: BHIMAVARAM-534204
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Dt: 07-05-2018

CIRCULAR

This is to inform you that the Department of ECE has convened a meeting on 08-05-2018 at 2.30 PM in the ECE Department Library. In this connection, all the Board of Studies members are requested to attend the same.

Agenda:

1. Finalization of III year B.Tech. (R16) syllabus and scheme for ECE.
2. Finalization of II year (R17) syllabus and scheme for ECE.
3. Any other item.

Head of the Department
Head of ECE Department
S.R.K.R. Engg. College
BHIMAVARAM-534 204

C.C to:
1. The Members of Board of studies
2. Office file

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BHIMAVARAM-534 204.

8 BOS Meeting of ECE on 8-5-2018

The Board of Studies meeting for UG courses is conducted on 18-5-2018 at 2-30pm in ECE dept.

Venue: ECE dept Library

Agenda:

1. Finalization of III years Tech. (R16) Syllabus & scheme for ECE
2. Finalization of II year (R17) Syllabus & Scheme for ECE
3. Any Other item.

Members present:	Signature
1. Prof NVSN Sarma	
2. Prof. B.V. Ramana Reddy	
3 Prof K. Padma Raju, VCE, JNTUK	
4. SVN Narayana Rao	
5 Dr A.M. Prasad	
6. Prof. DVR Mohan	
7. Prof N. Venkateswarlu	
8. M. VIJAYA RAMA RAJU.	

M. Jagapathi Raju
PRINCIPAL
S.R.K.R. Engg. College
BHIMAVARAM-534 204.

9.	G.V.S. Padma Rao		
10.	BVSSN Raju		
11	S.S Mohan Reddy		
12.	A. Manjusha.	[M Tech] I year	Manjusha. A
13	T. Pavani	[M.Tech] I year	T. Pavani Varma.
14	V. Ramachandra Rao	[B-tech] III year	V. Ramachandra Rao.
15.	Sk. Anwar	[B.tech] III year	Sk. Anwar
16.	A. Manikanta Swamy	[B.Tech] III year	Mani
17	Prof. P. Subba Rao		
18	Dr. P. V. Ramana Raju.	Prof & HOD, ECE,	

Date:8-5-2018

Resolutions:

Syllabi for III Year B.Tech (R16) ECE Courses and II Year B.Tech (R17) ECE courses are finalized with the following recommendations from the members of Board of Studies.

1. Moocs-II courses are to be decided by an internal committee and a list of subjects is to be given from which III-year ECE students will be choosing as per their interest.
2. For each of III Year B.Tech ECE and II Year B.Tech ECE Subjects, names of two textbooks and two reference books are to be given.
3. For each chapter in the books, weblink is to be given
4. Unit wise reference is to be specified while specifying textbooks.
5. Every lab should have 2 design experiments which should be realized, tested, and simulated.
6. Syllabus is designed according to the curriculum feedback.

Any Other Item:

1. Faculty members of ECE with Ph.D. qualification are advised to register as supervisor for Ph.D. programmes in JNTUK.
2. ECE Department is advised to acquire latest equipment and softwares like SDR kits, spectrum analyzer (upto 3GHz), network analyzer (up to 12 GHz), power meter, USART & UART Kits, Arduino boards, Node MCU, Cadence tools & Mentor graphics etc.,
3. ECE Department is advised to upgrade IOT Lab & setup mobile communication Lab.

P. Nagaraju

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SCHEME OF INSTRUCTION & EXAMINATION

(Regulation R17)

III/IV B.TECH

(With effect from 2017-2018 Admitted Batch onwards)

Under Choice Based Credit System

ELECTRONICS AND COMMUNICATION ENGINEERING

II-SEMESTER

Code No.	Name of the Subject	Credits	Lecture Hrs	Tutorial Hrs	Lab Hrs	Contact Hrs/ Week	Internal Marks	External Marks	Total Marks
B17 EC 3201	Microprocessors and its Applications	3	3	1	--	4	30	70	100
B17 EC 3202	Microwave Engineering	3	3	1	--	4	30	70	100
B17 EC 3203	VLSI Design	3	3	1	--	4	30	70	100
B17 EC 3204	Digital Signal Processing	3	3	1	--	4	30	70	100
B17 EC 3205	Radar Engineering	3	3	1	--	4	30	70	100
#OE	OPEN ELECTIVE	3	3	1	--	4	30	70	100
B17 EC 3208	Microprocessors and Microcontrollers Lab	2	--	--	3	3	50	50	100
B17 EC 3209	VLSI Lab	2	--	--	3	3	50	50	100
B17 BS 3201	Employability Skills	1	--	3	--	3	30	70	100
B17 BS 3203	Advanced Coding	1	--	--	3	3	50	50	100
B17 BS 3206	IPR & PATENTS	--	--	2	--	2	--	--	--
Total		**24**	**18**	**11**	**9**	**38**	**360**	**640**	**1000**

OPEN ELECTIVE	B17EC3206	Microcontrollers
	B17CS3214	Oops through Java
	B17CS3215	Data Mining
	B17ME3210	Industrial Robotics
	B17EE3209	Power Electronics
	B17EC3207	Bio Medical Engineering
	B17CS3216	Artificial Neural Networks

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Code: B17EC2201

ELECTRONIC CIRCUIT ANALYSIS

Lecture	**: 3 Periods**	**Int.Marks**	**: 30**
Tutorial	**: 1 Period.**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 3**

Course Objectives:

The aim of this course is to

1. Understand the concept of multistage amplifiers and analyze them.
2. Learn the classification of feedback amplifiers and analyze them.
3. Compare voltage, power and tuned voltage amplifiers and analyze them.
4. Understand the principle of oscillator and analyze different types of sinusoidal oscillators.
5. Understand the concept and analyze applications of op-amp.

Course Outcomes:

After the completion of the course, students will be able to:

1. Know the equivalent circuit of multistage amplifier and its analysis. [K3]
2. Identify the different feedback topologies and analyze them. [K1]
3. Explain the principle of oscillator and design different types of sinusoidal oscillators. [K3]
4. Define the difference between voltage and power amplifiers and design different classes and know that Tuned amplifiers amplify a narrow band of frequencies and will also be able to analyze them.[K1,K2, K3]
5. Identify that Op-amp not only amplifies but also performs different operations and analyze some of its applications.[K1,K2]

SYLLABUS

UNIT – I: Multistage Amplifiers

Transistor at high frequencies, CE short circuit current gain and concept of Gain Bandwidth product. BJT and FET RC coupled amplifiers at low and high frequencies. Frequency response and calculation of Band Width of Multistage Amplifiers.

UNIT – II: Feed Back Amplifiers

Concept of Feed Back Amplifiers - Effect of Negative Feedback on the amplifier characteristics. Four feedback topologies, Method of analysis of Voltage Series, Current Series, Voltage Shunt and Current Shunt feedback Amplifiers.

UNIT – III: Sinusoidal Oscillators

Condition for oscillations and types of Oscillators – RC Oscillators: RC Phase Shift and Wien bridge Oscillators. LC Oscillators: Hartley, Colpitts, Clapp, Tuned Collector and Crystal Oscillators.

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UNIT – IV: Power and Tuned Voltage Amplifiers

Classification of Power Amplifiers. Series fed, Transformer coupled class-A and class-B power amplifiers. Push Pull Class-A, Class-B and Class-AB Power Amplifiers. Cross-over Distortion in Pure Class-B Power Amplifier and Class-AB Power Amplifier- Trickle Bias, Derating Factor and Heat Sinks – Complementary Push Pull Amplifier. Analysis of Single tuned, Double tuned and Stagger Tuned Amplifiers with gain and Bandwidth Calculations.

UNIT – V: Operational Amplifiers

Concept of Differential Amplifier. Differential Amplifier supplied with a constant current source. Calculation of common mode rejection ratio. Block diagram and Ideal characteristics of an Op-Amp. Applications of Op-Amp: Inverting and Non-Inverting amplifiers, Integrator, Differentiator, Summing, Subtracting and Logarithmic Amplifiers. Definition and Measurement of OP-Amp Parameters.

Text Books:

1. Integrated Electronics- Millman and Halkias.
2. Op-amps and Linear Integrated Circuits – Gayakwad.

Reference Books:

1. Electronic Devices and Circuits – Mottershead.
2. Electronic Devices and Circuits by Salivahanan. Tata McGraw-Hill pub.

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Code: B17 EC 3202

MICROWAVE ENGINEERING

Lecture	**: 3 Periods**	**Int. Marks**	**: 30**
Tutorial	**: 1 Period**	**Ext. Marks**	**: 70**
Exam	**: 3 Hrs.**	**Credits**	**: 3**

Course Objectives:

1. The purpose of this course is to provide the operational characteristics and conceptual understanding of active and passive components at microwave frequencies.
2. This course also emphasizes formulation and application of scattering matrix for the analysis of different microwave passive components.
3. Further, this course also provides the understanding of measurement techniques of different parameters.

Course Outcomes: By the end of the course the learners (students) will be able to

1. Explain the working principle of different passive waveguide components used atmicrowave frequencies.
2. Apply the properties of scattering matrix for solving the scattering matrix of different passive microwave components for both ideal and practical considerations and analyse their operation.
3. Understand the conceptual and operational characteristics of different microwave Tube circuits.
4. Explain the operational characteristics of different microwave solid state devices.
5. Understand and implement different experimental procedures involving measurement of microwave parameters

SYLLABUS

UNIT-I: Microwave Components and its applications:
Introduction, Microwave Spectrum and Bands, Applications of Microwaves, Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide irises, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Resistive Card, Rotary Vane types; Waveguide Phase Shifters – Dielectric, Rotary Vane types, E-plane and H-plane Tees, Magic Tee, Hybrid Ring; Directional Couplers – 2Hole, Bethe Hole types, Ferrite Components– Faraday Rotation, Gyrator, Isolator, Circulator, Related Problems.

UNIT-II: Scattering Matrix:
Scattering Matrix – Significance, Formulation and Properties, Scattering Matrix of Isolator, circulator, directional coupler, E Plane Tee, H plane Tee and Magic Tee.

UNIT-III:Qualitative treatment on Microwave Tubes:
Limitations and Losses of conventional tubes at microwave frequencies.Re-entrant Cavities,Microwave tubes – O type and M type classifications. O-type tubes :2 Cavity Klystrons – Structure, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory, Applications, Reflex Klystrons – Structure, Applegate Diagram and Principle of working, Electronic Admittance; Electronic and Mechanical Tuning, Applications, Related Problems.

P. Nagaraju

PRINCIPAL
S.R.K.R. Engg. College
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HELIX TWTS: Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT (Qualitative treatment).
M-type Tubes Introduction, Cross-field effects, Magnetrons – Different Types, 8-Cavity Cylindrical Travelling Wave Magnetron – Hull Cut-off Condition, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics.

UNIT-IV: Microwave Solid state Devices:
Negative resistance phenomenon, Gunn Diode, domain formation, Tunnel Diode- principle of operation, IMPATT- principle of operation, TRAPATT, PIN Diodes and its applications (Qualitative analysis only).Detector diode or point contact diode and its characteristics.

UNIT-V:Microwave Measurements:
Microwave Test bench, Measurement of Power, VSWR, Frequency, Guide Wavelength, Unknown load impedance, S parameters of reciprocal and non reciprocal devices

Text Books:

1. Foundations for Microwave Engineering, R. R. Collin, McGraw Hill.
2. Microwave Devices and Circuits, Third Edition, Samuel Y. Liao, Pearson Education.

Reference Books:

1. Microwave Engineering, Annapurna Das, Sisir K. Das, Tata McGraw-Hill Education
2. Microwave Engineering, 4th Edition, David M. Pozar, November 2011.
3. Microwave and Radar Engineering, GottapuSasibhushanaRao, Pearson Education, New Delhi, 2014.
4. Microwave and Radar Engineering-M.Kulkarni, Umesh Publications, 3rd Edition.

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S.R.K.R. Engg. College
BHIMAVARAM-534 204.

Code: B17 EC 2107

ELECTRONICS DEVICES AND CIRCUITS LAB
(Common to ECE & EEE)

Lab : 3 Periods	**Int.Marks : 50**
Exam : 3Hrs.	**Ext. Marks : 50**
	Credits : 2

Course objectives:

1. To familiarize the students with various passive and active components like resistors, capacitors, inductors ,semiconductor diodes, Zener diodes, LEDs, BJTS, JFETs and UJTs.
2. To familiarize the students with operation of CROs , function generators and bread boards.
3. To observe and analyze the characteristics of devices like diodes, BJTs & FETs.
4. To analyze the behavior of BJT and JFET amplifiers.

Course outcomes:

At the end of the semester students should be able to

1. Design and fabricate simple circuits like diode rectifiers with filters for providing dc voltages in electronic circuits.
2. Design and fabricate amplifiers with required gain for use in various communication applications.
3. Design and fabricate simple electronic circuits for everyday applications like traffic control lights using relays, automatic counters using LDRs and Burglar alarms.

ELECTRONIC WORKSHOP PRACTICE

1. Identification ,Specifications and testing Of R,L,C components, colour codes, potentiometers, coils and bread boards
2. Identification ,Specifications and testing of devices like diodes, BJTs, JFETs, SCR and UJT.
3. Soldering of Simple Circuits using Active &Passive Components.
4. Study and operation of Transformers, Ammeters(Analog & Digital),Voltmeters(Analog &Digital) , Analog and Digital Multimeters and Function Generators, Regulated Power Supply, Decade Resistance, Inductance &Capacitance Boxes And CRO.

LIST OF HARDWARE EXPERIMENTS:

1. V-I Characteristics Of Semiconductor Diode (Ge& Si), LED and Zener Diode
2. Half Wave And Full Wave Rectifier With And Without Filter
3. Characteristics Of BJT In CE Configuration
4. JFET Characteristics
5. Transistor Biasing Circuits And Transistor As Switch
6. CE Amplifier
7. JFET Common Source Amplifier

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LIST OF SIMULATION EXPERIMENTS

1. Simulation of V-I Characteristics Of Semiconductor Diode, LED and Zener Diode
2. Simulation of Regulation Characteristics Of ZENER Diode
3. Simulation of CC Amplifier
4. Simulation of JFET Characteristics
5. Simulation of BJT Characteristics In CB Configuration
6. Simulation of JFET Amplifier
7. Simulation of UJT Characteristics

NOTE: (Minimum of Twelve Experiments Should Be Conducted)

Reference Books:

1. Integrated Electronics: Analog and Digital Circuits and Systems: Jacob Millman, C Halkias, Chetan D Parikh. McGraw – Hill.

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Code: B17 EC4109

INTERNET OF THINGS LAB

Lab : 3 Periods | **Int. Marks : 50**
Exam : 3Hrs. | **Ext. Marks : 50**
Credits : 2

Course Objectives: Students should learn

1. To design a IOT application prototypes with the knowledge of IOT.
2. This lab course enables students to get practical experience in interfacing IOT Modules with cloud.

Course Outcomes: On the completion of this course, the students will be able to:

S. No	Out Come	Knowledge Level
1	Able to acquire knowledge on interfacing different sensors and communication modules with the System on Chip Modules.	K3
2	Able to connect SOC devices with the cloud for accessing and analyzing the data.	K4

LIST OF EXPERIMENTS

1. Introduction to Aurdino and raspberry-pi and its applications like interfacing LED and Buzzer
2. Interfacing Push Button and DHT Sensors
3. Introduction to Communication Modules like IR and Bluetooth.
4. Interfacing Ultrasonic sensor
5. Interfacing OLED to display text and received data from sensors
6. Interfacing with TSL2561(Luminosity Sensor)
7. Establishing Serial Communication
8. Creating account in cloud and pushing data to cloud
9. MQTT Client Publish and MQTT Client subscribe
10. Assignment (Design Application)
11. Assignment (Design Application)
12. Assignment (Design Application)

Reference Books:

1. Lab Manual

Web Links:

1. http://www.etilabs.com/products/iot-board/

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