SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE (A)

China Amiram, Bhimavaram, Andhra Pradesh- 534204

VALUE ADDED COURSES

INDEX

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE (Autonomous)
CHINNAAMIRAM, BHIMAVARAM–534204,
ANDHRA PRADESH

INVITATION

"Introduction to MS Office Course" Organized by Department of Civil Engineering

Resource Persons
Mr. E. Ramanjaneya raju
Mr. M. S. K. Chaitanya

Venue: R101

Participants : 2/4 Civil Students

Course Contents:
1. Introduction to computers
2. Memories
3. MS-Word
4. MS-Excel
5. Microsoft PowerPoint

For Registrations:
Contact: Mr. E. Ramanjaneya Raju,
Assistant Professor, Civil Department

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE
(AUTONOMOUS)
CHINNA AMIRAM (P.O) :: BHIMAVARAM :: W.G. Dt., A.P., INDIA :: PIN: 534 204
DEPARTMENT OF CIVIL ENGINEERING

Date: 19-06-2017

Circular

It is hereby informed that the students of 2/4 Civil Engineering are instructed to enroll for the course titled "Introduction to MS Office" in the Department and are directed to approach the faculty listed below for further instructions on or before 21-06-2017. The details of the course are enclosed.

S.NO	Name of the Faculty
1	Sri E.Ramanjaneya Raju
2	Sri M.S.K Chaitanya

Head of the Department
HEAD
Dept. of Civil Engg.
S.R.K.R. Engg. College
CHINA AMIRAM
BHIMAVARAM-534 204.

Introduction to MS Office

Course Contents:

1. Introduction to computers
2. Memories
3. MS-Word
4. MS-Excel
5. Microsoft PowerPoint

Course Objectives:

1. Basics of the computer hardware and its functions
2. Basics of the computer software programs and its functions
3. MS-Word applications
4. MS-Excel applications
5. MS-Power point applications

Course Outcomes:
After successful completion of the course, the student shall be able to:

1. Know the fundamentals of computer hardware and software
2. Apply the MS Word for practical applications
3. Apply the MS Excel for practical applications
4. Apply the MS Power point for practical applications

Course Schedule:

The course is scheduled on tuesday and thursday from 1:45 to 3:25 PM, wednesday from 1:45 to 5:05 PM w.e.f 19-06-2017..

Head of the Department
HEAD
Dept. of Civil Engg.
S.R.K.R. Engg. College
CHINA AMIRAM
BHIMAVARAM-534 204.

SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (Autonomous)

CHINA AMIRAM. (P.O): BHIMAVARAM: W.G.DT., A.P., INDIA: PIN: 534202

"Introduction to Machine Learning using Python"

Organised by
Department of Mechanical Engineering

Resource Persons
Dr. K. Sita Rama raju, Associate Professor,
Mechanical Department
Sri. I. Ajit Kumar, Assistant Professor,
Mechanical Department

Participants
¾ Mechanical Students
Venue: Mechanical Department Seminar Hall
Duration:08-07-2017 to 08-10-2017 (2 hours per
week i.e 1:45 to 3:25PM)

For registration:
Contact: Sri. I. Ajit Kumar, Assistant Professor,
Mechanical Department

Dt: 03-07-2017

Circular

The course-Introduction to Machine learning using Python (IMLP) will be offered to students of mechanical engineering from 08-07-2017 onwards. This course will highly help the students to gain knowledge on python programming language and understand basics of machine learning. Learning this course will enhance the placement opportunities for the students.

Course Contents:

Introduction to Python: About Python, History, Features of Python, Variables, Data Types, Operations, Operators, FOR loops, IF loops, WHILE loops, Python Classes, and Python Methods

NumPy Arrays, Pandas and Matplotlib: NumPy arrays, Array creation, Indexing and slicing, Lists, Tuples, Dictionary, Sets, Data Frame: Reading and Writing a Data Frame, Creating and Extracting Features, Conversion of Categorical Data to Numerical Data, Merging Data Frames.
Data Visualization: Use of Matplotlib Library for Various Plots like Scatter, Bar, Histogram plots, Introduce Various Correlation Techniques.

Introduction to Machine Learning: Introduction to Artificial Intelligence (AI), Machine Learning and Deep Learning, Types of Machine Learning: Supervised, unsupervised & Reinforced Learning, Machine Learning Pipeline: Loading, Pre-processing, Normalizing of Data, Train and Test Split, Evaluation methods.
Supervised Algorithms: Regression- Simple Linear, Multiple Linear, Polynomial, Logarithmic Quadratic, Exponential, Sigmoidal Regression. Classification: Decision Tree, K-Nearest Neighbour, Logistic and Support Vector Machine classifiers. Unsupervised Learning: K-means Clustering, Hierarchical Clustering and DBSCAN

Building methods: Building of best machine learning model for 4 different real data

Course Outcomes:
- Understand fundamentals of python programming
- Acquire in-sights into Numpy, Pandas & Matplotlib
- Understand the importance of machine learning
- Differentiate supervised & unsupervised learning

PRINCIPAL
S.R.K.R. Engg. College
BHIMAVARAM-534 204

Professor & Head
Dept. of Mechanical Engg.
S.R.K.R. Engineering College
CHINA AMIRAM (P.O.)
BHIMAVARAM-534 204.

Reference Books:

1. Phuong Vo.T.H , Martin C, Getting Started with Python Data Analysis, Packt Publishing Ltd.
2. Charles Severance, Python for Everybody: Exploring Data in Python.
3. Oliver Theobald, Machine Learning with Python: A Practical Beginner's Guide, Scatterplot Press
4. Peter Harrington; Machine Learning in Action, Manning Publications Co.

Course schedule: Saturday 1:45-3:25 PM

1.
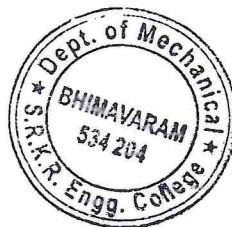
2.

3.

Course faculty

Head of the Department

Professor & Head
Dept. of Mechanical Engg.
S.R.K.R. Engineering College
CHINA AMIRAM (P.O.)
BHIMAVARAM-534 204.

CC to:

Department Notice board

PRINCIPAL
S.R.K.R. Engg. College
BHIMAVARAM-534 204

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE (A)

CHINNA AMIRAM, BHIMAVARAM-534204

THE ART OF LIVING

YES!+ Accelerating Excellence

YES+ (YOUTH EMPOWERMENT SKILLS)

Organized by

PAIE Cell, SRKREC

Course Content:
- Teams and guidelines
- three questions for reflections
- seven levels of existence
- Four sources of energy
- Importance of breath

Resource Persons
- B. Sudarsan
- P. Ramesh Raju
- Ch. Keerthi

PARTICIPANTS : 1/4 B. Tech Students
Venue: I Block
Date: 13-07-2017
Registrations: Dr. P. Bhavani,
In Charge PAIE Cell, Assistant Professor
Department of Engineering Chemistry

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE(A)
CHINNA AMIRAM (P.O) :: BHIMAVARAM:: W.G.Dt., A.P., INDIA::PIN:534204

CIRCULAR

Date: 11/07/ 2017

This is to inform all the students of I/IV B.Tech to enrol for the course entitled "YES+" in the college under directed to approach the faculty listed below for further guidance and instructions on or before 13/07/2017

S.NO	Name of the Faculty
1	B.Sudarshan
2	P.Ramesh Raju
3	Ch. Keerthi

P. Bhav

Dr. P. Bhavani

(In-charge, PAIE Cell)

INCHARGE

PAIE

Course Contents:

1. Teams and Guidelines.
2. Three Questions for Reflection.
3. Seven Levels of Existence.
4. Four Sources of Energy.
5. Importance of Breath.
6. Sudarshan Kriya

Course Objectives:

Students Will learn

1. Pranayama.
2. Bhastrika.
3. Ujjayi Breath.
4. Three Stage Breathing.
5. Sudarshan Kriya.
6. Seven Levels of Existence.

Course Outcomes:

Students should be capable of

1. Practice Pranayama & Sudarshan Kriya.
2. Knowledge on Breath.
3. Meditation.
4. He will Explore His Full Potential.
5. Time Management.
6. Team Work.

Course schedule:

The course is scheduled for every 1st year Student from Monday to Saturday in the INDUCTION PROGRAM.

Course Faculty

Course Coordinator
INCHARGE

PAIE

Principal
PRINCIPAL
S.R.K.R. Engineering College
(Autonomous)
China Amiram, Bhimavaram-534 204.

Course schedule: YES+ course

From 11-07-2017 to 29-07-2017

Venue: I-Block, Department seminar Halls

For all branches

Day	Time
Monday	9:30am to 12:00pm
Tuesday	9:30am to 12:00pm
Wednesday	9:30am to 12:00pm
Thursday	9:30am to 12:00pm
Friday	9:30am to 12:00pm
Saturday	9:30am to 12:00pm

INCHARGE

P.Bhavz

PAIE

INVITATION

SRKR Engineering College (A)

PAIE Cell

Cordially Invite you to join

Inner Engineering

Content : **Pathanjali Yoga Sutras**
Human Values
Creative Arts
Service

Participants: I/IV B.Tech Students

Start Date : 27-07-2017   Venue: I Block

Resource Persons:

Sri. B. Sudhrshan

Sri. P. Ramesh Raju

FOR REGISTRATION

Dr. P.Bhavani, Asst. Professor, InCharge-PAIE Cell

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE(A)
CHINNA AMIRAM (P.O) :: BHIMAVARAM:: W.G.Dt., A.P., INDIA::PIN:534204

CIRCULAR

Date: 25/07/2017

This is to inform all the students of I/IV B.Tech to enrol for the course entitled
"INNER ENGINEERING " in the college is directed to approach the faculty listed below for
further guidance and instructions on or before 27/07/2017.

S.NO	Name of the Faculty
1	B.Sudarshan
2	P.Ramesh Raju

P.BL

Dr. P. Bhavani

(In-charge, PAIE Cell)
INCHARGE

PAIE

INNER ENGINEERING

Course Contents:
1. Patanjali yoga sutras
2. Human values
3. Creative arts
4. Service

Course Objectives:

Students should learn

1. Human values and tools to lead a happy, stress-free life.
2. Yoga asanas, Sudarshan Kriya, Pranayama & Meditation
3. At- least two creative arts out of Photography, Sketching, craft-making, singing, clay moulding, upcycling, etc.
4. Concentration Pranayama and Ego bursting process.
5. To take responsibility for society and teach classes of their choice to school children.
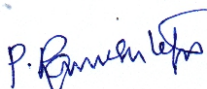6. About good food habits for good health.

Course Outcomes:

Students should be capable of

1. To improve his concentration levels and improve his public speaking abilities.
2. To his academic and non-academic activities(Work-Life balance)
3. To widen his vision and increase his breadth of perspective in his journey of 4 years.
4. To improve his communication skills, leadership skills, teamwork & decision-making skills.
5. To inculcate Creativity and innovation, planning, and organizing as part of their life.
6. Taking Responsibility for themselves and the people around them.
7. To make their journey more fun and enjoyable.

Course schedule:

The course is scheduled for CIVIL, EEE, and MECH In the first semester. The course is scheduled for CSE, ECE, and IT for the second semester.

Course Faculty

Course Coordinator
INCHARGE

PAIE

Principal
**PRINCIPAL
S.R.K.R. Engineering College
(Autonomous)
China Amiram, Bhimavaram-534 20:**

Course schedule: Inner Engineering

w.e.f 02-08-2017

Venue: S-Block

S.No	Section	Time
	I Semester	
1	Civil-A	3:25pm to 5:05 Monday
2	Civil -B	3:25pm to 5:05 Tuesday
3	Civil -C	3:25pm to 5:05 Wednesday
4	Civil -D	3:25pm to 5:05 Thursday
5	Mech-A	3:25pm to 5:05 Friday
6	Mech-B	3:25pm to 5:05 Monday
7	Mech-C	3:25pm to 5:05 Friday
8	Mech-D	3:25pm to 5:05 Thursday
9	EEE-A	3:25pm to 5:05 Tuesday
10	EEE-B	3:25pm to 5:05 Wednesday
11	EEE-C	3:25pm to 5:05 Monday
	II Semester	
1	ECE-A	3:25pm to 5:05 Monday
2	ECE-B	3:25pm to 5:05 Tuesday
3	ECE-C	3:25pm to 5:05 Wednesday
4	CSE-A	3:25pm to 5:05 Thursday
5	CSE-B	3:25pm to 5:05 Friday
6	CSE-C	3:25pm to 5:05 Monday
7	CSE-D	3:25pm to 5:05 Friday
8	IT-A	3:25pm to 5:05 Thursday
9	IT-B	3:25pm to 5:05 Tuesday
10	IT-C	3:25pm to 5:05 Wednesday

INCHARGE

P. Bhavan

PAIE

S.R.K.R ENGINEERING COLLEGE (AUTONOMOUS)
CHINNA AMIRAM, BHIMAVARAM-534204
ANDHRA PRADESH

INVITATION

"LABVIEW GRAPHICAL PROGRAMMING"

ORGANIZED BY

DEPARTMENT OF E.C.E INASSOCIATION WITH LAB VIEW ACADEMY

PARTICIPANTS

ECE AND EEE STUDENTS

DURATION

BATCH1:01-09-2017 TO 05-09-2017 (8 HOURS PER DAY)

BATCH2:07-09-2017 TO 11-09-2017 (8 HOURS PER DAY)

FOR REGISTRATIONS:

CONTACT:Dr.B.V.S.S.N.RAJU, NI COORDINATOR

PROFESSOR,E.C.E DEPARTMENT

S.R.K.R. ENGINEERING COLLEGE: CHINNAAMIRAM::BHIMAVARAM

CIRCULAR

Date: 02-11-2017.

All 2/4 B.Tech ECE and EEE students are hereby informed that the department of ECE in association with NI LabVIEW Academy is organizing LabVIEW Training Program. All the interested students are directed to approach Dr. B.V.S.S.N Raju, Professor, ECE Department to enroll for the program. The **LabView Graphical Programming** training is scheduled from 01-09-2017 to 11-09-2017. The course details are provided below:

Course Description

A study of computer-assisted measurement and automation techniques. Students receive hands-on experience in measuring and controlling physical phenomena through laboratory exercises and projects.

Course Outcomes

Upon successful completion of this course, students will have mastered the foundational principles of LabVIEW including topics such as:

- LabVIEW programming principles
- Data types, software constructs, and Graphical User Interface (GUI) elements
- Variables and functions
- Simple design patterns
- SubVI design
- VI design and documentation
- Error handling
- Debugging tools and techniques.

Schedule

S.NO	Training Module	Dates	Duration
1	LabVIEW Graphical Programming	01-09-2017 to 05-09-2017	8 Hrs Per Day
2	LabVIEW Graphical Programming	07-09-2017 to 11-09-2017	8 Hrs Per Day

Signature of NI-Coordinator

B. V.S.S.N. RAJU, M.E.
Professor in E.C.E
S R K R Engg. Coll...

Head of ECE Department
S.R.K.R. Engg. College
BHIMAVARAM-534 204

HEAD OF THE DEPARTMENT

C.C. to:

Department Notice Board
Master file.

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE
(Autonomous)
CHINNA AMIRAM, BHIMAVARAM-534204,
ANDHRA PRADESH

INVITATION

"Verilog HDL Course"
Organized by
Department of E.C.E

Resource Person
Ms. K. Bala Sindhuri
Mr. K. Yugandhar
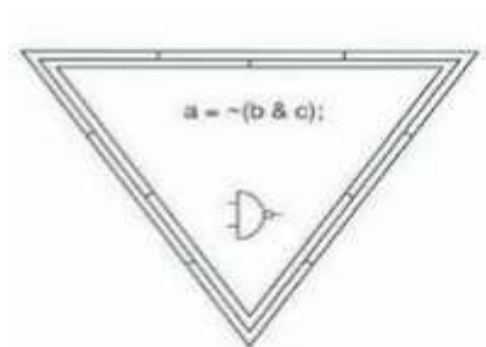**Venue: ECE-Seminar Hall**

Participants

2/4 ECE Students

Duration: 22-11-2017 to 17-03-2018 (2 hours per week)

For Registrations:

Contact: Ms. K. Bala Sindhuri, Asst. Professor, ECE Department

a = ~(b & c);

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE
(AUTONOMOUS)
Department of Electronics and Communication Engineering

Date: 20-11-2017

Circular

It is hereby informed that students of 2/4 ECE are instructed to enroll for the course titled **"Verilog HDL"** and are directed to approach the faculty members **Ms. K. Bala Sindhuri, Mr. K. Yugandhar**, Assistant Professor, Department of ECE for further instructions on or before 21-11-2017. The Program will commence from 22-11-2017. All the students must attend the course without fail. The course details are provided below:

Course Content

| Module-1 | Overview of Digital Design with Verilog HDL | 10 hours |

Overview of Digital Design with Verilog HDL

Evolution of CAD, emergence of HDLs, typical HDL-flow, why Verilog HDL?, trends in HDLs.

Hierarchical Modeling Concepts

Top-down and bottom-up design methodology, differences between modules and module instances, parts of a simulation, design block, stimulus block.

| Module-2 | Basic Concepts | 10 hours |

Basic Concepts

Lexical conventions, data types, system tasks, compiler directives.

Modules and Ports

Module definition, port declaration, connecting ports, hierarchical name referencing.

| Module-3 | **Gate-Level Modeling** | **10 hours** |

Gate-Level Modeling

Modeling using basic Verilog gate primitives, description of and/or and buf/not type gates, rise, fall and turn-off delays, min, max, and typical delays.

Dataflow Modeling

Continuous assignments, delay specification, expressions, operators, operands, operator types.

| Module-4 | **Behavioral Modeling** | **10 hours** |

Behavioral Modeling

Structured procedures, initial and always, blocking and non-blocking statements, delay control, generate statement, event control, conditional statements, Multiway branching, loops, sequential and parallel blocks

| Module-5 | **Introduction to VHDL** | **10 hours** |

Introduction to VHDL

Introduction:

Why use VHDL?, Shortcomings, Using VHDL for Design Synthesis, Design tool flow, Font conventions.

Entities and Architectures:

Introduction, A simple design, Design entities, Identifiers, Data objects, Data types, and Attributes.

Course Objectives:

This course teaches:

• Designing digital circuits, behavioral and RTL modeling of digital circuits using Verilog HDL.

• Students gain practical experience by designing, modeling, implementing and verifying several digital circuits.

This course aims to provide students with the understanding of the different technologies related to HDLs, construct, compile and execute Verilog HDL programs using provided

software tools. Design digital components and circuits that are testable, reusable and synthesizable.

Course outcomes

At the end of this course, students should be able to

- Write Verilog programs in gate, dataflow (RTL), behavioral and switch modeling levels of Abstraction.
- Write simple programs in VHDL in different styles.
- Design and verify the functionality of digital circuit/system using test benches.
- Identify the suitable Abstraction level for a particular digital design.
- Write the programs more effectively using Verilog tasks and directives.
- Perform timing and delay Simulation.

Schedule:

S.No	Day	Time	Name of the faculty	Section
1.	Saturday	2.00 P.M to 4.00 P.M	Mrs. K. Bala Sindhuri	A & B
2.	Thursday	5.05 P.M to 6.05 P.M	Mr. K. Yugandhar	C & D
3.	Friday	5.05 P.M to 6.05 P.M	Mr. K. Yugandhar	C & D

Venue: ECE Seminar Hall

Signature of the Faculty Members:

K.B.sindhuri
1.Ms. K. Bala Sindhuri
Assistant Professor
Department of ECE
S.R.K.R. Engineering College
BHIMAVARAM-534 204

2.Mr. K. Yugandhar
Assistant Professor
Department of ECE
S.R.K.R. Engineering College
Bhimavaram-534 204

Head of ECE Department
S.R.K.R. Engg. College
BHIMAVARAM-534 204

C.C. to:
Department Notice Board
Master file.

S.R.K.R ENGINEERING COLLEGE (AUTONOMOUS)
CHINNA AMIRAM, BHIMAVARAM-534204
ANDHRA PRADESH

INVITATION
"MYDAQ TECHNICAL PROGRAM"
ORGANIZED BY
DEPARTMENT OF E.C.E INASSOCIATION WITH LAB VIEW ACADEMY

PARTICIPANTS

ECE AND EEE STUDENTS

DURATION

BATCH1:27-11-2017 TO 01-12-2017 (8 HOURS PER DAY)

BATCH2:04-12-2017 TO 08-12-2017 (8 HOURS PER DAY)

FOR REGISTRATIONS:

CONTACT:Dr.B.V.S.S.N.RAJU, NI COORDINATOR
PROFESSOR,E.C.E DEPARTMENT

S.R.K.R. ENGINEERING COLLEGE: CHINNA AMIRAM::BHIMAVARAM

CIRCULAR

Date: 02-11-2017.

All 2/4 B.Tech ECE and EEE students are hereby informed that the department of ECE in association with NI LabVIEW Academy is organizing **myDAQ** from 27-11-2017 to 08-12-2017. All the enrolled students previously for NI LabView training program are directed to approach Dr. B.V.S.S.N Raju, Professor, ECE Department for further instructions. The course details are provided below:

Course Contents

- Interactively acquire and analyze single-channel and multi-channel data from NI DAQ devices and non-NI instruments
- Create user interfaces with charts, graphs, and buttons
- Use programming structures, data types, and the analysis and signal processing algorithms in LabVIEW
- Debug and troubleshoot applications
- Log data to file
- Use best programming practices for code reuse and readability
- Implement a sequencer using a state machine design pattern

Schedule

S.NO	Training Module	Dates	Duration
1	myDAQ	27-11-2017 to 01-12-2017	8 Hrs Per Day
2	myDAQ	04-12-2017 to 08-12-2017	8 Hrs Per Day

Signature of NI-Coordinator

(B V S S N Raju)

B V.S.S.N. RAJU. M E.

Professor in E C E

Head of ECE Department
S.R.K.R. Engg. College
BHIMAVARAM-534 204

HEAD OF THE DEPARTMENT

C.C. to:

Department Notice Board
Master file.