

## Faculty Profile

Name	A.V.S.SURYA VARMA
Date of Birth	29/05/1985
Unique ID (AICTE ID)	1-758273354
Education Qualifications	M.Tech
Work Experience	11 years
a) Teaching	11 years
b) Research	--
c) Industry	--
d) Others	--
Area of Specialization	VLSI Design
Courses taught at Under Graduate/ Post Graduate Level	<ul style="list-style-type: none"> <li>• Basic electronics</li> <li>• Digital Logic Design</li> <li>• Switching Theory and Logic Design</li> <li>• Electronic Devices and Circuits</li> <li>• Digital IC Applications</li> <li>• VLSI Design</li> <li>• Cellular Mobile Communication</li> <li>• Linear Integrated circuits and Applications</li> </ul>
Research guidance	
a. No. of papers published in National/ International Journals/ Conferences	--
b. Master (No. of Completed/Ongoing)	--
c. Ph.D. (No. of Completed/Ongoing)	--
No. of Projects Carried out	--
No. of Patents (Filed & Granted)	01
Technology Transfer	--
Research Publications (No. of papers published in National/International Journals/Conferences)	8
No. of Books published	--

