Faculty Profile

Name	Kurella Pavan Kumar
Date of Birth	08 June 1990
Unique ID (AICTE ID)	1-3548504463
Education Qualifications	Pursuing (PhD) from NIT Goa Master of Engineering in Control Systems
Work Experience	
a) Teaching	7Years 10months
b) Research	5 years, in DST Sponsored Project "Demonstration of Grid Supportive EV Charger and Charging Infrastructure at L.T Level (D- EVCI)" in collaboration with IIT Delhi from 2nd January 2019 to till date
c) Industry	
d) Others	AICTE IDEALab Co-Coordinator and IIC Innovation Coordinator of SRKR Engineering College
Area of Specialization	Control Systems, Cyber-Physical Systems
Courses taught at Under Graduate/ Post Graduate Level	Post Graduate Level: Real-Time Control of Power Systems Under Graduate Level: Electrical Vehicles,
	Digital Signal Processing, Microprocessors & Microcontrollers, Circuit Theory, Introduction Electrical Systems, Control Systems, Electrical Technology, Power Systems -1, Digital Logic Design, Python Programming, Smart Systems Lab, Solar Energy & Systems Lab, Electrical Vehicle Technology Lab
Research guidance	-
a. No. of papers published in National/ International Journals/ Conferences	-
b. Master (No. of Completed/Ongoing)	-
c. Ph.D. (No. of Completed/Ongoing)	-
No. of Projects Carried out	1 Co-PI for DST Sponsored Project "Demonstration of Grid Supportive EV Charger and Charging Infrastructure at L.T Level (D-EVCI) in collaboration IIT Delhi

No. of Patents (Filed & Granted)	-
Technology Transfer	
	As a Resource Person for FDPs/
	Workshops: 3
	• FDPS/ Workshops attended: 21
	 NPTEL Courses Completed:13
	• Internship Training for Students
	under DST Project: 12 Students
	completed and ongoing with another 18 Students
	 Internship Training for Students acts
	as a mentor on Cyber-Physical
	Systems and Cyber Security with the
	application of Reinforcement
	Learning
	• Internship Training for Students acts
	as a mentor for the project related to
	developing a IoT system that
	addresses a critical problem in aquaculture which is a power cut or
	Power Failure
	• Qualified UGC_NET Junior Research
	Fellow(JRF) & UGC_NET
	ASSISTANT PROFESSOR in
	Electronic Science (Nov 2017)
	• Got GATE Rank of 2395(Score:592) in
	the year 2021
	• Got NPTEL Discipline Star Certificate
	in Electrical Engineering Discipline in
	July-Dec 2021
	Mentored and participated in the final
	competition of Smart India Hackathon-
	2022(25th -26th August 2022) at
	Manipal University, Jaipur for the
	problem statement BV806 -"Suggest a
	redefined solution to help cities locate
	EV(Electric Vehicles) charging
	infrastructure" and got 2nd Position.
	• Mentored the project titled "Cotton
	ginning machine for small scale
	farmers" in the e-Yantra Innovation
	Challenge (eYIC) 2022-23 and The
	team has been selected as one of the 10
	team has been selected as one of the 10

Research Publications (No. of papers published in National/International	finalist teams out of 490 teams. This team demonstrated their project at the eYIC National Finals 2022-23 held on 5th - 6th April,2023. Organising Secretary for National Level 7 Day Hands on Training Program on "Integrative Research in various fields of Engineering "sponsored by DST under STUTI Program - 2021 (DST/RND/STUTI/2021/18) by SRKR Engineering College in Collaboration with GITAM Institute of Technology, GITAM Deemed to be University, Visakhapatnam during 21th September - 27th September 2022. Developed and Lab Incharge of Smart Systems Lab, Dept. of EEE, SRKR Engineering College Developed, Lab in charge and SPOC Person of APPLIED ROBOT CONTROL Lab of SRKR Engineering college. Got Top Performing Mentor certificate for the course DESIGN FOR INTERNET OF THINGS (NPTEL) Got Mentor certificate for the course INDUSTRIAL AUTOMATION AND CONTROL (NPTEL)
Journals/Conferences)	
No. of Books published	-
No. of Books published	-