

Faculty Profile

Name	Dr. Kumararaja Andanapalli
Date of Birth	29-11-1986
Unique ID (AICTE ID)	1-751320234
Education Qualifications	M.Tech., Ph.D.
Work Experience	
a) Teaching	12 Years
b) Research	6 years
c) Industry	2 years
d) Others	-----
Area of Specialization	Power Systems & Automation, Power system Protection, Signal processing techniques.
Courses taught at Under Graduate/ Post Graduate Level	<p>UG Courses:</p> <p>Electrical machines (EM)</p> <p>Power System Operation and Control (PSOC)</p> <p>Logic Design & Microprocessor (LDMP)</p> <p>Power system Analysis & Stability (PSAS)</p> <p>Power System Protection (PSP)</p> <p>Digital Logic Design (DLD)</p> <p>PG Courses:</p> <p>Advanced Power System Operation and Control (APSOC)</p> <p>Intelligent System Control (ISC)</p> <p>Artificial Intelligence Techniques (AIT)</p>
Research guidance	
a. No. of papers published in National/ International Journals/ Conferences	<p>International Journal :</p> <p>SCIE : 01</p> <p>Scopus : 05</p> <p>Other : 06</p> <p>Book Chapter:</p> <p>Scopus : 01</p> <p>International conferences:</p> <p>Scopus indexed : 06</p> <p>Non Scopus indexed : 03</p> <p>National Conferences:</p> <p>Scopus indexed : 01</p> <p>Non Scopus Indexed : 05</p>
b. Master (No. of Completed/Ongoing)	11 completed
c. Ph.D. (No. of Completed/Ongoing)	----
No. of Projects Carried out	----

No. of Patents (Filed & Granted)	----
Technology Transfer	21 FDPs/Workshops attended
Research guidance	
No. of papers published in National/ International Journals/ Conferences	<p>International Journal :</p> <p>SCIE : 01</p> <p>Scopus : 05</p> <p>Other : 06</p> <p>Book Chapter:</p> <p>Scopus : 01</p> <p>International conferences:</p> <p>Scopus indexed : 06</p> <p>Non Scopus indexed :03</p> <p>National Conferences:</p> <p>Scopus indexed : 01</p> <p>Non Scopus Indexed :05</p>
No. of Books published	Book chapter published: 01