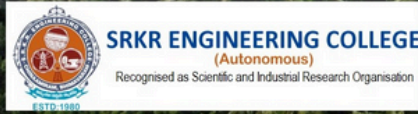


This issue

Brings you awareness about department activities done by staff and students.



ABOUT THE INSTITUTE

Sagi Rama Krishnam Raju Engineering College, established in 1980, is a self-financed academic institution of coeducation striving to provide a high-quality technical education to engineering aspirants. Being one of the premier and well-established technical institutions in the country, it continues to render service to the nation and the world at large with its alumni holding highly prestigious positions and making a substantial contribution. It is an autonomous Institution (UGC Approved) and permanently Affiliated with Jawaharlal Nehru Technological University Kakinada (JNTUK). The college is replete with lush coconut trees spanning across a plot area of 30 acres at China Amiram, Bhimavaram, on the Bhimavaram-Vijayawada state highway and it is also well-connected to all other places by road and rail transport. Over the years, the college has established itself as a premier institution imparting technical education of a very high standard leading to the B. Tech. degrees in 11 branches of engineering, P.G degrees in 6 specializations, and 7 Ph. D. programs. S.R.K.R. Engineering College was established with a mission to develop the institution into a world-class Centre of excellence in the field of technical education that fosters research and essentials of employability and strives for pre-eminence amongst the premier institutions with high commitment to the socio-ecological well-being of the nation.

Alumni Insights: GATE Guidance Webinar with Top Rankers

An **online Guest Lecture** on the theme “**Guidance to the GATE Exam**” was organized by the **Department of Electronics and Communication Engineering** on 19/09/2024 at 7:00 PM onwards through Google Meet (link: <https://meet.google.com/dwx-msty-yoo>).

The session featured two distinguished alumni:

- **Mr. Ankireddy Sai Praveen**, GATE Cracker-2022, **AIR 14 (EC) & AIR 76 (IN)**, currently working at **AMD**.
- **Mr. K. Ravi Kiran**, GATE Cracker-2023, **AIR 52 (IN) & AIR 221 (EC)**, currently pursuing **M.Tech at IIT Kharagpur**.

The event began with opening remarks by **Dr. N. Udaya Kumar**, Head of the ECE Department, and was coordinated by **Sri T. Nalini Prasad**, Assistant Professor, ECE. Over **100 ECE students** from second, third, and final years, along with faculty members, attended the session.

Student coordinators, Jaswanth Kumar and Abhishek (4th year), welcomed the guests and introduced the agenda. The speakers emphasized structured preparation, useful study materials, mock tests, and shared personal experiences. The session concluded with an **interactive Q&A** and a **vote of thanks** by **Sunitha**, a third-year ECE student.

The event was officially announced through a **circular dated 12-09-2024**.

CHIEF EDITOR

Dr. N. UDAYA KUMAR
HOD, ECE

SUB EDITORS

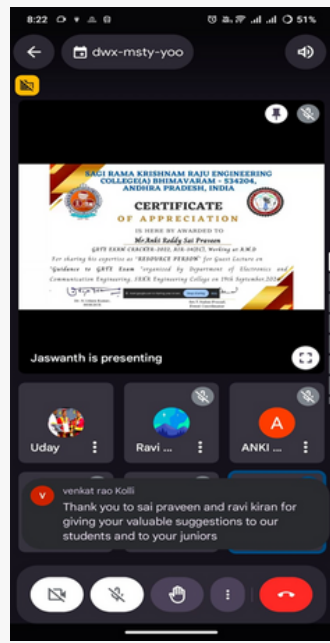
Sri M. VIJAYA RAMARAJU,
Dr. G. NAGARAJU,
Smt. K.L. DIVYA

STUDENT COORDINATORS

A.JAHNAVI VADDEVI 4th ECE
DEEPTHI MEGHANA KARI 4th ECE
M.SIVA SAI PRAKASH, 3rd ECE
M.YUGANDHAR, 3rd ECE

DEPARTMENT VISION

Envision a diverse and stimulating academic research ambience for the student community and shaping them into competent professionals in the field of Electronics and Communication Engineering and to cater to the needs of society with a keen sense of environmental consciousness.



Physical Design Challenges as We Move into Angstroms

A guest lecture titled “Physical Design Challenges as We Move into Angstroms” was conducted on **18th October 2024** at **10:30 AM** in the **ECE Seminar Hall (T-305), T-Block**. The session was delivered by **Mr. Jami Balaji Rao, SOC Designer and Flow Deployment Lead at INTEL Technologies Pvt. Ltd.**, and was organized by the **Centre of Excellence in VLSI Design, Department of ECE, SRKR Engineering College**.

The speaker highlighted the **emerging physical design challenges** in **VLSI** as the industry transitions into the **angstrom scale era**. Mr. Balaji Rao also provided valuable insights into **core job opportunities** in the semiconductor domain, shared **real-world design challenges**, and offered **practical guidance** on preparing for roles in the **VLSI** industry.



The session saw an enthusiastic response from **175 students of 3/4 B.Tech ECE** and active participation from faculty members. The event was coordinated by **Mr. T.V. Syamala Raju** and **Mr. A.V.S. Surya Varma**. The circular for the session was released on **16th October 2024**.

Guest Lecture on Cutting-Edge VLSI: “Physical Design Challenges as We Move into Angstroms”

A guest lecture titled “Physical Design Challenges as We Move into Angstroms” was conducted on **October 18, 2024**, at **3:30 PM** in the **ECE Seminar Hall (T-305), T-Block**, organized by the Department of ECE in association with the **SRKREC Alumni Association**. The session was delivered by **Mr. Jami Balaji Rao, SOC Designer and Flow Deployment Lead at INTEL Technologies Pvt. Ltd.** He addressed the key challenges in VLSI physical design as technology scales down to the angstrom level, offering valuable insights into the evolving semiconductor landscape. The speaker also discussed core job roles, industry expectations, and preparation strategies for careers in **VLSI**. **Dr. N. Udaya Kumar**, Head of the Department – ECE, delivered the opening remarks. The event was coordinated by **Dr. G. Nagaraju**, Associate Professor, ECE, and witnessed active participation from **125 students of 3rd-year B.Tech ECE**. The session was highly informative and well appreciated by all attendees.



DEPARTMENT MISSION

- To educate the students with the state of the art technologies in Electronics and Communication Engineering to meet the ever growing challenges of the industry.
- To nurture the spirit of innovation and creativity in the faculty and students in order for them to carry out research in collaboration with research organizations and industry
- To provide ethical and value based education that promotes activities pertaining to societal needs.

Guest Lecture on Embark on a Journey into Embedded Systems

On October 25, 2024, the Department of Electronics and Communication Engineering, SRKREC, organized a guest lecture titled “Embark on a Journey into Embedded Systems” at the ECE Seminar Hall.

SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (A)
(ACCREDITED BY NBA & NAAC WITH A+, RECOGNIZED BY AICTE, NEW DELHI)
SRKR MARG, BHIMAVARAM - 534204, A.P, INDIA

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

GUEST LECTURE
EMBARK ON A JOURNEY INTO EMBEDDED SYSTEMS

VIJAY VEERA
DIRECTOR
VIJAY EMBEDDED HUB
AND
MICROSYSLOGIC TECHNOLOGIES

FRIDAY
25-OCTOBER-2024 @ **ECE SEMINAR HALL**

TIME
10:00 AM **ENROLL** https://docs.google.com/forms/d/NDKJDOOCBEY2HQMFZGZJYQHAWQ5W3F3EF_SF5PPAPM/PSE/FILL

FACULTY INCHARGE **CO ORDINATORS**

SRI.N.KISHORE CHANDRA DEV,ASST.PROF.,ECE DEPT.
DR. H.SATISH KUMAR,ASST.PROF.,ECE DEPT.
SRI.T.NALINI PRASAD,ASST.PROF.,ECE DEPT.
SRI.M.PRAVEEN KUMAR,ASST.PROF.,ECE DEPT.



The session was delivered by **Mr. Vijay Veera**, Founder and Tech Director of **Vijay Embedded Hub & Micro Syslogic Technologies**, and was attended by over **190 ECE students** from the second, third, and fourth years, along with faculty members. The event began with a welcome address by **Dr. N. Udaya Kumar**, Head of the Department. Mr. Vijay Veera provided valuable insights into **embedded system design**, highlighting real-world applications and emerging career opportunities in the domain. The session was coordinated by faculty members **Sri. N. Kishore Chandra Dev**, **Dr. M. Satish Kumar**, **Sri. T. Nalini Prasad**, and **Sri. M. Praveen Kumar**, with support from student coordinators **A. Manikanta** and **A. Jahnvi**, who also delivered the vote of thanks. The session was well-received and offered a comprehensive overview of the embedded systems landscape, inspiring students to explore this dynamic and in-demand field.

Exploring the Skies: Role of EC in Modern Aviation – A Guest Lecture

A Guest Lecture on “**Important Role of Electronics and Communication in Aviation**” was organized by the **Department of ECE** in collaboration with the **Women Empowerment Cell** on **13th September 2024** at **3:00 PM** in **U-402, U-Block**. The session was delivered by **Sri K. Rambabu**, **Joint General Manager (ATC), Airports Authority of India, Hyderabad**. He provided valuable insights into the critical role of Electronics and Communication (EC) in **flight operations, air traffic management, and aircraft design**, while also highlighting the growing importance of **technology and sustainability** in modern aviation systems. The lecture concluded with an interactive Q&A session and a vote of thanks by **Mr. Sairam Chaganti**. The event was coordinated by **Mrs. G. Prathima** and **Mrs. D.V.N. Bharathi**, and was attended by **66 enthusiastic participants**.



DEPARTMENT MISSION

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- To nurture the spirit of innovation and creativity in the faculty and students in order for them to carry out research in collaboration with research organizations and industry
- To provide ethical and value based education that promotes activities pertaining to societal needs.

ENCENDER-1

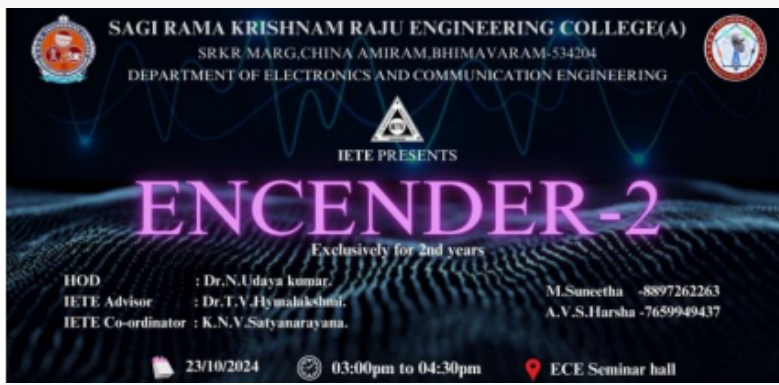
On **October 3rd and 4th, 2024**, the **IETE Student Forum (ISFC-65)** of the **Department of ECE, Sagi Ramakrishnam Raju Engineering College (Autonomous)**, Bhimavaram, organized “ENCENDER-1”, an engaging orientation-cum-interactive event aimed at welcoming and inspiring 180 first-year ECE students, fondly referred to as “young elegants.” The event commenced with a warm welcome address by **Dr. N. Uday Kumar**, Head of the Department, followed by insightful talks from **Dr. T. V. Hyma Lakshmi** (IETE Faculty Advisor) and **Sri K. N. V. Satyanarayana** (IETE Faculty Coordinator), who introduced the objectives and benefits of the IETE community. Activities included an informative PPT presentation on the department by IETE Executive Body Members, interactive rounds such as “Guess the Song,” a session on “IETE & its Events” focusing on skill development, and a motivational speech by the IETE Treasurer. Students also toured the wiring labs and provided feedback via QR code.

The event concluded on a high note with a vote of thanks delivered by M. Padma Pravinya and K. Yashwanth, and was widely appreciated by participants for its informative and fun-filled approach.



ENCENDER-2

On **October 23, 2024**, at **3:00 PM**, the **IETE Student Forum (ISFC-65)** of the **Department of ECE, Sagi Ramakrishnam Raju Engineering College (Autonomous)**, Bhimavaram, successfully conducted “ENCENDER-2” at the **ECE Seminar Hall** for **117 second-year students**. The session was designed to enhance awareness about the **hardware and software domains in ECE** and promote the benefits of being part of the IETE community. The event featured inspiring speeches by **Dr. N. Uday Kumar** (HoD), **Dr. T. V. Hyma Lakshmi** (IETE Advisor), and **Sri K. N. V. Satyanarayana** (Coordinator). IETE Executive Body Members presented overviews of the “Hardware” and “Software” specializations, followed by engaging activities such as “Guess the Symbol” and “Guess the Dialogue.” A focused discussion on **higher studies and career opportunities** added academic value, while a **lab visit** and **interactive Q&A session** enriched the practical perspective. Feedback was collected digitally via QR code. The session received an **overall rating of 9/10**, with students expressing appreciation for its informative content, engaging format, and career clarity—marking ENCENDER-2 as another impactful initiative by the IETE team.



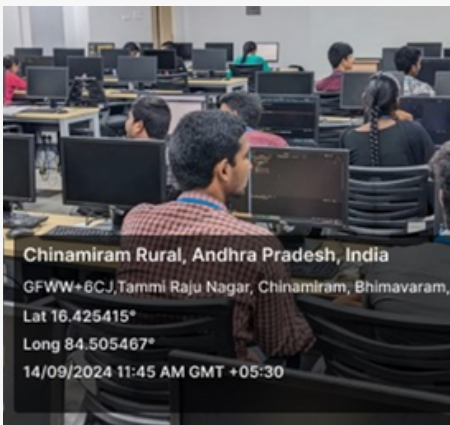
ABOUT OUR DEPARTMENT

The department of ECE was established in the year 1980 with an intake of 35 which was subsequently raised to 240 over a period of time. M. Tech course in Communication Systems was started in 2006. The department is presently offering 4-year B. Tech program in ECE, 2-year M. Tech in Communication Systems and Ph.D. program. The department has highly qualified, experienced and dedicated staff members. The department has dedicated technical and non-teaching staff members. The department has good infrastructure facilities. The department was accredited four times by National Board of Accreditation (NBA). The department has student chapters of professional bodies like IETE and college level ISTE, which conduct various student technical activities. Students of ECE participate in National level technical paper and hardware competitions being organized at various other colleges and some of them are awarded with prizes. They have been obtaining good scores in GRE, TOEFL and CAT. Students are placed in companies like TCS, Polaris, CTS, IBM, Infosys, Texas Instruments, BSNL, BEL, ISRO and DRDO. ECE department has been identified as research center by JNT University. The research center provides required facilities for carrying out research work for scholars. The department is associated with organizations like DRDO, NSTL, BEL, ISRO, BSNL, NRSA and RTTC. The department has MOUs with DS Reddy Consultancy, ETI Lab Pvt. Ltd., Salcit Technologies, Hyderabad and TCS for collaborative work and faculty improvement programs. Faculty published and presented about 512 research papers in technical journals and conferences.



Guest Lecture on “Important Role of EC in Aviation” – WEC Collaboration

On the occasion of **Engineer’s Day**, the **Department of ECE, SRKREC** organized a guest lecture titled “**Role of Electronic Engineers in Aviation**” on **14th September 2024 at 10:00 AM** in the **Skill Development Lab, T-109, T-Block**. The session was delivered by **Sri K. Rambabu, Joint General Manager (ATC), Airports Authority of India, Hyderabad**. He spoke on the significant contributions of electronic engineers to the aviation sector, focusing on **flight operations, aircraft design, air traffic management**, and the growing role of **technology and sustainability** in modern aviation. The speaker also emphasized **career opportunities in government sectors**, the **competitive nature of the aviation industry**, and offered valuable guidance for students aspiring to enter this field. The session concluded with an interactive Q&A and a vote of thanks by **Mr. Sairam Chaganti**. The event was coordinated by **Mr. Y. Rama Lakshmana** and was attended by **66 second-year B.Tech ECE students**. The circular for the event was released on **10th September 2024**.



Guest Lecture: Role of Research Publications in Enhancing NIRF Performance

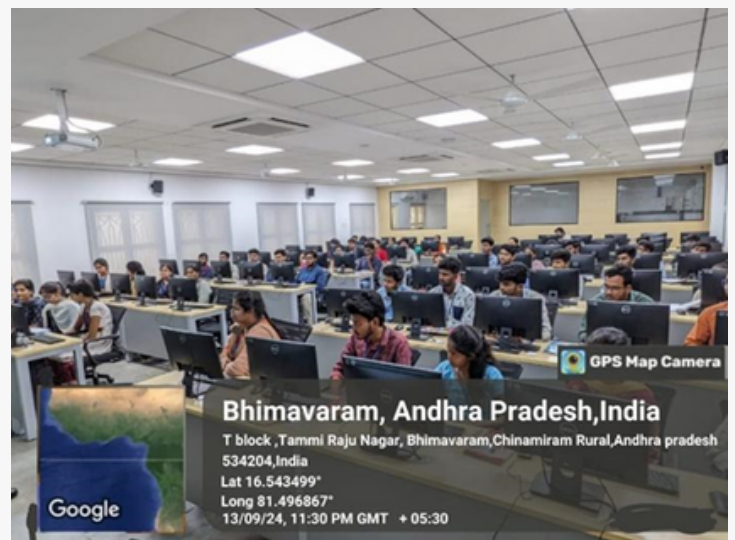
The Department of ECE, SRKR Engineering College, organized a guest session titled “Insights on Paper Publications and Their Role in NIRF Ranking” on 24th October 2024 at 4:30 PM in the Skill Development Lab. The session was delivered by Dr. T. Vamsi Naga Raju, Dean of R&D and Civil Engineering at SRKREC, and a Ph.D. graduate from NIT Karnataka. A distinguished academician, Dr. Vamsi has been honored with several accolades, including the Springer Best Paper Award (2023) and the Young Researcher Award (2022).

During the session, he emphasized the critical role of high-quality research publications in improving an institution’s NIRF (National Institutional Ranking Framework) standing, and offered practical strategies to boost research output and academic visibility. The event was inaugurated by Dr. N. Udaya Kumar, HOD – ECE, and witnessed enthusiastic involvement from the department’s faculty members.



The Sky’s the Limit: Role of Electronic Engineers in Aviation – A Guest Lecture

The Department of ECE, SRKREC organized a guest lecture on the occasion of **Engineer’s Day** titled “**Role of Electronic Engineers in Aviation**” on **14th September 2024** at **10:00 AM** in **Room T-109, T-Block**. The session was delivered by **Sri. K. Rambabu, Joint General Manager (ATC), Airports Authority of India, Hyderabad**. The lecture highlighted the critical role of electronic engineers in **flight operations, air traffic management, and emerging aviation technologies**. Sri. Rambabu also shared insights into career **opportunities in government sectors** and offered practical advice for students to navigate challenges in the aviation industry. The event concluded with an interactive **Q&A** session and a **vote of thanks by Mr. Sairam Chaganti**. The session was coordinated by **Mr. Y. Rama Lakshmana** and attended by **66 students from 2/4 B.Tech ECE**. The circular for the event was released on 10th September 2024.



Guest Lecture Series:

How Electronics Shape Air Traffic Control Systems

To commemorate **National Engineer's Day (15th September)**, the **IETE Student Forum of SRKR Engineering College** organized a guest lecture titled **"Role of Electronics in Air Traffic Control (ATC)"** on **14th September 2024** at **3:00 PM** in the **Skill Development Lab**. The session was delivered by **Sri. K. Rambabu, Joint General Manager (ATC), Airports Authority of India, Hyderabad**, and an alumnus of SRKR (1992–1995). Drawing from his professional experience, he explained the **crucial role of electronics in enhancing ATC systems**, focusing on modern advancements in **communication, surveillance, navigation, and aviation safety**. He also shared insights from his career path, inspiring students to explore careers in the aviation and electronics sectors. The session concluded with an engaging **Q&A** and a **vote of thanks** from an **IETE Executive Body Member (EBM)**. The event witnessed **enthusiastic student participation and received positive feedback** from attendees.



Dasara Pooja

The Department of ECE, SRKR Engineering College, celebrated the auspicious occasion of Dasara with the Sri Kanakadurgamma Pooja on 5th October 2024. The pooja was conducted in a traditional and devotional manner in the department premises. Faculty and students participated with great enthusiasm, and Devi Prasadam was distributed to all attendees following the rituals. The event was coordinated with the dedicated efforts of Dr. N. Udaya Kumar, HOD – ECE, along with the support of department faculty, contributing to the successful and joyful completion of the celebration.



Student Development Program on "Introduction to Internet of Things"

The **Department of ECE, SRKR Engineering College** organized a **5-Day Student Development Program** on **"Introduction to Internet of Things"** from **10th to 14th September 2024**. A total of **63 second-year ECE students** participated in this peer-led initiative, which was conducted by final-year B.Tech students. The program aimed to provide foundational knowledge and hands-on experience in IoT. Sessions covered key topics including **Tinkercad basics, Arduino coding, sensor integration, ESP32 connectivity, cloud platforms like ThingSpeak and Adafruit**, and culminated in a **Smart Home mini project**. The sessions were coordinated by **B. Tapasvi** and **K. Phani Varma**, under the guidance of **Convener Dr. N. Udaya Kumar (HOD, ECE)** and **Faculty Advisor Mrs. K.L. Divya**. The program concluded with encouraging remarks from the faculty, emphasizing the importance of self-driven learning and continued exploration in the field of **IoT**.

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE (A)
SRKR MARG, CHINAMIRAM, BHIMAVARAM-534204

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
PRESENTS
A 4-DAY WORKSHOP On

INTRODUCTION TO INTERNET OF THINGS

Your Gateway to Smart Innovations

KEY TAKEAWAYS:

- CAREER GUIDANCE
- UNDERSTANDING IOT FUNDAMENTALS
- SENSOR INTEGRATION
- ARDUINO PROGRAMMING BASICS
- COLLABORATION & PROJECT EXECUTION

VENUE: SKILL DEVELOPMENT LAB

DATES: 10/09/2024-13/09/2024

HURRY UP!!! LIMITED SEATS

REGISTRATION FEE: 100/-

CHIEF PATRONS:

- Dr. M. Jagapathi Raju, Director, SRKREC
- Prof. K V Murali Krishnam Raju, Principal, SRKREC

CONVENER:

Dr. N Udaya Kumar, HOD, Dept. of ECE

FACULTY ADVISOR:

Mrs. K L Divya, Asst.Prof., Dept. of ECE

FACULTY COORDINATORS:

- Sri B. Tapaswi, Asst. Prof., Dept. of ECE
- Sri K Phani Varma, Asst. Prof., Dept. of ECE

STUDENT COORDINATORS:

- Mr. U. Santhosh Kumar: 9963845715
- Mr. D. Sri: 9010992426
- Mr. Ch. Sairam: 8688833482



Skill Enhancement Program: Introduction to Internet of Things (Batch-2)

The Department of Electronics and Communication Engineering, SRKR Engineering College, organized a **Three-Day Student Development Program** titled “Introduction to Internet of Things (Batch-2)” from **November 7–9, 2024**, for **second-year B.Tech ECE students**. The program adopted a peer-led approach, where final-year students served as **resource persons**, facilitating interactive and hands-on learning sessions.

On **Day 1**, **G. Hemanth** discussed **career opportunities in ECE and IoT**, along with an introduction to **Arduino basics**. **Day 2** featured **U. Santhosh**, who provided an overview of **IoT architecture** and demonstrated **Arduino coding** with motor control. On **Day 3**, **D. Sri** guided students through the development of a **Smart Home project**, including hardware setup and real-time coding.

The program effectively built a **strong foundational understanding of IoT**, combining theoretical insights with practical application, and significantly enhanced participants' confidence in **hardware interfacing and embedded system design**.

SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE (A)
SRKR MARG, CHINAAMIRAM, BHIMAVARAM-534204

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
PRESENTS
A WORKSHOP On

INTRODUCTION TO INTERNET OF THINGS

KEY TAKEAWAYS:

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- COLLABORATION & PROJECT EXECUTION

Your Gateway to Smart Innovations
Batch-2

VENUE: SKILL DEVELOPMENT LAB

DATE: 07/11/2024 TO 9/11/2024

CHIEF PATRONS:

- Dr. M. Jagapathi Raju
Director, SRKREC
- Prof. K V Murali Krishnam Raju
Principal, SRKREC

CONVENER:

- Dr. N Udaya Kumar
HOD, Dept. of ECE

FACULTY ADVISOR:

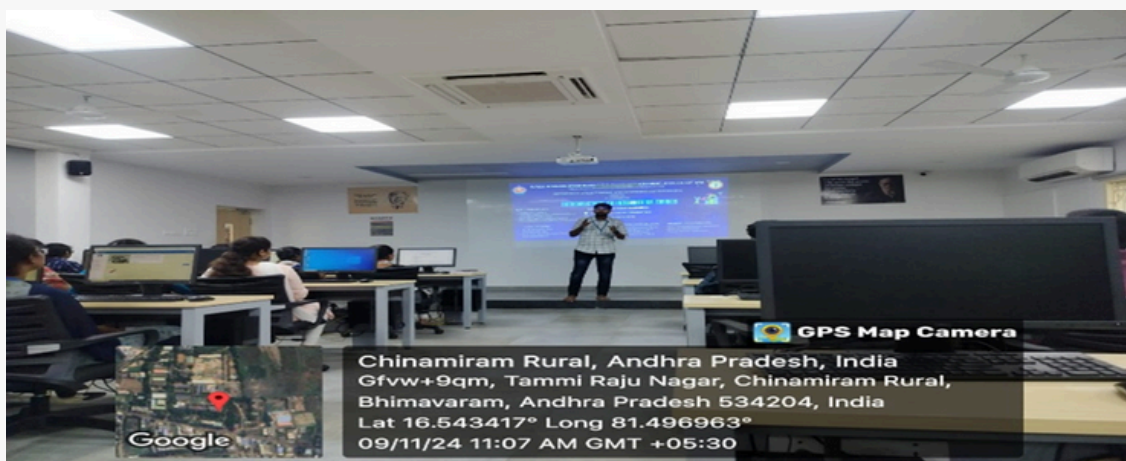
- Dr. R. Krishna Chaitanya
Assoc., Prof., Dept. of ECE

FACULTY COORDINATORS:

- Dr. P. Krishnakanth Varma
Asst. Prof., Dept. of ECE
- Mrs. D.V.N Bharathi
Asst. Prof., Dept. of ECE

STUDENT COORDINATORS:

- Mr. U. Santhosh Kumar: 9963845715
- Mr. D. Sri: 9010992426
- Mr. Ch. Sairam: 8688833482



On **September 21, 2024**, the **Department of Electronics and Communication Engineering, SRKREC**, organized a guest lecture on “Careers in VLSI” at **T-306**, featuring **Ms. Botta Sireesha**, Senior Project Engineer at **Wipro Technologies**, as the keynote speaker. The session, attended by **51 third- and fourth-year B.Tech ECE students**, was aimed at providing clarity on **career opportunities in the VLSI domain** and preparing students for **core job roles** in semiconductor design. Ms. Sireesha shared valuable insights on **job profiles, required technical skills, and essential core subjects** in VLSI, drawing from her professional experience at Wipro. The event began with a welcome address by **Dr. N. Udaya Kumar**, HoD of ECE, and was coordinated by **Mr. Y.S.S. Sriramam** and **Mrs. S. Swathi**. The session concluded with an interactive discussion and a vote of thanks delivered by **Purna Chandra**, representing the student body.



Guest Lecture: Unlocking Career Potential in VLSI and Embedded Systems

On **November 26, 2024**, the **Department of Electronics and Communication Engineering, SRKREC**, hosted an interactive session on “Career Opportunities in VLSI and Embedded Systems” at the **Skill Development Lab**. The session was delivered by **Mr. Venkat Sunkara**, Founder of **Chip Edge Technologies Pvt. Ltd.** and a distinguished alumnus of SRKREC. With over **26 years of experience** in the semiconductor industry, Mr. Sunkara has held key positions at **Cadence Design Systems, Time to Market Pvt. Ltd.**, and others before founding ChipEdge Technologies in 2012. During the session, he offered deep insights into **career paths, skill requirements, and industry expectations** in both VLSI and Embedded Systems, while also sharing highlights from his professional journey and entrepreneurial experiences. The session was attended by prominent faculty members including Dr. N. Udaya Kumar (HoD, ECE), **Dr. Mantena Jagapathi Raju** (Director), **Prof. K. V. Murali Krishnam Raju** (Principal), and others, making it a significant event in the department’s academic calendar.



Guest Lecture

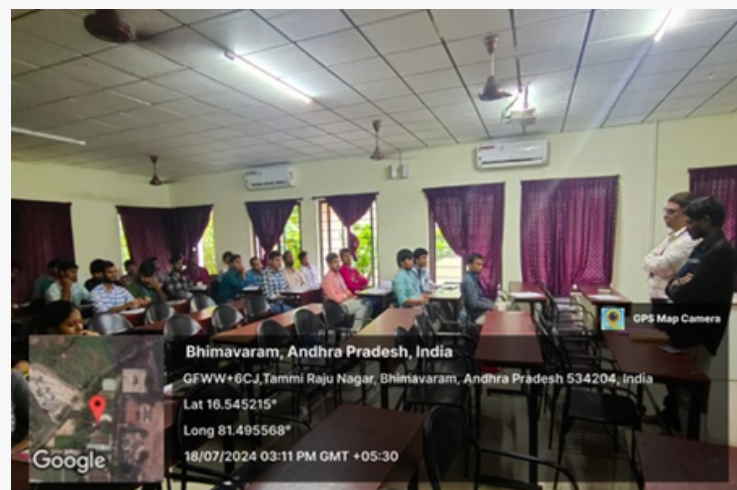
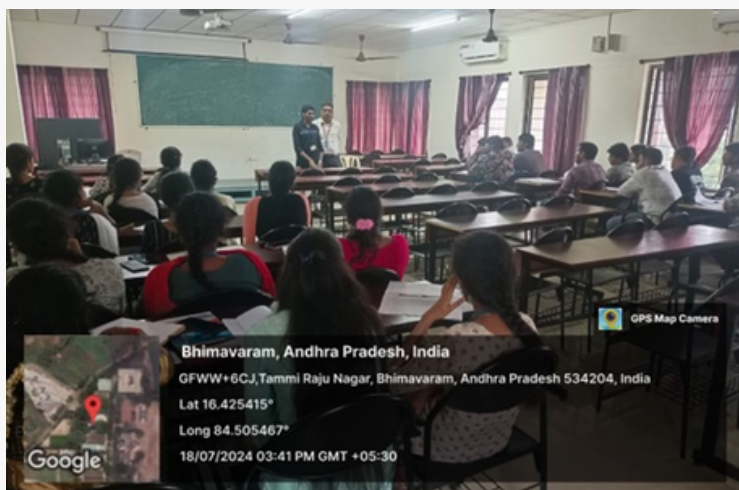
From Classroom to Industry: The Value of Hands-on Industrial Projects

On **November 22, 2024**, the **Department of Electronics and Communication Engineering, SRKREC**, organized a distinguished guest lecture on “Significance and Role of Industrial Projects in Bridging Academia and Industry” at the **Skill Development Lab**. The session was delivered by **Dr. Sujan Kasani**, an esteemed alumnus (2008–2012) and Product **Development R&D Engineer at Intel Corporation, USA**. Dr. Kasani holds a Ph.D. in Semiconductor Electronics (Nanotechnology) from **West Virginia University** and is recognized for his outstanding research contributions, having authored **15 peer-reviewed journal articles** and **12 conference papers**. He is a recipient of the prestigious **O1 visa and Green Card** and is also the founder of **Silicon Infotech**, a startup based in the USA. In his address, Dr. Kasani emphasized the critical role of **industrial projects in fostering innovation and collaboration**, highlighting how such initiatives can effectively bridge the **gap between academic knowledge and industry expectations**. The session inspired students to pursue practical exposure through interdisciplinary projects and research. The event was graced by the presence of **Dr. N. Udaya Kumar** (HoD, ECE), **Dr. K. Bala Sindhuri** (Event Coordinator), **Dr. T. Vamsi Nagaraju** (Dean of R&D), and other ECE faculty members.



Interactive Guest Session on VLSI Industry Career Pathways

On **July 18, 2024**, the **Department of Electronics and Communication Engineering, SRKREC**, organized a guest lecture on **career opportunities in the VLSI industry**, held in the **E-Class Room** from **2:00 PM to 4:00 PM**. The session was delivered by **Mr. Ratna Chaitanya**, Engineer Trainee, and coordinated by **Mr. N.V. Phani Sai Kumar**. Targeted at **final-year B.Tech ECE students**, the lecture focused on the various **job roles, domains, and pathways** available in **VLSI Design** for graduates. Mr. Chaitanya provided practical insights and actionable guidance for students aspiring to enter the semiconductor industry. With active engagement from the **35 students** in attendance, the session proved to be **interactive, informative, and well-received**, giving attendees a clearer understanding of the core job landscape in the VLSI field.



Expert Session on Analog Circuit Design: Principles and Relevance

On **October 29, 2024**, the **IETE Student Forum**, in association with the **Centre of Excellence in VLSI** and the **Department of ECE, SRKREC**, organized a virtual expert talk titled "Analog Circuit Design – What, Why & How" via **Google Meet**. The session was delivered by **Sri Souri Subhakar Nadakuduru**, **AMS Engineer at Siemens EDA, Bengaluru**, and an alumnus of **SRKR Engineering College (2014–2018)**. Conducted ahead of **IETE Foundation Day**, the talk provided an insightful overview of **analog circuit design principles**, emphasizing their significance and applications in modern electronics and semiconductor industries. The event concluded with a dynamic **Q&A session**, followed by a **vote of thanks** presented by **N. Sushmitha**, Executive Body Member of the SRKR IETE Forum.



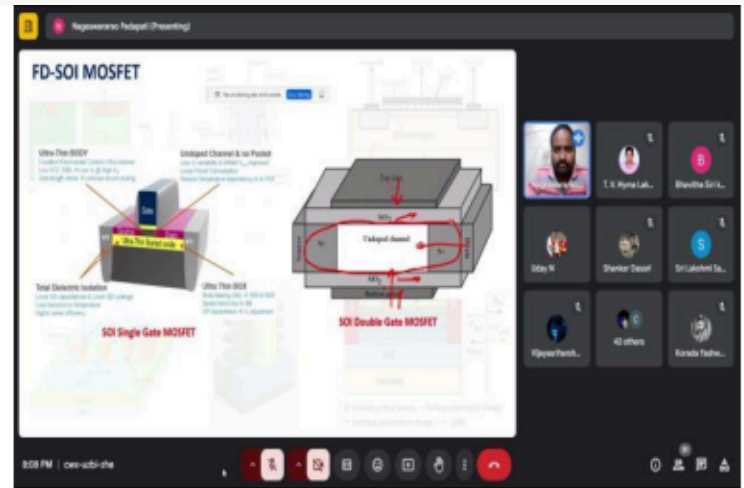
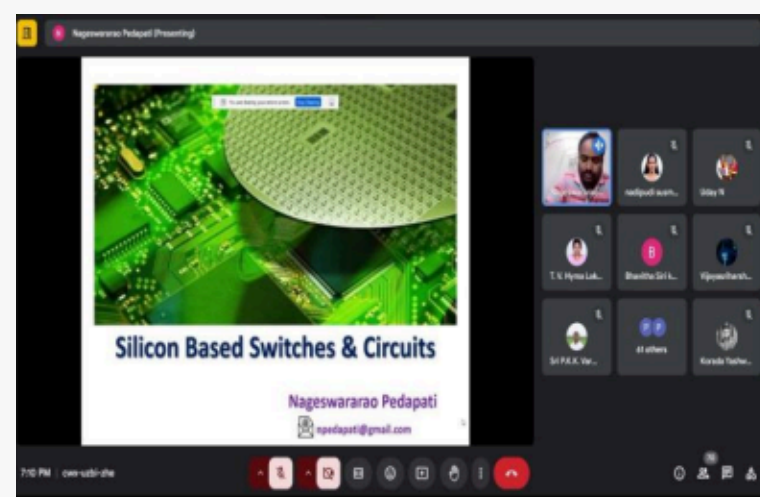
Building a Future in VLSI – Guest Session on Career Guidance

On **July 19, 2024**, the **Department of Electronics and Communication Engineering, SRKREC**, organized a guest lecture on "Career Guidance in the VLSI Industry" at the E-Classroom, from **2:00 PM to 4:00 PM**. The session featured **Mr. G. Venkata Rao**, a **researcher at Ericsson AB, Stockholm, Sweden**, who shared deep insights into **career opportunities, skillsets, and job roles** within the **VLSI design industry**. With a global perspective and industry-driven approach, Mr. Rao addressed practical expectations and growth areas for engineering graduates. The session was highly **interactive and informative**, with **45 students** actively engaging in the discussion. The event was coordinated by **Mr. M. Praveen Kumar** and served as a valuable platform for students to understand pathways into core VLSI careers.



Understanding Silicon-Based Switching Devices: A Technical Session

On **November 23, 2024**, the **IETE Student Forum**, in collaboration with the **Centre of Excellence in VLSI** and the **Department of ECE, SRKREC**, organized a virtual expert talk on “Silicon-Based Switching Devices” via **Google Meet**. The session was delivered by **Sri Nageswararao Pedapati**, **Manager** at **Bharat Electronics Limited (BEL)**, **Bengaluru**, and an esteemed alumnus of SRKR. With an **M.Tech in Electrical Engineering (Microelectronics)** from **IIT Bombay** and over **16 years of experience** at BEL, Sri Nageswararao offered detailed insights into the **working principles, design, and industrial applications** of silicon-based switching devices. His impressive profile includes distinctions such as being a **Certified Quality Engineer (CQE)** and a recipient of a **Certificate of Accomplishment with Distinction** from the World Bank. The session was highly **interactive**, with enthusiastic student participation, and concluded with a vote of thanks by **M. Padma Pravinya**, Executive Body Member of the SRKR IETE Forum.

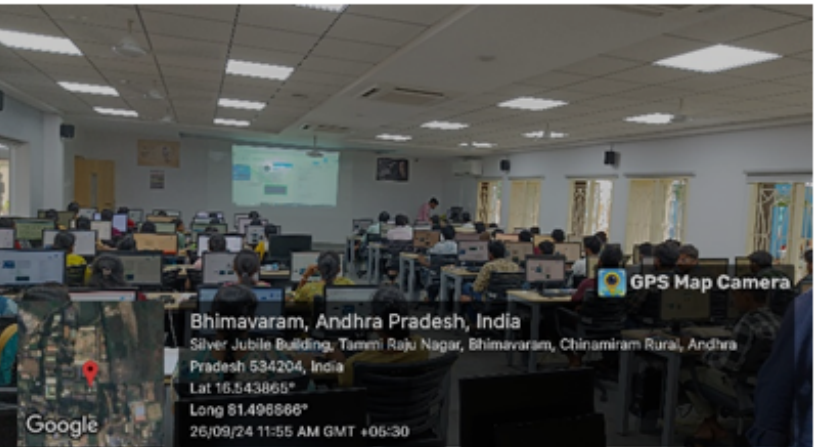


An event poster for an expert talk. At the top, it reads "SRKR ENGINEERING COLLEGE (A), BHIMAVARAM" and "DEPT. OF ELECTRONICS AND COMMUNICATION ENGINEERING". Below this, it says "Organized by IETE Students Form ISFC-65 In Association with COE VLSI". The main title is "Expert talk on 'Silicon based switching devices'" by "HONOURABLE GUEST NAGESWARARAO PEDAPATI, MANAGER, Bharat Electronics Limited, Bengaluru". A circular portrait of Nageswararao Pedapati is on the left. At the bottom, it says "Virtual Meet", "DATE: 23/11/2024", and "TIME: 7Pm - 8.30Pm". Logos for SRKR Engineering College and IETE are also present.

Sensors to Solutions: Unlocking the Power of IoT and Raspberry Pi

From **September 24 to 28, 2024**, the **Department of Electronics and Communication Engineering, SRKR Engineering College**, conducted a **week-long Student Development Program (SDP)** focused on **IoT and Raspberry Pi** for **third-year students**. The program, led by **Mr. T. Bhanu Chandar** from **Chaturvedi IT Solutions**, was designed to equip students with **practical knowledge** required for their final-year projects. With **67 participants**, the workshop provided hands-on training on **IoT concepts, embedded systems, Arduino programming, Linux basics, and sensor-based project development**. Sessions included topics such as smart home automation, weather monitoring, and health monitoring systems.

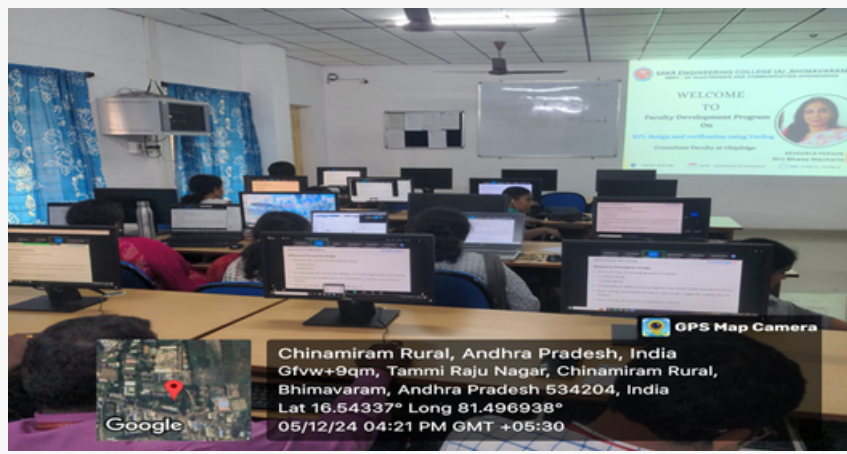
Each day of the workshop was structured to progressively build students' skills—from **basic Arduino programming** and **real-world IoT applications** to **Raspberry Pi integration** and **sensor interfacing**. The event was coordinated by **Dr. N. Udaya Kumar** (Convener), **Mrs. K. L. Divya**, **Dr. V. Nagavalli**, and other faculty members, and successfully bridged the gap between theory and real-world applications in IoT development.



Faculty Development Program on Chip Edge

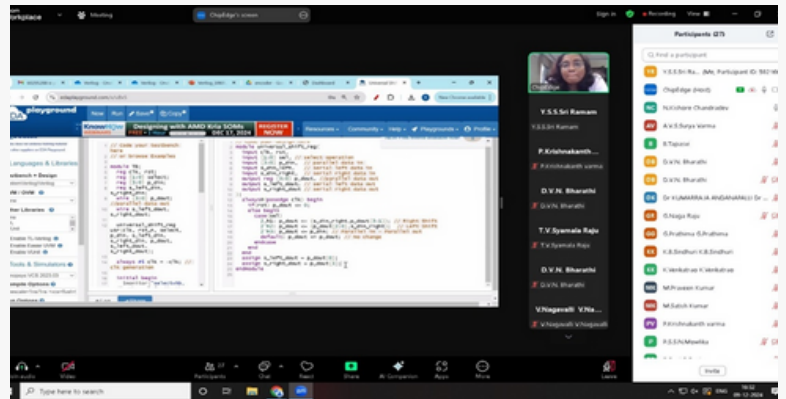
The **Department of Electronics and Communication Engineering, SRKR Engineering College**, organized a comprehensive **9-day Faculty Development Program (FDP)** on “Chip Edge” from **December 3 to 13, 2024**, featuring **Mrs. Bhanu Macharla**, an esteemed alumna from the **1994 batch**. Currently a **consultant faculty at Chip Edge**, Mrs. Macharla brought with her extensive experience from leading industry giants such as **Synopsys** and **Intel**. Throughout the FDP, she shared her **career journey** and provided in-depth training on **VLSI design using Verilog HDL**. The sessions included topics such as **lexical tokens, data types, gate-level and dataflow modelling, control statements, and testbench development**, culminating in advanced modules like **RTL design, FSMs (Mealy and Moore), and behavioral modelling**. The final day featured **theory and lab evaluations**, along with interactive doubt-clearing sessions. The program proved to be an inspiring and enriching experience for the faculty, encouraging them to further explore opportunities in the evolving VLSI domain. The department extended heartfelt thanks to **Mrs. Bhanu Macharla** for her valuable contributions.





FDP on RTL design and verification using Verilog

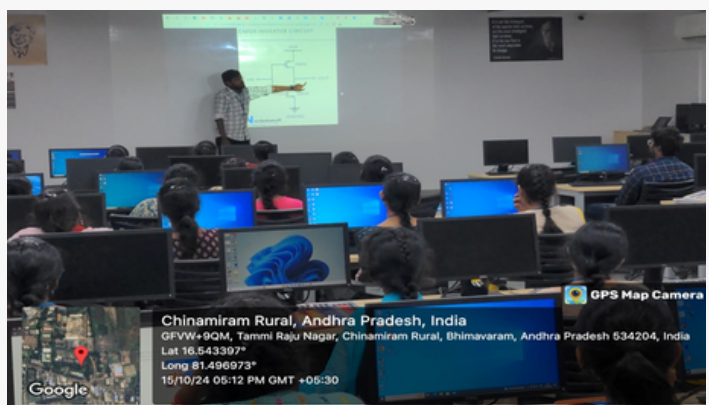
From **November 3 to 13, 2024**, the **Department of Electronics and Communication Engineering, SRKR Engineering College**, conducted a **Faculty Development Program (FDP)** on “RTL Design and Verification Using Verilog” in collaboration with Chip Edge. The sessions were delivered by **Mrs. Bhanu Marchala**, a seasoned industry expert and Consultant Faculty at Chip Edge. The program was coordinated by **Dr. K. L. Bala Sindhuri**, Associate Professor, and **Mrs. S. Swathi**, Assistant Professor, ECE. A total of **25 faculty members** participated in this intensive training. The FDP aimed to provide a comprehensive understanding of **Verilog HDL** and its practical applications in **VLSI design**, helping participants build a strong foundation in **digital electronics**. The curriculum included **advanced modelling techniques, testbench development, and control statements**, empowering faculty with the necessary skills to design, simulate, and verify **combinational and sequential circuits**. The program successfully bridged the gap between academic instruction and real-world design workflows.



Three-Day Student Development Program on Digital Design with DSCH Software

The **Department of Electronics and Communication Engineering, SRKR Engineering College (Autonomous), Bhimavaram**, organized a **three-day hands-on workshop** on Digital VLSI Design using DSCH software from **October 15 to 17, 2024**. The workshop, attended by **53 students**, aimed to build a strong foundation in **digital electronics** and provide practical exposure to circuit simulation. Sessions were led by senior students: **Ch. Sairam** introduced transistors and DSCH software on Day 1, **U. Santhosh Kumar** covered logic gates on Day 2, and **U. Santhosh Kumar** along with **V.P.V.B. Datta** concluded with XOR, XNOR gates, and multiplexers on Day 3. **Dr. N. Udaya Kumar**, Head of the Department, emphasized the growing relevance of **VLSI** in modern electronics. The workshop offered both **theoretical understanding and hands-on skills**, effectively preparing students for careers in VLSI and digital systems.

Dr. T. V. Hyma Lakshmi, Associate Professor, **Department of ECE**, SRKR Engineering College, was honored as an **NPTEL Discipline Star, Motivated Learner**, and **NPTEL Believer** for her exceptional commitment to continuous learning. Recognized in the discipline of **Computer Science and Engineering** for the **January–April 2024** session, she completed an impressive **76 weeks** of NPTEL coursework, with over **50 weeks** achieving scores above 55%. This recognition is part of the **NPTEL program** (an initiative by MHRD and coordinated by IITs and IISc) that acknowledges outstanding learners who consistently perform well across courses. Her dedication sets a shining example for both students and peers, reinforcing the importance of lifelong learning in engineering education.



Faculty Recognition by NPTEL: Honored as Discipline Star, Motivated Learner & NPTEL Believer

Dr. T.V. Hyma Lakshmi, Associate Professor, Department of ECE, SRKR Engineering College, has been recognized as a “Discipline Star,” “Motivated Learner,” and “NPTEL Believer” by NPTEL for her outstanding commitment to continuous learning. She received a Certificate of Appreciation for being recognized as an “NPTEL Discipline Star” in Computer Science and Engineering for the January–April 2024 session. This prestigious recognition is awarded by NPTEL (a national initiative by the MHRD and coordinated by seven IITs and IISc) to learners who demonstrate consistent academic dedication. Dr. Hyma Lakshmi successfully completed 76 weeks of NPTEL courses, with over 50 weeks of certifications in the same discipline, each with scores exceeding 55%, showcasing her dedication to interdisciplinary knowledge enhancement and professional excellence.



The Importance of Counselling and NEP 2020 Policies

The Department of Electronics and Communication Engineering, SRKR Engineering College, organized a series of awareness sessions titled "The Importance of Counselling and NEP 2020 Policies" for first-year ECE students. The sessions were held on 17th, 18th, and 19th September 2024 for A, C, and B sections respectively, from 4:15 PM to 5:15 PM at ECE T-407.

The event focused on two key themes: student counselling and the New Education Policy (NEP) 2020. Dr. N. Udaya Kumar, HOD of ECE, spoke about the crucial role of counselling in providing academic guidance, emotional support, career planning, and personal development. Sri Y. Sriramam (NEP Faculty Coordinator) and Jahnavi (NEP Student Coordinator) introduced the NEP 2020 framework, highlighting its flexible, multidisciplinary learning approach, focus on skill development, and the provision for multiple entry and exit options in higher education.

The sessions were well-received, with enthusiastic participation from students and increased awareness of the academic and policy support systems available to them.



"THE STRUGGLE YOU'RE IN TODAY IS DEVELOPING THE STRENGTH YOU NEED FOR TOMORROW."