

SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (AUTONOMOUS)

(Affiliated to JNTUK, Kakinada), (Recognized by AICTE, New Delhi) UG Programmes CE,CSE,ECE,EEE,IT & ME are Accredited by NBA, Accredited by NAAC with A⁺ CHINNA AMIRAM (P.O):: BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN: 534 204

Estd:1980

Regulation: R20				IV / IV - B.Tech. I - Semester								
	ELECTRICAL AND ELECTRONICS ENGINEERING											
	SCHEME OF INSTRUCTION & EXAMINATION (With effect from 2020-21 admitted Batch onwards)											
Course Code	Course Name		Catego ry	Cr	L	Т	Р	Int. Marks	Ext. Marks	Total Marks		
B20HS4102	Managerial Economics Financial Accountancy	and	HS	3	3	0	0	30	70	100		
#PE-III	Professional Elective -I	II	PE	3	3	0	0	30	70	100		
#PE-IV	Professional Elective -IV		PE	3	3	0	0	30	70	100		
#PE-V	Professional Elective -V	/	PE	3	3	0	0	30	70	100		
#OE-III	Open Elective-III		OE	3	3	0	0	30	70	100		
#OE-IV	Open Elective-IV		OE	3	3	0	0	30	70	100		
B20EE4113	Electric Vehicles Labor (Skill Oriented Course	atory e)	SOC	2	1	0	2) i f	50	50		
B20EE4114	Industrial/Research Inte 2 Months	ernship	PR	3	_				50	50		
	TOTAL 23 19 0 2 180 520 700											

	Course Code	Course					
	B20EE4101	Electric Vehicles					
#PE-III	B20EE4102	Power System Operation and Control					
	B20EE4103	Digital Design and Computer Architecture					
	B20EE4104	MOOCS- 1					
	B20EE4105	Energy Storage and Battery Management Systems					
	B20EE4106	Smart Grid					
#PE-IV	B20EE4107	ARM Microcontrollers					
	B20EE4108	MOOCS - 2					
	B20EE4109	Power Electronics for Renewable Energy					
	B20EE4110	Switchgear and Protection					
#PE-V	B20EE4111	Introduction to Unmanned Aerial Vehicles					
	B20EE4112	MOOCS - 3					
#OE-III &	Student has to stu	dy one Open Elective each from OE-III & IV offered by AIDS or					
#OE-IV	CE or CSBS or CSE or ECE or IT or ME or S&H from the list enclosed.						

Cou	rse Co	se Code Category L T P C I.M H								
B2	0HS41	02 HS	3	0	0	3	30	70	3 Hrs.	
					1		L	I	1	
		MANAGERI	AL ECONO	OMICS A	ND FIN	IANCIAL	ACCOUNT	ΓANCY		
			(C	ommon t	o ECE &	EEE)				
Cours	se Obje	ectives:								
1.	1. To Study Managerial Economics and Demand Analysis									
2.	To familiarize about the Concepts of Cost and Break-Even Analysis.									
3.	To understand the nature of markets and to know the Pricing Policies									
4.	To l	earn about accou	nting cycle	and prepa	aration of	f Financia	Statements.			
5.	Tok	now the concept	of Capital	and sourc	es of rais	sing and S	tart-ups			
Cours	se Outo	comes: At the en	d of the cou	rse, Stude	ents will	be able to				
S No				Outoor	mo				Knowledge	
5. NU				Outcol	ne				Level	
1	Equip	oneself with	the knowle	edge of	estimatin	ig the De	emand and	demand	V2	
1.	elastic	cities for a produ	ct.						<u>K</u> 2	
2.	Have	knowledge of Co	ost and its ty	pes and a	ability to	calculate	BEP		K3	
3.	Under	rstand the nature	of different	markets					K2	
4.	Unde	rstand Pricing Pr	actices prev	ailing in	today's b	usiness wo	orld		K2	
5.	Prepa	re Financial State	ements and	kno <mark>w h</mark> ov	w to calc	ulate Profi	t & Loss for	a firm	K3	
6.	Know	Types of capita	l, their sour	ces & sta	rt-ups				K2	
	1	35-34	ENG	INE			JLLEU			
	Este	.1980		SYL	LABUS	OMOU	5			
		Introduction to	Manageria	al Econor	mics and	demand	Analysis:			
		Managerial Ec	onomics: D	efinition	of Econo	omics & C	lassification	of Econo	omics (Micro	
		& Macro), Mean	ning, Nature	e, & Scop	e of Man	agerial Ec	conomics.			
	irs)	Demand Analysis: Concept of Demand, Determinants of Demand, Demand schedule,								
		Demand curve,	Law of D	emand a	nd its ex	foreceptions.	Elasticity c	of Deman	a, Types of	
		Elasticity of Del	manu. mipu		uemanu	Torecastin	g and its wie	mous.		
		Cost Analysis [.]	Importance	of cost a	malysis	Types of	Cost- Actua	l cost Vs	Opportunity	
		cost. Fixed co	st Vs Var	iable cost	st. Expli	icit Vs I	mplicit cost	. Histori	cal cost Vs	
		Replacement co	st, Increme	ntal cost	Vs Sunk	cost; Elei	nents of cos	ts - Mate	erial, Labour,	
		Expenses; Metl	nods of cos	s ting - Jo	ob costin	ig, contrac	ct costing, F	rocess co	osting, Batch	
(10 H	lrs)	costing, Unit	costing, So	ervice c	osting,	Multiple	costing. B	sreak-eve	en analysis:	
		Determination	of Breakev	en point	- Appl	ications,	Assumption	s and Li	mitations of	
Break -even analysis (Theory only)										
UNIT	ъ п	Introduction to	Markets &	& Pricing	Policies					
(10 H	rs)	Market Struct	ures: Salier	nt Feature	es of Per	fect Com	petition, Mo	nopoly, 1	Monopolistic	
(~,	competition, Oligopoly and Duopoly. Pricing: Importance of pricing and its meaning;								

		Methods of Pricing: Cost Based -Full cost, Mark-up, Marginal &Break-even Demand									
		Based - Penetrating, Skimming; Competition Based- Going rate, Sealed Bid, Discount;									
		Internet Pricing - Flat-rate, Usage sensitive									
	Introduction to Financial Accounting:										
UNI	Γ-ΙV	Importance of Accounting - Double Entry System of Accounting - Types of Accounts -									
(08 H	lrs)	Journal, Ledger, Trail Balance, Trading Account, Profit and Loss Account and Balance									
		Sheet (outlines only).									
		Capital & Start-ups: Types of Capital - Fixed capital & Working Capital, Components									
UNI	T-V	of Working Capital, Factors influencing Working capital, Methods of Raising Finance.									
(12 F	- Irs)	Business Startups: Meaning, Definition, Types, Benefits, Challenges, Limitations and									
(115)	Disadvantages of Startups in India; Ideas- Sources and Techniques of generating new									
		ideas.									
Text	Books	:									
1	AR	Aryasri, Managerial Economics and Financial Analysis, TMH Pvt. Ltd, New Delhi									
2	Dr.	N.Appa Rao, Dr.P. Vijayakumar: Managerial Economics and Financial Analysis',									
2	Ceng	gage Publications, New Delhi									
3	Arya	Kumar: "Entrepreneurship", Pearson Publishing House, New Delhi 2012									
Refer	ence l	Books:									
1	Dr.B	.Kuberudu & T.V. Ramana : Managerial Economics and Financial anaysis,									
1	Hima	alaya Publishing House									
2	Vars	hney R.L, K.L Maheswari, Managerial Economics, S. Chand & Company Ltd,									
3	Shas	hi K. Gupta & R.K. Sharma Management Accounting, Kalyani Publishers									
4	Mah	eswari S.N, An Introduction to Accountancy, Vikas Publishing House Pvt Ltd									
5	VSP	Rao, Kuratko: "Entrepreneurship", Cengage Learning, New Delhi									

Cour	se Code	e Category	L	Т	Р	С	I.M	E.M	Exam
B20EE4101		PE	3			3	30	70	3 Hrs.
		·							·
	ELECTRIC VEHICLES								
				(F	For EEE)				
Cour	se Obje	ctives: Students	will learn	about					
1.	The int	roductory conce	pts of EVs	and dyn	amic mo	delling eq	uations o	f EVs	
2.	The va	rious configurati	ons of EVs	s and HE	EVs and p	ower train	n compon	ents.	
3.	Variou	s Energy storage	systems for	or EVs a	nd under	stand their	r characte	eristics	
4.	The Dr	ive systems of E	Vs and the	eir contro	ol				
5.	About	the charging tech	nnology in	EVs.					
Cour	se Outc	omes: Students	will be able	e to					
S. No	,			Oute	ome				Knowledge
5.110				oute					Level
1.	Analy	ze and underst	and dyna	mic mo	delling	and desig	gn consi	derations of	K4
2	Electr	ical venicles.	una of alaa	tuio vohi	alac and	nouvou tuoi		ponto	
Ζ.	Fyolu	ate the architect	ure of elec	tric vem	cles and	power trai	n compo	other energy	КЗ, К4
3.	storag	e methods for EV	Je Je	arameter	SIOPEV	's and und	lerstand	other energy	K4
	Analy	ze and understau	nd the elec	tric driv	es using	power ele	ctronic c	onverters for	
4.	EVs.								K4
5.	Illusti	ate the EV char	ger infrastr	ucture.	FFF	ING	COL	IFGE	K3, K4
	3				ALIT	NIOM	NIK		
	E	std. 1980		SY	LLABU	5			
]	NTRODUCTIO	ON TO EI	ECTRI	C VEHI	CLES AN	ND MOD	ELING	
]	ntroduction to E	Electric Vel	hicles (E	EV), Hyb	rid Electri	c Vehicl	es (HEV), EV	/ History, EV
	1	Advantages, ove	erall Com	parison	of EV	with Inter	rnal Con	nbustion Eng	gine vehicles,
UNI	T-I 1	easibility of EV	, Vehicle	Mechan	ics and I	Dynamics	modellin	g-Roadway F	Fundamentals,
(10H	Irs)	Laws of Motion	, Vehicle 1	Load Fo	rces, Vel	nicle Kine	etics, Dyi	namics of Ve	hicle Motion,
		Propulsion Powe	r, Force-V	elocity	Characte	ristics, M	aximum	Gradeability,	Velocity and
	4	Acceleration for	Constant	Iractive	Force of	1 Level R	oad, Gen	eral Accelera	tion for Non-
	(constant Tractive	Force, Pro	opulsion	System	Design, De	esign Coi	isiderations.	
		DCUITECTI	DE OF FY	VC AND	DOWE		COMP	NIENTE	
	1	Architecture of F	RE OF E V $S = 2 - m$	otors A	motors	n INAIN	COMPC	DINEINIS	Hybrid EVs _
UNI	т-п	Series Parallel	Series na	rallel c	notors, a complex	Plug_in	Hybrid F	Electric Vehi	$\frac{1}{2} \frac{1}{2} \frac{1}$
(10)	Hrs)	Architecture of	Fuel cell	EV Po	ompiex, ower trai	n compo	nents of	EVs - EV	Transmission
		Configurations '	Fransmissi	on Com	popents	Ideal Ges	urbox: St	eady State M	odel, and EV
	1	Motor Sizing. Sta	andard Driv	ve Cvcle	es.				
				- cycre					
UNI	Γ-III	ENERGY STO	RAGE FO	R EV					
(10 I	Hrs)	Battery- Battery	Basics, Di	ifferent	types, Le	ad Acid I	Batteries	and Lithium	Batteries (Li-

		ion, Li-Polymer), Battery Parameters, Battery Power, Battery modelling, Battery Failure						
		and Protection, Battery Management system, Battery Pack Design, Lifetime and Sizing,						
		Fuel cell, Hydrogen Storage Systems, Ultra capacitors, Flywheel.						
		ELECTRIC VEHICLE MOTOR DRIVES						
		Electric Drive Components of EV, Permanent Magnet Synchronous Motor (PMSM) Drive						
UNI	T-IV	- PMSM motor operation using simple controller, Brushless DC (BLDC) Motor Drive -						
(10)	Hrs)	BLDC motor operation, model, DC link current control, Comparison of PMSM and						
		BLDC, Switched Reluctance Motor (SRM) Drive - SRM motor operation, Converter						
		topologies for SRM.						
		EV CHARGING TECHNOLOGY						
		Overview of the EV battery charging system, Infrastructure Needed for Charging Electric						
UN	IT-V	Vehicles, Basic Requirements for Charging System, Charger Architectures-AC charger,						
(10	Hrs)	DC Charger, Basics of Wireless charging - Static and Dynamic charging, EV Charging						
		Standards and Technologies, Effects of EV load on the Grid, Introduction to V2G and						
		V2V technologies.						
Text	books:							
1.	Iqbal	Husain, "Electric and Hybrid Vehicles: Design Fundamentals", CRC Press, Taylor &						
	Franc	is Group, 2003.						
2.	John	G. Hayes and G.A. Goodarzi, "Electric Power train - Energy Systems, Power electronics						
	and d	rives for Hybrid, electric and fuel cell vehicles" Wiley Publication, 1st edition, 2018						
Refe	rence	Books:						
1.	James	s Larminie, John Lowry, "Electric Vehicle Technology Explained" Wiley publication, 2 nd						
	Editic							
2.	M. El	nsani, Y. Gao, and A. Emadi, "Modern Electric, Hybrid Electric, and Fuel Cell Vehicles",						
	CRC	Press, 2010.						

Cour	se Cod	e Category	L	Т	Р	С	I.M	E.M	Exam
B20	EE4102	PE	3			3	30	70	3 Hrs.
	POWER SYSTEM OPERATION AND CONTROL								
	(For EEE)								
Cours	Course Objectives: Students will learn about								
1.	1. The stability enhancement methods, preventive & emergency control and Contingency Analysis								
2.	2. The optimal dispatch of generation with and without losses								
3.	The op	timal scheduling	of hydroth	ermal sy	ystems ar	d unit cor	nmitmen	t	
4.	The lo	ad frequency con	trol for sin	gle area	system w	ith and w	ithout co	ntrollers.	
5	The lo	ad frequency cor	ntrol for tw	o area s	ystem wi	th Tie-lin	e bias, E	conomic disp	patch control,
5.	automa	tic voltage contr	ol, generat	or const	raints and	l governor	dead bar	nd.	
Cours	se Outo	omes: Students	will be able	e to					
S. No				Outco	ome				Knowledge
50110									Level
1.	Explo	re stability enha	ancement r	nethods,	preventi	ve & em	ergency	control and	K3, K4
	analy	ze contingency o	f power sy	stem.	6 771				17.4
2.	Optin	lize the economi	c load sche	auling c	of Therma	al power p	lants.		K4
3.	must	ate the concepts	of hydro t	nermal s	chedulin	g and unit	commitr	nent.	K3
4.	Analy	ze the frequency	deviations	of a sin	gle area j	power syst			K 4
5	Analy	ze the Load free	juency con	trol of a	two-area	t system v	vith tie-li	ne bias and	V2 V4
5.	contro	are generator c	.onstraints,	govern	oi ueau	Danu and	i automa	uie voltage	K3,K4
	contro	td. 1980			AUTC	NOM)US		
				SYI	LABUS				
		Introduction To	Power Sv	stem Or	peration	& Contro	1		
		Power system	operating	states.	Equality	and Ine	uality o	constraints.	Concepts of
UNI	T-I	preventive and	emergency	contro	l, Coher	ent area	dynamic	s, Stability	enhancement
(10E	Irs)	methods, long te	erm frequei	ncy dyna	amics, A	verage sy	stem free	juency, Cent	re of inertia.
		Factors affecting	g the powe	r syster	n securit	y, Contin	gency ar	alysis, Line	ar sensitivity
		factors, Continge	ency selecti	on.					
		Optimal System	Operation	1					
UNI	T-II	Introduction, Op	timal oper	ation of	generato	ors on a b	ous bar, (Generator op	perating cost,
(10 H	Hrs)	Optimal generat	ion withou	t consid	lering lo	sses, Opt	imal gen	eration sche	duling using
		amda iteration	technique	, Repre	sentation	of trans	smission	loss by B	-coefficients,
		Jerryation of trai	IISHIISSION I	USS IOIN	1018.				
	·	Hydro - Thorma	Schodul	na					
TINIT		Ontimal schedul	ing of Hyd	ng Irotherm	al Sveter	n• Mathe	matical f	ormulation	- Solution of
(10 F	Hrs)	vdrothermal sch	neduling pr	oblem	ar Syster		ination 1		Solution of
		Optimal Unit Co	ommitmen	t					

		Need for unit commitment, Constraints in unit commitment, Cost function formulation, Solution methods, Priority ordering, Dynamic programming						
		Solution meurous, i nonty ordering, 2 yname programming.						
UN (10	 Load Frequency Control-I Introduction, Load frequency control (Single area case), Turbine speed governing system, Model of speed governing system, Turbine model, Generator load model, Complete block diagram representation of an isolated power system, Steady state analysis, Dynamic response, Control area concepts, Proportional plus integral control of single area. 							
UN (10	UNIT-V (10 Hrs)Load Frequency Control-IITwo area load frequency control and its block diagram, Load Frequency Control of two area system uncontrolled case and tie line bias controlled case. Load frequency control and economic dispatch control. Automatic voltage control, Load frequency control with generation rate constraints, Speed Governor dead band and its effects on AGC.							
T	4							
Tex	tbooks	rn Dower System Analysis by LLNegroth & D. D.Kotheri Tate McCrow Hill Dublishing						
1.	Comp	any ltd, 2 nd edition.						
2.	Electrical Energy Systems Theory - by O.I. Elgerd, Tata McGraw-Hill Publishing Company Ltd, 2 nd edition.							
Ref	erence	Books:						
1.	1. Power Generation, Operation and Control - by A.J. Wood and B.F. Wollenberg, John Wiley& sons Inc. 1984.							
2.	Power	System Analysis by Hadi Sadat, Third Edition, Tata McGraw Hill publication.						
		Estd. 1980 AUTONOMOUS						

Cour	ourse Code Category L T P C I.M E.M							Exam		
B20EE4103		PE	3			3	30	70	3 Hrs.	
			I		I		1			
	DIGITAL DESIGN AND COMPUTER ARCHITECTURE									
				(F	for EEE)					
Cour	se Obje	ctives: Student v	will learn a	bout						
1.	1. System Verilog hardware description language for digital system design.									
2.	2. Thebuilding blocks used in digital systems.									
3.	The ar	chitecture of a di	gital comp	uter.						
4.	The m	croarchitecture of	of a process	sor.						
5.	Memor	y systems and In	nput/Outpu	t system	s.					
I										
Cour	se Outo	omes: Student w	vill be able	to						
S No				Outo	0.000				Knowledge	
3. INO				Oute	ome				Level	
1	Acqui	re the knowledg	ge for the d	lesign of	combina	ational, se	quential	logic circuits	К3	
	using	system Verilog I	HDL.							
2.	Explo	re the arithmetic	e circuits, c	ounters,	shift reg	isters, me	mory arra	iys and logic	K3	
	arrays		_	1.1.		<u> </u>	<u> </u>			
3.	instru	ate MIPS digita	l computer	architec	ture in te	rms of ins	truction s	set, registers,	K3	
	Explo	rethe single cyc	ele multic	vele and	l pipelin	e microar	chitecture	es for MIPS		
4.	proces	sor.		yere une	i pipeiii				K4	
~	Explo	re different mer	nories, inp	out output	ut device	s and prin	nciples o	f interfacing	W2	
5.	I/O de	vices to a proces	sor.						K3	
				SY	LLABU	5				
]	Hardware Desci	ription La	nguage	(System	Verilog)				
]	ntroduction-Mo	dules-Lang	uage	Origins-	Combinati	onal L	ogic-Bitwise	Operation-	
UNI	т.т	Reduction Operation-Conditional Assignment-Internal Variables-Precedence-Number								
(10F	Irs)	Z's and X's –I	Bit Swizzl	ing-Dela	ays-Struc	tural Mo	delling-S	equential Log	gic-Registers-	
(202]	Resettable Regis	ters-Enabl	ed regis	ters- Mu	ltiple regi	isters- La	tches-More of	combinational	
]	Logic-Case stat	ement-If	statemer	nt- Trut	h table	with D	on't Cares-E	Blocking and	
	1	nonblocking assi	gnments- F	Finite sta	te Machi	ne-Data ty	/pes.			
			DI :							
]	Digital Building	Blocks							
		Arithmetic circ	cuits -Ad	dition-S	ubtractio	n-compara	ators-AL	\cup -shifters a	nd rotators-	
UNI	г-н	Multiplication-D	ivision-Nu	mber sy	stems -	Fixed po	int num	per system-F	loating point	
(10 F	Irs)	number system-	Sequential	buildin	g block	- Counter	s-Shift 1	egisters-Mem	ory arrays -	
(101]	Dynamic Rando	m Access	Memory	(DRAN	1)- Static	Random	Access mem	ory-(SRAM)-	
		Areas and dela	ys-Read	only me	emory-Lo	ogic using	g memo	ry arrays-Lo	gic arrays -	
]	Programmable lo	gic arrays-	Field pr	ogramma	ble arrays	-array im	plementation	n.	

UNI7 (10 F	ArchitectureMIPS architecture -Assembly language-Instructions-Operands- Machine language-R typeinstructions-I type instructions-J type instructions instruction-Interpreting machineIanguage code-The power of stored program-Programming-arithmetic/logical instructions-Irs)branching-conditional statement- getting loopy-arrays-functional calls -addressing modes-The memory map-translating and starting a program- odds and ends- Real worldperspective: x86 architecture-x86 registers-x86 operands-status flags-x86 instructions-x86instruction encoding.						
	Micro architecture						
UNIT (10 F	NIT-IV I0 Hrs)Introduction- Architectural state and instruction set-Design procedure-Microarchitecture- Performance analysis-Single cycle processor-single cycle data path-Single cycle control- More instructions-Performance analysis-Multicycle processor- Multicycle data path- Multicycle control-Performance analysis-Pipelined Data path-Pipelined control-Hazards- Mpre instructions-Performance analysis- spatial and temporal parallelism, latency and throughput calculations.						
UNI (10 F	Memory Systems And Input Output SystemsMT-VIntroduction-Memory system analysis-Caches-Virtual memory -Address translation-Pagetable -Translation look a side buffer-Memory protection -Replacement policies -Multilevelpage table - Memory mapped I/O-Embedded I/O systems-PC I/O systems.						
1.	Digital Design and Computer Architecture, ARM Edition, Sarah L. Harris & David Money Harris, Morgan Kaufmann Publishers, 2016 edition.						
Refer	ence Books:						
1.	Computer Organization and Design ARM Edition, The Hardware Software Interface, David A Patterson and John L. Hennessy, 2016						
2.	2. Computer Architecture: A Quantitative Approach, David A Patterson and John L. Hennessy Sixth Edition, 2017						
e-Res	ources						
1.	Digital Design and Computer Architecture Course by Prof. Onur Mutlu at ETH Zurich, https://safari.ethz.ch/digitaltechnik/spring2022/doku.php						
2.	ARM Graphical Micro-Architecture Simulator, https://github.com/arm-university/Graphical-Micro-Architecture-Simulator						

Cour	ourse Code Category L T P C I.M E.M								Exam	
B20EE4105		PE	3			3	30	70	3 Hrs.	
	ENERGY STORAGE AND BATTERY MANAGEMENT SYSTEMS									
	(For EEE)									
Cour	Course Objectives: Students will learn about									
1.	1. The Energy storage systems and its techno economical aspects									
2.	2. The classification of Energy Storage Systems (ESS) and their assessment									
3.	The ba	tery types and th	neir princip	oles of op	peration					
4.	The ba	tery parameters,	componen	nts and f	unctiona	lity of Bat	tery Man	agement Syst	tem (BMS)	
5.	The me	asurement, prote	ection, bala	ancing, t	hermal n	nanagemen	nt and saf	ety precaution	ons of BMS	
Cour	se Outc	omes: Students	will be able	e to						
S. No				Outco	ome				Knowledge	
1			· 1			- <u>Q</u>	. 1	1 .	Level	
1.	Evalu	ate the techno-ed	conomic cl	naracteri	stics of I	inergy Sto	rage tech	nologies	K3	
2.	Explo	reenergy storage	systems, a	assessme	ent and c	omparison	1 11'		K3	
3.	Explo	re different type	s of batteri	es, batte	ry termii	lology and	i modellii	1g	К3	
4.	topolo	gy	paramete	ers and	BINIS .	lunctionan	ity, techi	lology and	K3	
5	Explo	re the measurem	ent, protec	ction, the	ermal ma	nagement.	, commur	nication and	K3	
	safety	aspects of BMS	_							
			EIV	GIN	EE		CUL	LEGE		
	E	td. 1980		SY.		SNOM	ous			
		TECHNO-ECO	NOMIC A	NALY	515		Definiti			
UNI		ntroduction -	Electrical	Energy	y Stora	ge (EES)	-Definition	on-Role, El	Einangial	
(10E	Irs)	components, Applications and Technical benefits of energy storage systems, Financial Benefits of energy storage systems. Techno economic characteristics of energy storage								
	5	vstems.	storage	system	5, 100m		ie enarae		storage	
		<u> </u>								
]	ENERGY STO	RAGE SY	STEMS						
TINIT	т п (Classification –	Pumped H	Iydro sto	orage, ba	atteries en	ergy stor	age, flow ba	atteries energy	
	1-11 Jrc)	torage, flywhee	el energy	storage,	super	capacitor	energy s	storage, Sup	er conducting	
	115)	nagnetic energy	storage,	Fuel cel	l Hydro	gen energ	y storage	e, thermal e	nergy storage,	
	C	ompressed air e	nergy stora	ige, asse	ssment a	nd compar	rison of e	nergy storage	e technologies.	
	I									
		ELECTRO CH	EMICAL	STORA	GE	1.1-				
TINIT		standard Batterio	es-Lead A	Acid- Va	alve Reg	ulated Le	ad Acid	(VRLA) - N	i-Cd, Modern	
	I-III I J ma)	Satteries- Ni-M	H – L1-I	on, Flo Dringinle	w Batte	eries – B	$r_2 Zn - v$	anadium R	edox, Battery	
	.115) (omposition, con he above batteri	suucuoli,	Termi	ology	auoii, Typ characteris	es, Auva	ntagesallu uli edance Mod	els - Warburg	
	i	mpedance	co, Dattel	y i Ci IIIII	lology,		nes, mp		sis - waibuig	
		r								

		BATTERY MANAGEMENT SYSTEMS-I								
		Battery parameters - Capacities, Depth of Discharge (DOD) -State of Charge (SOC) -								
UNIT	-IV	State of Estimation (SOE) - State of Health (SOH) - State of Function (SOF), BMS -								
(10 H	Hrs)	Functionality – Constant Current Constant Voltage (CCCV) chargers, Regulators, Meters,								
		Monitors, Balancers and Protectors. Technology-Analog, Digital and their comparison.								
		Topology - Centralized, Modular, Master-Slave, Distributed & their comparison								
TINITT	1 7	BATTERY MANAGEMENT SYSTEMS-II								
	- V ma)	Measurement of Voltage, Temperature & Current, Protection, Balancing & Thermal								
(10 П	rs)	Management, Evaluation, External Communication, Safety & Precautions.								
Textb	ooks:									
1	Ener	gy Storage Technologies and Applications, Ahmed Faheem Zobaa, InTech Publishers,								
1.	2013	3.								
2	Lithi	um Batteries and Other Electrochemical Storage Systems, Christian Glaize, Sylvie Geniès,								
۷.	ISTE	E & John Wiley, 2013.								
3	Davi	wide Andrea, Battery Management Systems for Large Lithium-Ion Battery Packs, Artech,								
5.	2010).								
Refer	ence l	Books:								
1	Jiang	g, J. and Zhang, C., Fundamentals and Applications of Lithium-ion Batteries in Electric								
1.	Driv	e Vehicles, 2015, Wiley Publications.								
r	Ang	el Kirchev, Battery Management and Battery Diagnostics, Ch.20 of Electrochemical Energy								
۷.	Stora	age for Renewable Sources and Grid Balancing, Elsevier, 2015.								
e-Res	ource	sace								
1.	https	:://www.coursera.org/learn/battery-management-systems								
2.	https	://nptel.ac.in/courses/108106170								
		LOCUL TO OO								

Cou	rse Code	Category	L	Т	Р	С	I.M	E.M	Exam		
B20	EE4106	PE	3			3	30	70	3 Hrs.		
SMART GRID											
(For EEE)											
Course Objectives: Students will learn about											
1.	1. The concept of smart grid and its advantages over conventional grid										
2.	. Different smart grid technologies and wide area monitoring systems										
3.	The phasor measurement unit and intelligent electronic devices										
4.	The micro grid concept and different storage systems										
5.	The advancedmetering infrastructure, communication technologies and cyber security in smart grids										
Cour	se Outco	mes: Students	will be able	e to							
S. No				Outc	ome				Knowledge Level		
1.	Explo	e the evolution	and function	ons of th	e smart g	rid.			K3		
2.	Illustr	ate the technolo	gies used i	n smart j	grid.				К3		
3.	Illustr	ate the function	s an <mark>d us</mark> e o	f PMUs	and IED	s.			K3		
4.	Explo	e the conceptor	micro gric	l and inte	egration of	of renewal	ble energ	y <mark>systems.</mark>	K3		
5.	Explor grids	e modern com	nunication	technol	ogies and	l cyber se	curity iss	ues in smart	K3,K4		
			EN	GIN	EEF	RING	<u>COL</u>	<u>LEGE</u>			
	E	td 1980		SY	LLABUS	BNOM	OUS				
	I	ntroduction To	Smart Gi	rid							
UNI (10H	T-I F Irs)	Evolution of Electric Grid, Smart Grid-Definition, Concept, Need of Smart Grid, Functions of Smart Grid, Opportunities of Smart Grid, Challenges in Implementation of Smart Grid, Difference between Conventional and Smart Grid, Concept of Resilience and self-healing									
	5	in neuring.									
UNI' (10 I	Smart Grid Technologies and WamsIntroduction to Smart Grid Architecture, Components of Smart Grid, Remote TerminalUNIT-IIUnits (RTU)-Architecture, RTU in Distribution System, Supervisory control and Data(10 Hrs)Acquisition(SCADA),Distribution Management system(DMS),DMS Functional Layers, Smart Substation, Wide Area Monitoring System(WAMS).Comparison between SCADA and WAMS.								ote Terminal rol and Data ional Layers, veen SCADA		
	_			(
UNI7 (10 F	NIT-III 0 Hrs)Phasor Measurement Units Concept of Phasor Measurement, Phasor Measurement Unit (PMU)-Features – Fundamentals, Global Positioning Satellite (GPS) Systems, Synchro phasor –Definition- Measurements, Applications of PMUs in Power Systems. Intelligent Electronic Devices (IED)-Functions-Advantages										

		Micro-Grid								
UNIT (10 H	Г-IV Hrs)	Concept of Micro grid, need & applications of Micro grid, Structure of Micro grid, Control of micro grid-Master slave mode, Peer-Peer mode and Combined mode, basic concept of fuel cell, Pumped hydro, Compressed air storage, Integration of Renewable Energy Sources								
		Smart Metering And Communication Technologies								
UNI	T-V	Introduction, Smart Meter Systems-Benefits, Advanced Metering Infrastructure (AMI),								
(10 H	Hrs)	Local Area Network (LAN), Home Area Network (HAN), Wide Area Network (WAN),								
		Broadband Over Power Lines (BPL)-Objectives & Features, Cyber security for smart grids								
Textb	ooks:									
1	Bhar	at Modi, Anu Prakash, Yogesh Kumar, "Fundamentals of Smart Grid Technology", Katson								
	book	s,2016.								
2.	Hars	h Tanwar, Bhavishya Mittal, Bhawana Chouhan,"Smart Grid Technology", Genius								
	Publ	ications,2016.								
Refer	ence l	Sooks:								
1.	Stua	rt Borlase, "Smart Grid: Infrastructure, Technology and Solutions", CRC Press, 2012.								
2	Li	Fusheng,Li Ruisheng, Zhou Fengquan "Microgrid Technology and Engineering								
	Appl	lication", Elsevier, 2016.								
e-Res	ource	s								
1.	https	://nptel.ac.in/courses/108107113								
2. https://archive.nptel.ac.in/courses/117/107/117107148										
	1	ENGINEERING COLLEGE								
		Estd. 1980 AUTONOMOUS								



Cour	se Code	Category	L	Т	Р	С	I.M	E.M	Exam			
B20	EE4107	PE	3			3	30	70	3 Hrs.			
ARM MICROCONTROLLERS												
	(For EEE)											
Cour	Course Objectives: Students will learn about											
1.	1. The architecture of ARM Microcontroller.											
2.	2. The instruction set of ARM Microcontroller.											
3.	. The THUMB instruction set.											
4.	The handling of exception and interrupts.											
5.	The int	erfacing of perip	heral devic	es to AR	M Micro	controller						
Cour	se Outco	mes: Students v	will be able	to								
S. No				Outc	ome				Knowledge			
1	T11 (6.0			11			Level			
1.	Illustr	ate thearchitectu	ire of Corte	x-M M10	crocontro	oller			K3			
2.	Make	use of the instru	ction set of	ARM N	licrocont	roller	_		K3			
3.	Make	use of the THUI	MB instruc	tion set		1 . 1 .		. 11	K3			
4.	Demo	strate the exce	ptions and	interrupt	schemes	related to	microcoi	ntrollers	K4			
5.	Intert	ice the periphera	alsto micro	controlle	rs				K4			
	- 44			C V/								
		the local To	Cart I	SY		HNG	COL	LEGE-				
TINI		he APM Corte	v M Proce	MICFOC	ontrollel		struction	Set Architect	tura Pagistar			
(10F	III P	et Processor (Dnerating	Modes	Interrupt	s and Pro	ocessor 1	Set Architect	ce Pipelined			
(101		rchitecture and	Data Path.	Memory	Address	Map.		Reset Bequen	ee, ripenneu			
			, , ,									
		he Arm Micro	controller	Instruct	ion Set							
UNI (10 I	I-II Jr c) I	Introduction, Data processing instructions, Load - Store instruction, Software interrupt										
(101	iiis)	nstructions, Prog	gram status	register	instructio	ons, Loadi	ng consta	nts, Condition	al Execution.			
	1	he Thumb Inst	truction Se	et								
UNI	Γ-III I	ntroduction, TH	IUMB reg	gister us	age, AR	M - TH	IUMB in	nterworking,	other branch			
(10 ł	drs) 1	structions, Da	ita proces	sing in	struction	s, Stack	instruct	ions, Softwa	are interrupt			
	1	istructions.										
	T	vcentions And	Interrunt	2								
		lested Vectored	Interrupt	, Controlle	er. Excer	ntion Type	es. Excen	tion and Inter	rupt Priority			
UNI	Г-IV І	nterrupt States.	Interrupt C	Configura	tion-Bas	ic Interrut	ot Config	uration. Interr	upt Masking.			
(10 I	Hrs) S	etting Up Inter	rupt Vecto	r Table,	Configu	ring an Ir	nterrupt,	Handling of I	Exceptions or			
	I	nterrupts-Regist	er Stacking	g in Re	sponse t	o Interrup	ot Occur	ence, Updati	ng Registers,			
	E	xception Exit of	Return, In	terrupt L	Latency.	-		-				

UNIT V	Serial Communication Interfaces
(10 Hzr)	UARTs, I2C Interface, Serial Peripheral Interface (SPI), Controller Area Network (CAN)
(10 Hrs)	Interface.

Textb	Textbooks:							
1	ARM® Microprocessor Systems, Cortex®-M Architecture, Programming, and Interfacing,							
1.	Muhammad Tahir and Kashif Javed, CRC Press Taylor & Francis Group.							
2.	ARM System Developer's guide -Andrew N. SLOSS, ELSEVIER Publications, ISBN 978-81-							
	8147-646-3, 2016.							
3	Fundamentals of System-on-Chip Design on Arm Cortex-M Microcontrollers, René Beuchat,							
5.	Florian Depraz, Andrea Guerrieri, Sahand Kashani, Arm Education Media.							
Refer	ence Books:							
1.	ARM Assembly Language – William Hohl, CRC Press, ISBN:978-81-89643-04-1							
C	Embedded Microcomputer Systems- Real Time Interfacing, Third Edition, Jonathan W. Valvano,							
۷.	University of Texas at Austin							
e-Res	ources							
1.	www.Arm.com							
2	ARM University Program-							
Δ.	https://www.arm.com/resources/education/education-kits/introduction-to-soc.							



Cour	se Code	Category	L	Т	Р	C	I.M	E.M	Exam		
B20	EE4109	PE	3			3	30	70	3 Hrs.		
	POWER ELECTRONICS FOR RENEWABLE ENERGY										
	(For EEE)										
Cours	Course Objectives: Students will learn about										
1.	1. The importance of renewable energy sources and need of Power Electronics for their use.										
2.	2. The Power electronic topologies in PV systems and their importance.										
3.	3. The Power electronics for Type - 3 and Type - 4 wind energy system.										
4.	4. The basic configurations of power conversion system for Fuel cells.										
5.	The requ	irements and is	sues of gri	d integra	tion of re	enewables.					
Cours	se Outcor	nes: Students v	vill be able	to							
S. No				Outco	ome				Knowledge		
5110				oute	,				Level		
1.	Identify	the use of pow	ver electron	ics to re	newable	energy sou	urces.		K3		
2.	Apply efficient	the fundament	tals of Po	ower Ele	ectronics	to enhar	nce the	PV system	K3		
3.	Choose	power electro	nic topolog	gies for	Type –	3 and Ty	pe – 4 w	ind energy			
	systems	Contraction of the second		-					K3		
4.	Apply I	DC – DC conve	rters for fu	el cells		7			K3		
5.	Explore	the grid integr	ation issue	s of PV	and Wind	l energy sy	y <mark>stem</mark> s		K3		
	199										
	2		EN	SY	LLABUS	5	COL	LEGE			
UNI (10H	T-I ren Irs) res At	troduction To assification of newable energ sources: Photo embrane Fuel tributes of pow	Renewabl Energy Sc y generation voltaic Sy Cell (PEM ver electron	e Energ ources – on on e stems-W IFC) - F ics for re	ynum Importar nvironmo /ind Ene &enewable	nce of ren ent - Qua rgy Syste e Energy energy sy	ewable e litative s ms-Fuel sources a stems.	nergy source tudy of rend Cells – Pro and their Int	s - impacts of ewable energy oton Exchange erconnections-		
	-										
UNI' (10 I	UNIT-II (10 Hrs)Photovoltaic Power ElectronicsPV cell- PV modules- Strings-Array- Mismatch losses-PV system configurations- Power electronic topologies in PV Standalone systems (Single stage and two stage)- Design of photovoltaic array for single and two stage standalone system based on the Sine PWM technique for inverter- MPPT operation with various DC-DC Converters(non-isolated)- Importance of inverter for two stage and single stage system-advantages and disadvantages of single stage and two stage systems.										
	1										
UNI] (10 I	W F-III Po Hrs) tur IV	ind Power Ele ower flow in bines, Wind ge systems, Back	etronics wind turbi enerators, P -to-Back P	ne, Pov Principle WM VS	ver Char of operat	acteristics tion PMSC Converte	of wind G, Power r using D	l turbine,Co electronics fo FIG and PM	ntrol of wind or Type-III and SM.		

		Fuel Cell Power Electronics								
UNIT	-IV	Power Electronic Converter for Fuel Cell, Basic configurations of power conversion system								
(10 F	Irs)	(PCS) for FC_DC-DC converters: non-isolated isolated (flyback converter). Bidirectional								
(101		converters								
		Dermar Electronica For Orid Laternation								
		Power Electronics For Grid Integration								
UNI	Г-V	Power electronics requirement forGrid connected system, Grid connected and Islanding								
(10 H	Hrs)	mode, Grid synchronization, PLLs(basic PLL operation), Grid connection Issues, Grid								
		control strategy for PV and Wind systems (dq current control of voltage source converters)								
Textb	ooks:									
1	Sudi	pta Chakraborty, Marcelo G. Simões, William E. Kramer, "Power Electronics for Renewable								
1.	and I	nd Distributed Energy Systems" Springer 2013.								
2.	Muk	und R Patel, "Wind and Solar Power Systems", CRC Press, 1stEdition, 1999.								
3.	SN E	Bhadra, D. Kastha, S. Banerjee, "wind electrical systems", OXFORD higher education, 2018								
Refer	ence I	Books:								
1	Solar	Photovoltaics, Fundamentals, Technologies and Applications, Chetan Singh Solanki, Third								
1.	Editi	on								
C	Powe	er Electronics: Circuits Devices and Applications - M.H. Rashid, Prentice Hall of India,								
2.	4 th ed	ition.								
		22011275								



Cours	se Code	Category	L	Т	Р	С	I.M	E.M	Exam		
B20E	EE4110 PE 3 3 30 70								3 Hrs.		
SWITCHGEAR AND PROTECTION											
	(For EEE)										
Cours	Course Objectives: Students will learn about										
1.	. The need for protection and basic principles of arc interruption.										
2.	The principle of operation, constructional features and testing of circuit breakers.										
3.	The principle of operation and protective relays.										
4.	The protection of power system components.										
5.	The bas	sics of numeric	al relays an	d substatic	ons.						
Cours	e Outco	mes: Students	will be able	e to							
S. No				Outcon	ıe				Knowledge Level		
1.	Illustra arcing p	ate the need for t	r protectio	n, rating c	of circuit b	reakers ai	nd analyz i	ing the	K3, K4		
2.	Explor	e different circ	uit breaker	s and their	testing.				K3		
3.	Illustra compu	te the behave te the operating	iour of d g times by 1	ifferent ty using time-	pes of el current cha	ectromagi aracteristic	netic relay cs.	ys and	K3, K4		
4.	Apply protecti	electromagnet	ic relays	to alternat	or, transfo	ormer, fee	eder and	busbar	K3		
5.	Explor	e protection wi	th numeric	al relays ar	nd illustrat	e substati	on compor	nents.	К3		
	1	- r		j			F				
				SYL	LABUS						
	Int	roduction To l	Protection	& Circuit	Breakers						
UNIT (10 Hi	C-I Frs) Nec Ess Me Vol	ed for protecti ential qualities thods of arc ex ltage (RRRV),	ve system of protecti tinction, R Resistance	s, Nature on, Switch estriking v switching,	and cause gear Equip roltage, Re Current ch	s of faul oment-Isol covery vo nopping, F	ts, Types ating Swit Itage, Rate Ratings of e	and eff ches. Fo e of Rise circuit b	ects of faults, rmation of arc, e of Restriking reakers.		
	-		D 1								
UNIT (10 H	NIT-II 0 Hrs)Types Of Circuit Breakers And Testing Principle of operation of circuit breakers, Classification of circuit breakers, Construction and working of Air Blast Circuit Breakers, Minimum Oil Circuit Breaker (MOCB), Puffer type SF-6 Circuit Breakers and Auto reclosure, Testing procedure and Indirect testing of Circuit Breakers.										
		· · · • •									
UNIT- (10 Hi	 (IT-III) Hrs) Protective Relays Types of relays, Classification of protective Schemes, Primary and Back-up protection, Basic relay terminology. Time–Current characteristics, Calculation of relay operating time-Simple problems, Principle of operation & construction of induction type over current relay, Directional over-current relay, Differential relays (Current balanced protection) & percentage differential relays. Universal torque equation, Distance relays – Impedance, Reactance and 										

UNI' (10]	T-IV Hrs)	Apparatus Protection Percentage differential protection and Protection against stator inter-turn faults of Alternators, Percentage differential protection and Protection against magnetizing inrush current of Transformers, Protection of Single and parallel feeders, Bus-Bar Protection, Protection against Surges.					
		Numerical Relaying And Sub - Stations					
TINIT		Numerical Protection - Block diagram of a typical numerical relay, Advantages and					
UNI (10 I	1-V	disadvantages of numerical relays, Block diagrams of Numerical over current, distance,					
	Hrs)	differential relays. Introduction to substation, Classification of substations, Substation					
	equipment, key diagram of 11KV/400V indoor substation, Substation earthing.						
Text	t Book	<s:< th=""></s:<>					
1	Pow	er System Protection and Switchgear by Badri Ram and D. N. Vishwakarma, Tata McGraw-					
1.	Hill	Education, Second Edition, 2017.					
2.	Swit	chgear and Protection by J.B Gupta, S.K Kataria& sons, 3 rd edition, 2013, reprint 2023.					
Refe	erence	e Books:					
1	Princ	ciples of Power System by V.K Mehta and Rohit Mehta, S. Chand publication, 3rd					
1.	editi	on,2005.					
2	Prote	ection and Switchgear by Bhavesh R. Bhalja R. P. Maheshwari Nilesh Chothani, Oxford					
۷.	Univ	versity Press, Second Edition, 2018.					



Estd. 1980

Cour	se Cod	e Category	L	Т	Р	C	I.M	E.M	Exam		
B201	E E411 1	PE	3			3	30	70	3 Hrs.		
INTRODUCTION TO UNMANNED AERIAL VEHICLES											
(For EEE)											
Cour	Course Objectives: Students will learn about										
1.	1. The overview of UAVs										
2.	2. The components and classification of UAVs										
3.	3. The concepts of Autonomous UAVs & Communication Infrastructure										
4.	The U	AV Data Collect	ion and Re	gulatory	systems						
5.	The ap	plications of UA	Vs								
Cour	se Out	comes: Students	will be able	e to							
S. No				Outc	ome				Knowledge		
									Level		
1.	Explo	ore the history, ch	haracteristi	cs and w	orking o	f UAVs.			K3		
2.	Ident	ify various comp	onents of U	JAVs an	id illustra	ate their p	urpose.		K3		
3.	Illust	rate the Autonom	nous UAV	s and Co	ommunica	ation Infra	structure.		K3		
4.	Explo	ore UAVs Data C	Collection a	ind Regu	llatory sy	stems.	_		K3		
5.	Explo	ore the applicatio	ns of UAV	technol	ogy				K3		
	\rightarrow			SY.				FRE			
TINI	тт	OVERVIEW O	F UNMAP	NNED A	ERIAL	VEHICL	ES a Tachni	ical Tarma	horostaristics		
(10F	I-I Irc)	of UAVs_Photos	rammetry		LiDAR	IAVs W	s, Technik orking of	a HAV-Alti	tude Vertical		
(101	11.5)	and Oblique Pho	d Oblique Photographs, Positioning. Advantages and Disadvantages of UAVs								
			<u>8</u>	001010111			1544 - 4				
		COMPONENTS	S OF UAV	'S							
		Basic Aerodynar	nics–Lift,	Drag, Tł	nrust, Sta	bility and	Control	of UAV-Rol	, Pitch, Yaw,		
UNI	T-II	Throttle, UAVs	Classificati	ion-Ope	ration &	Use, Size	, Altitude	e, Wings type	. Transmitter,		
(10 I	Hrs)	Receiver, Body Frame, Motors, Electronic Speed Controller (ESC), Propellers, Flight									
		Controller, The l	Payloads, (Gimbals	and Tilt	Control, S	Sensors,	Global Positi	oning System		
		(GPS), Remote S	ensing Ser	isors, Ba	ittery, Gr	ound Cont	trol Static	on (GCS).			
								DUCTUDE			
		The Automatic	SUAVSC and Auton			rchitectur	rrasi.		System The		
UNI	г-Ш	Concept of Au	tonomy. V	Jarious	Measure	s of UA	V Autor	omv- Fully	Autonomous		
(10 I	Hrs)	Operations, Sem	i-autonomo	ous Oper	rations, F	Fully Hum	an Opera	ted, UAV Co	ommunication		
,	,	System, Types o	f Commun	ication-	Wireless	s Sensor N	Jetwork (WSN) Syster	n, Free Space		
		Optical (FSO) A	pproach.				Ň	•	*		
UNI	Γ-ΙV	UAV DATA CO	OLLECTIO	ON ANE) REGU	LATORY	SYSTE	MS			
(10 I	Hrs)	Data Products– A	Aerial Rem	ote Sens	sing Data	, LiDAR	Data, Iss	ues of Concer	n for UAVs-		

		Air Traffic Control, Wi-Fi/Bluetooth Security Issues, Privacy Issues, Moral and Ethical				
		Issues, Safety Issues, Legal Issues, Aviation Regulations—India.				
		APPLICATIONS OF UAVS				
UN	IT-V	Overview of applications, Power sector, Construction sector, Agriculture, Surveying and				
(10	Mapping, Industrial Applications, Inspection and Monitoring, Traffic monitoring and					
management, Search and rescue, Smart Cities.						
Text	books:					
1	Garg	PK, "Unmanned aerial vehicles: An introduction", Mercury Learning and Information;				
1.	2021					
r	Garvi	t Pandya, "Basics of Unmanned Aerial Vehicles Time to start working on UAV				
۷.	² . Technology", 2021.					
Refe	rence	Books:				
1	Avtar	R, Watanabe T, editors. Unmanned aerial vehicle: Applications in agriculture and				
1.	enviro	onment, Springer International Publishing: 2020.				



Cour	Course CodeCategoryLTPCI.ME.MExample										
B20	EE4113	SOC	1		2	2		50	3 Hrs.		
		I		I							
ELECTRIC VEHICLES LABORATORY											
(Skill Oriented Course)											
(For EEE)											
Cour	Course Objectives: Students will learn to conduct experiment on										
1.	1. The basic components of EV.										
2.	Different	types of cor	overters a	nd PWM	l techniqu	es used fo	or EV.				
3.	Modes of	Charging ir	n EVs.								
4.	Battery p	erformance	in EVs.								
5.	Speed co	ntrol of Indu	ction mo	otor / BLI	DC motor.						
Cour	se Outcon	nes: Student	s will be	able to e	xperimen	tally					
S No				Ou	tcome				Knowledge		
5.110				Ou	teome				Level		
1	Analyze	the performation	ance of el	lectric ve	hicles (EV	/s).			K4		
2	Explore	the working	ofdiffere	nt types of	of convert	ers used f	for EVs.		K4		
3	Analyze	the performa	ance of E	V chargi	ng system				K4		
4	Evaluate	the battery p	berfor <mark>ma</mark>	nce of EV	/s				K4		
5	Explore	the s <mark>peed</mark> co	ntrol of I	nduction	motor / B	LDC mo	tor.		K4		
			1								
	- 227			S	YLLABU	JS					
	52	34	E	Simulat	tion Expe	riments	<u>100 c</u>	<u>.LEGE</u>			
1	Simulatio	on of Buck, I	Boost and	l Buck-B	oost conv	erter	1005				
2	Simulatio	on of bidirec	tional po	wer conv	erter for H	EV vehicle	e.				
3	Simulatio	on of gating	pulses for	r inverter	using sin	e PWM p	ulse gener	ation (PD, PO	DD, APOD).		
4	Simulatio	on of EV cha	rging usi	ng PWM	l voltage s	source inv	erter.				
5	Simulatio	on of EV bat	tery syste	em (SOC	/SOH).						
6	Simulatio	on of Open le	oop speed	d control	of inducti	on motor	/ BLDC n	notor.			
				Hardw	are Expe	riments					
7	E-Vehicle	e two-wheel	er compl	ete set up).						
8	Bidirectio	onal power c	onverter	for elect	ric vehicle	es.					
9	PWM pu	lse generatio	on using A	ARM Co	rtex-M4 N	licrocont	roller.				
10	Study of	EV Charger	using PV	VM Volta	age Sourc	e Convert	ter.				
11	Estimatio	on of SOC/S	OH of EV	V Battery	Systems.						
12	Open loo	p speed cont	trol of the	ee phase	induction	motor (V	//f control)).			
13	Performa	nce verificat	tion of sp	eed contr	rol of BLI	OC motor	/ EV char	ging station.			
Refer	rence Boo	ks:									
1	John G. H	Hayes and A	. Goodar	zi, "Elect	ric Power	train - En	ergy Syste	ms, Power el	ectronics and		
1	drives for	Hybrid, ele	ctric and	fuel cell	vehicles"	Wiley Pu	ublication,	1 st edition, 20	18.		
2	Y. Gao, S	S. Gay and A	A. Emadi	, Modern	Electric,	Hybrid E	lectric, and	l Fuel Cell V	ehicles, CRC		
-	Press, 2005.										



SAGI RAMA KRISHNAM RAJU ENGINEERING COLLEGE (AUTONOMOUS)

(Affiliated to JNTUK, Kakinada), (Recognized by AICTE, New Delhi) UG Programmes CE, CSE, ECE, EEE, IT & ME are Accredited by NBA, Accredited by NAAC with A⁺ CHINNA AMIRAM (P.O):: BHIMAVARAM :: W.G.Dt., A.P., INDIA :: PIN: 534 204

Estd:1980

Regula	ntion: R20	IV / IV - B.Tech. II - Semester								
	ELECTRICAL AND E	LECTR	ONI	CS EN	GIN	EERI	NG			
SCHEME OF INSTRUCTION & EXAMINATION (With effect from 2020-21 admitted Batch onwards)										
Course Code	Course Name	Catego ry	Cr	L	Т	Р	Int. Marks	Ext. Marks	Total Marks	
B20EE4201	Project Work (Project work, seminar and internship in industry)	PR	8	0	0	16	60	140	200	
18	T	OTAL	8	0	0	16	60	140	200	





Course Code		Category	L	Т	Р	С	I.M	E.M	Exam		
B 2	0EE4201	PR			16	8	60	140	3 Hrs.		
							·				
				PROJ	ECT WO)RK					
				(F	For EEE)						
Cou	rse Objecti	ves:									
1	1 To Survey and study the published literature on the selected problem and identify the objective.										
2	Identify the necessary tools and components in order to initiate the project.										
3	3 The Design, build and test a system with hardware /software.										
4	4 To Develop a prototype/model of the project work.										
5	To Prepare	e documentatio	on in stand	ard form	at and co	mmunicat	e technica	l concepts			
Cou	rse Outcon	nes: At the end	l of the cou	urse the s	tudents v	vill be abl	e to				
S.No. Outcome						Knowledge					
1. J. I. N.	0.			Outo	come				8		
0.110	0.			Outo	come				Level		
1	o. Identif	y the objectiv	res from t	the gaps	through	literature	e survey a	and propose	Level K4		
1	o. Identif solution	y the objectivn for solving of	ves from t bjective	he gaps	come through	literature	e survey a	and propose	Level K4		
1	o. Identif solution Identif laborate	y the objectiv a for solving ol y the required ory level.	res from t bjective tools & c	he gaps	through	literature	e survey a project/pr	and propose ocess at the	Level K4 K4		
1 2 3	 Identify solution Identify laborate Design softwar 	y the objectivn of for solving of y the required ory level. solutions to e/hardware app	res from t bjective tools & c o comple proach.	the gaps compone ex elect	through nts to in rical en	literature itiate the gineering	e survey a project/pr problen	and propose ocess at the ns utilizing	Level K4 K4 K5		
1 2 3 4	 Identify solution Identify laborate Design softwar Develog ethical 	y the objective of for solving of y the required ory level. solutions to e/hardware app p the project we values & social	res from t bjective tools & o o comple proach. within the l responsil	the gaps compone ex elect available oility.	through ents to in rical en e resourc	literature itiate the gineering ees, in sti	e survey a project/pr problen pulated tin	and propose ocess at the ns utilizing ne and with	Level K4 K4 K5 K5		
1 2 3 4	 Identify solution Identify laborate Design softwar Develog ethical Write to the solution 	y the objective of for solving of y the required ory level. solutions to e/hardware app p the project ve values & social the documentation	tools & o tools & o o comple proach. within the l responsibilition in star	the gaps compone ex elect available oility.	through nts to in rical en e resourc	literature itiate the gineering ees, in sti	e survey a project/pr problen pulated tin cate propo	and propose ocess at the ns utilizing ne and with sed solution	Level K4 K4 K5 K5		
1 2 3 4 5	 Identify solution Identify laborate Design softwar Develog ethical Write to orally 	y the objective of for solving of y the required ory level. solutions to e/hardware app p the project of values & social the documentation in a profession	es from t bjective tools & o o comple proach. within the l responsit tion in star	he gaps compone ex elect available oility. ndard for ner enha	through nts to in rical en e resource mat and ncing se	literature itiate the gineering ees, in sti- communi lf-study	e survey a project/pr problem pulated tin cate propo	and propose ocess at the ns utilizing ne and with esed solution ng learning	Level K4 K4 K5 K5 K6		
1 2 3 4 5	 Identify solution Identify laborate Design softwar Develog ethical Write to orally abilities 	y the objective of for solving of y the required ory level. solutions to e/hardware app p the project of values & socia the documentation in a profession s.	res from t bjective tools & o o comple proach. within the l responsit tion in star onal manr	the gaps compone ex elect available oility. ndard for her enha	through nts to in rical en e resource rmat and ncing se	literature itiate the gineering ces, in sti communi lf-study	e survey a project/pr problen pulated tin cate propo and lifelo	and propose ocess at the ns utilizing ne and with esed solution ng learning	Level K4 K4 K5 K5 K6		

*The object of Project Work is to enable the student to take up investigative study in the broad field of Electrical and Electronics Engineering, either fully theoretical/practical or involving both theoretical and practical work to be assigned by the Department on an individual basis or a group of students, under the guidance of a supervisor. This is expected to provide a good initiation for the student(s) in R&D work.

The assignment to normally include:

a) Survey and study of published literature on the assigned topic.

b) Working out a preliminary approach to the problem relating to the assigned topic.

c) Conducting preliminary Analysis/Modeling/Simulation/Experiment/Design/ Feasibility.

d) Preparing a written report on the study conducted for presentation to the department.

e) Final Seminar, as oral Presentation before a departmental committee.